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## Shinagawa

### [54] METHOD OF REMOVING RESIST MASK AND A METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

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- [73] Assignee: Fujitsu Limited, Kanagawa, Japan
- [21] Appl. No.: 264,915
- [22] Filed: Jun. 24, 1994
- [30] **Foreign Application Priority Data**
- [JP] Japan ..... 5-232018 Sep. 17, 1993
- [51] Int. Cl.<sup>6</sup> ..... H01L 21/312
- U.S. Cl. ..... 438/514; 438/704; 438/725; [52] 438/714
- [58] Field of Search ..... 156/643, 646, 156/651, 659.1; 437/229, 20, 22, 931; 148/DIG. 83

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Primary Examiner-John Niebling

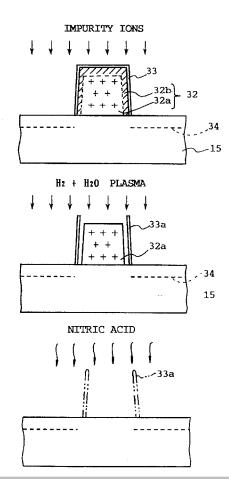
[45]

Assistant Examiner-Thomas G. Bilodeau Attorney, Agent, or Firm-Nikaido Marmelstein Murray & Oram LLP

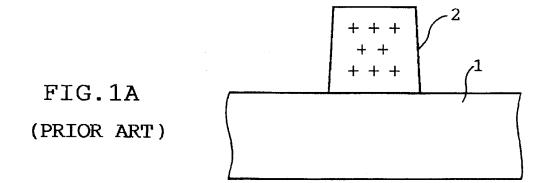
#### [57] ABSTRACT

The present invention relates to a method of manufacturing a semiconductor device including a process of removing a photoresist mask or a photosensitive polyimide mask remaining after implanting impurity ions into a semiconductor layer or the like, and has an object to prevent generation of oxides of impurities and photoresist explosion and arranging it so that no residue remains. The present invention comprises the steps of forming a mask composed of photosensitive organic matter on a layer, implanting impurity ions into the layer through the mask, and removing the mask through processing including three steps of: exposing the mask to a plasma activated gas containing hydrogen, exposing to the mask to a plasma activated gas containing oxygen, and exposing the mask to a solution containing nitric acid under conditions sufficient to dissolve alumina which had formed on the mask during exposure of the mask to oxygen.

### 18 Claims, 8 Drawing Sheets



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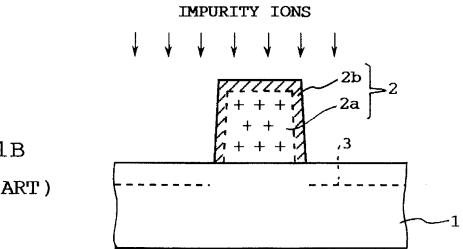


FIG.1B (PRIOR ART)

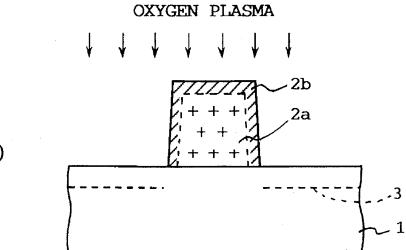


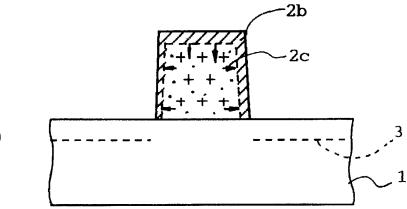
FIG.1C

(PRIOR ART)

R

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## FIG.1D

(PRIOR ART)

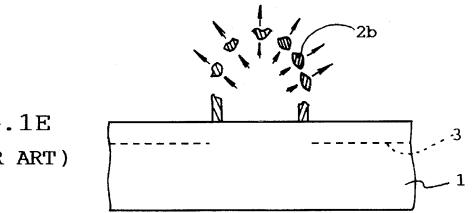


FIG.1E (PRIOR ART)

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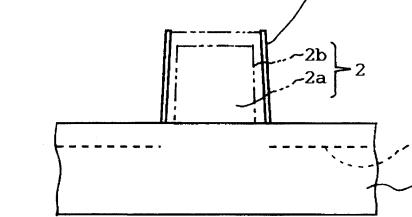
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FIG.2

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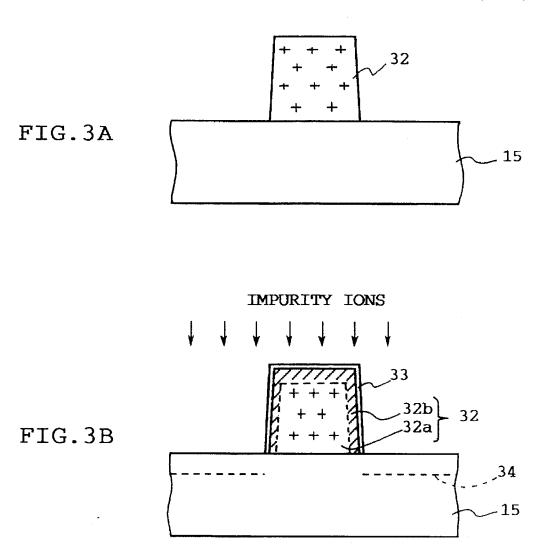
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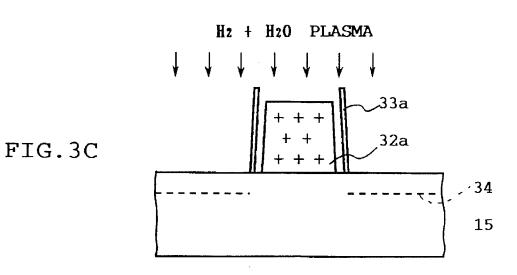
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