

# United States Patent [19]

### Bach et al.

**Patent Number:** 

5,619,650

**Date of Patent:** [45]

Apr. 8, 1997

[54]	NETWORK PROCESSOR FOR
	TRANSFORMING A MESSAGE
	TRANSPORTED FROM AN I/O CHANNEL
	TO A NETWORK BY ADDING A MESSAGE
	IDENTIFIER AND THEN CONVERTING THE
	MESSAGE

[75] Inventors: Maurice J. Bach, Haifa, Israel; Robert B. Hoppes, Hyde Park, N.Y.; Clifford B. Meltzer, Ossining, N.Y.; Kenneth J. Parchinski, Wappingers Falls, N.Y.; Gary J. Whelan, Rhinebeck, N.Y.

[73] Assignee: International Business Machines Corporation, Armonk, N.Y.

[21] Appl. No.: 531,579

[22] Filed: Sep. 21, 1995

### Related U.S. Application Data

[63]	Continuation	of	Ser.	No.	966,821,	Dec.	31,	1992,	aban
	doned.								

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[	52]	U.S.	Cl.	395/2	00.01;	395/2	00.14	ŀ

395/285; 370/466; 370/469 [58] Field of Search ... 395/200.01, 200.14, 285, 800; 370/85.1, 85.13, 60

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Primary Examiner—Thomas C. Lee Assistant Examiner-Duo Chen

Attorney, Agent, or Firm-Gerald R. Woods

### ABSTRACT [57]

A system and method for distributing application-to-application network communications protocol processing. Host computers implement distributed API processing across a high speed I/O channel increasing throughput. The application API conforms to standard protocols but protocol processing is distributed using a cross-channel distributed sockets API at the session layer. This API allows multiplexing of data from one or more hosts to one or more front end routers managing network communications. Multiplexing increases network performance through parallel processing and advantageously employs host high speed I/O functions. Front end routers perform lower level protocol tasks necessary to exchange data over the communications network.

### 7 Claims, 16 Drawing Sheets

### System 370/390 Data Application 1002 DSI Heade **Build Frame** command Header 1008 DSL DSI Header Channel Program Data Channel 1010 Front End Router DATAMSG 1004 CID DSI Heade Data DATAMSG Build API DSM Issue write() DSI Header command Header TCP/IF



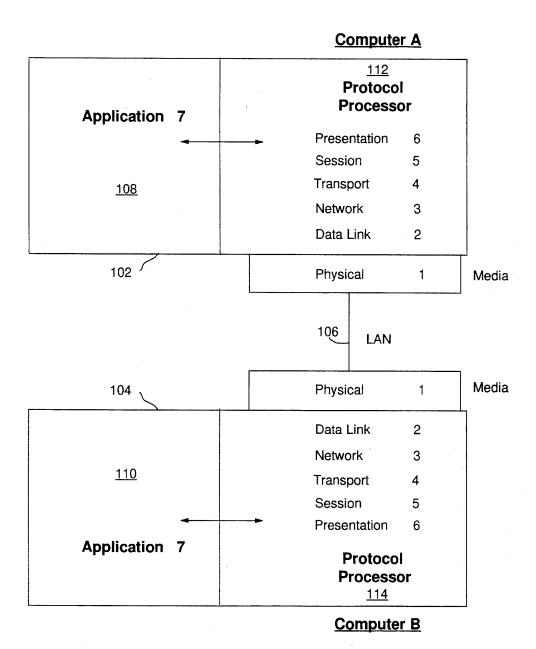


FIG. 1

## **System 370/390**

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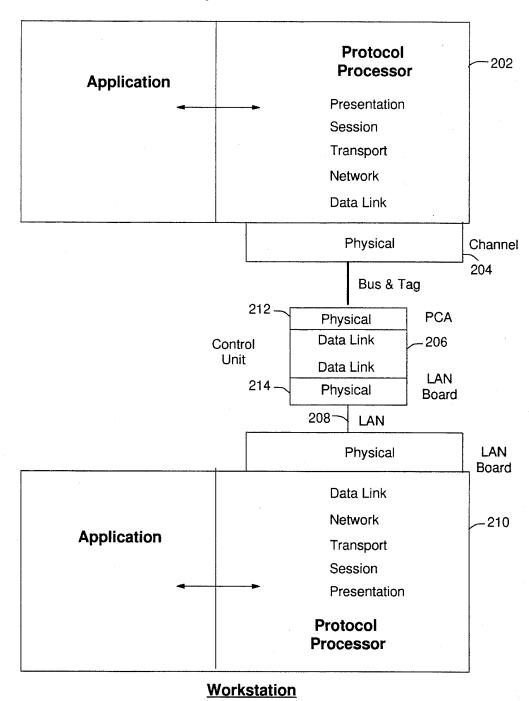
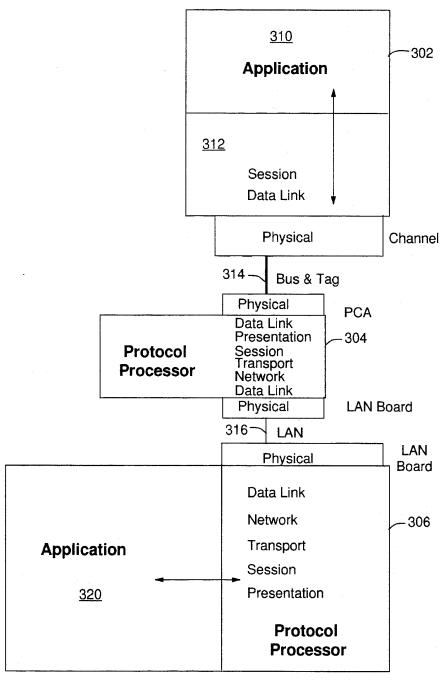


FIG. 2 Prior Art

# **System 370/390**



Computer B

FIG. 3

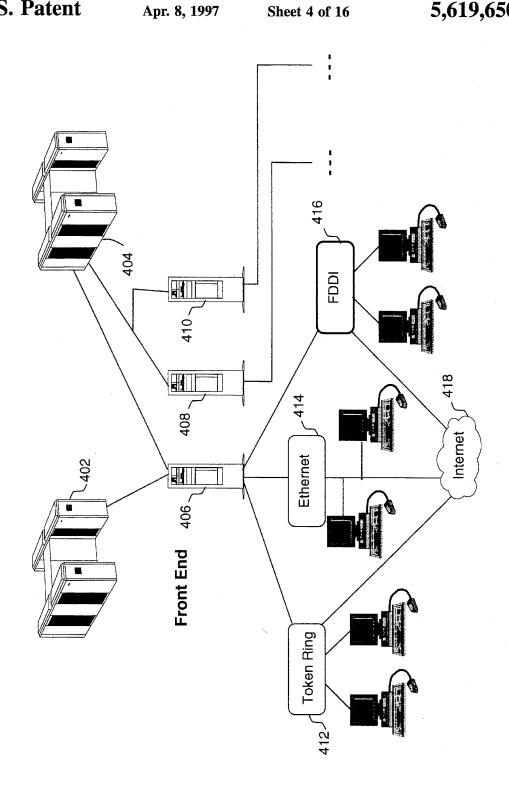


FIG. 4

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