

Alacritech Inc.'s Patent Initial Disclosures For Cavium
Exhibit 2

'072 Patent Claim ² (P.R. 3-1(a))	Accused Instrumentalities And Where Each Claim Element Is Found ³ (P.R. 3-1(b)-(c))
<p>comprising transferring status information for the context to the interface device during the same operation as transferring protocol header information to the interface device.</p>	<p>Cavium has committed and continues to commit acts of infringement of claim 2 under 35 U.S.C. § 271 in connection with the Accused Instrumentalities.</p> <p>Cavium's Accused Instrumentalities, as used and tested in connection with a server or computer, perform the network controller operations of [072.1]-[072.1f], <i>supra</i>. In addition, the Cavium Accused Instrumentalities, as used and tested in connection with a server or computer, are capable of transferring status information for the context to the interface device during the same operation as transferring protocol header information to the interface device.</p> <p>On information and belief, in connection with infringing LSO functionality, the network controller of each of the Accused Instrumentalities transfers status information for the context from the host computer to local memory during the operation of transferring the protocol header information. For example, on information and belief, the network controller of each of the Accused Instrumentalities transfers status information for the context to local memory during the operation of transferring the protocol header (also called a prototype header) containing both status information for the context (e.g., Acknowledgment information) and protocol header information.</p> <p><i>See, e.g.</i>, 8262-k Datasheet, BATES ALA00007608-7611, at p. 3, describing support for large and giant send operations, which constitutes infringing LSO functionality.¹³</p>

¹³ See also *supra* note 7.