UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

INTEL CORP., CAVIUM, INC.,
WISTRON CORPORATION, and DELL INC.
Petitioner,

v.

ALACRITECH, INC.,
Patent Owner.

Case IPR2017-01406[1]
U.S. Patent No. 7,673,072
Title: FAST-PATH APPARATUS FOR TRANSMITTING DATA CORRESPONDING A TCP CONNECTION

SUR-REPLY IN OPPOSITION TO PATENT OWNER'S MOTION TO AMEND FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 7,673,072

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[1] Cavium, Inc., which filed a Petition in IPR2017-01707, Wistron Corporation, which filed a Petition in IPR2018-00329, and Dell Inc., which filed a Petition in IPR2018-00375, have been joined as petitioners in this proceeding.

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Ex.1003	Declaration of Robert Horst
Ex.1004	*Curriculum Vitae* of Robert Horst
Ex.1005	U.S. Patent No. 5,768,618 ("Erickson")
Ex.1006	Tanenbaum, Andrew S., *Computer Networks*, Prentice-Hall, Inc., New Jersey (1996) ("Tanenbaum96")
Ex.1007	Transmission Control Protocol, "Darpa Internet Protocol Specification," RFC: 793, Sept. 1981 ("RFC 793")
Ex.1008	Stevens, W. Richard, *TCP/IP Illustrated Volume 1: The Protocols*, Addison-Wesley (1994) ("Stevens1")
Ex.1009	Lilinkamp, J., Mandell. R. and Padlipsky, M., "Proposed Host-Front End Protocol," Network Working Group Request for Comments: 929, Dec. 1984 ("RFC 929")
Ex.1010	Alacritech's Preliminary Claim Construction and Extrinsic Evidence Disclosures, January 31, 2017
Ex.1011	Declaration of Rice Mayors regarding Tanenbaum, Andrew S., *Computer Network*
Ex.1012	U.S. Patent No. 4,831,523
Ex.1013	Stevens, W. Richard and Wright, Gary R., *TCP/IP Illustrated Volume 2: The Implementation*, Addison-Wesley (1995) ("Stevens2")
Ex.1014	Touch, J., "TCP Control Block Interdependence," Network Working Group Request for Comments: 2140, April 1997 ("RFC 2140")

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Ex.1015	Thia, Y.H., Woodside, C.M., "A Reduced Operation Protocol Engine (ROPE) for a Multiple-Layer Bypass Architecture," Protocols for High Speed Networks (Dordrecht), 1995 ("Thia")
Ex.1016	Biersack, E. W., Rütsche E., "Demultiplexing on the ATM Adapter: Experiments with Internet Protocols in User Space," Journal on High Speed Networks, Vol. 5, No. 2, May 1996 ("Biersack")
Ex.1017	Rütsche, E., Kaiserswerth, M., "TCP/IP on the Parallel Protocol Engine," Proceedings, IFIP Conference on High Performance Networking, Liege (Belgium), Dec. 1992 ("Rütsche92")
Ex.1018	Rütsche, E., "The Architecture of a Gb/s Multimedia Protocol Adapter," Computer Communication Review, 1993 ("Rütsche93")
Ex.1019	Padlipsky, M. A., "A Proposed Protocol for Connecting Host Computers to Arpa-Like Networks Via Directly-Connected Front End Processors,",Network Working Group RFC #647, Nov. 1974 ("RFC 647")
Ex.1020	U.S. Patent No. 5,619,650 ("Bach")
Ex.1021	U.S. Patent No. 5,915,124 ("Morris")
Ex.1022	Cooper, E.C., et al., "Protocol Implementation on the Nectar Communication Processor," School of Computer Science, Carnegie Mellon University, Sept. 1990 ("Cooper")
Ex.1023	Kung, H.T., et al., "A Host Interface Architecture for High-Speed Networks," School of Computer Science, Carnegie Mellon University and Network Systems Corporation ("Kung")
Ex.1024	Exhibit D to Declaration of Dr. Gregory L. Chesson in Support of Microsoft's Opposition to Alacritech's Motion for Preliminary Injunction: "Protocol Engine Handbook," Protocol Engines Incorporated, Oct. 1990 ("Chesson")

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