

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

INTEL CORP. CAVIUM, INC., WISTRON CORP., and DELL, INC.
Petitioners,

v.

ALACRITECH, INC.,
Patent Owner.

Case IPR No. 2017-01392
U.S. Patent No. 7,337,241¹

PETITIONER'S UPDATED EXHIBIT LIST

¹ Cavium, Inc., which filed a Petition in Case IPR2017-01728, Wistron Corporation, which filed a Petition in Case IPR2018-00328, and Dell Inc., which filed a Petition in Case IPR2018-00372, have been joined as a petitioners in this proceeding.

Exhibit #	Description
Ex. 1001	U.S. Patent No. 7,337,241 (“241 Patent”)
Ex. 1002	Excerpts from Prosecution File History of U.S. Patent No. 7,337,241 (“241 File History”)
Ex. 1003	Declaration of Robert Horst
Ex. 1004	<i>Curriculum Vitae</i> of Robert Horst
Ex. 1005	U.S. Patent No. 5,768,618 (“Erickson”)
Ex. 1006	Tanenbaum, Andrew S., <i>Computer Networks</i> , Prentice-Hall, Inc., New Jersey (1996). (“Tanenbaum96”)
Ex. 1007	Transmission Control Protocol, “Darpa Internet Protocol Specification”, RFC: 793, Sept. 1981. (“RFC 793”)
Ex. 1008	Stevens, W. Richard, <i>TCP/IP Illustrated Volume 1: The Protocols</i> , Addison-Wesley (1994). (“Stevens1”)
Ex. 1009	Lilinkamp, J., Mandell. R. and Padlipsky, M., “Proposed Host-Front End Protocol”, Network Working Group Request for Comments: 929, Dec. 1984. (“RFC 929”)
Ex. 1010	<i>Number Not Used</i>
Ex. 1011	Librarian Declaration of Rice Mayors regarding Andrew S. Tanenbaum, <i>Computer Networks</i> (3rd ed. 1996) (Ex.1006,“Tanenbaum96”)
Ex. 1012	<i>Number Not Used</i>
Ex. 1013	Stevens, W. Richard and Wright, Gary R., <i>TCP/IP Illustrated Volume 2: The Implementation</i> , Addison-Wesley (1995). (“Stevens2”)
Ex. 1014	<i>Number Not Used</i>
Ex. 1015	Thia, Y.H., Woodside, C.M., “A Reduced Operation Protocol Engine (ROPE) for a Multiple-Layer Bypass Architecture”, <i>Protocols for High Speed Networks</i> (Dordrecht), 1995. (“Thia and

Exhibit #	Description
	Woodside”)
Ex. 1016	Biersack, E. W., Rüttsche E., “Demultiplexing on the ATM Adapter: Experiments with Internet Protocols in User Space”, Journal on High Speed Networks, Vol. 5, No. 2, May 1996. (“Biersack”)
Ex. 1017	Rüttsche, E., Kaiserswerth, M., “TCP/IP on the Parallel Protocol Engine”, Proceedings, IFIP Conference on High Performance Networking, Liege (Belgium), Dec. 1992. (“Rüttsche92”)
Ex. 1018	Rüttsche, E., “The Architecture of a Gb/s Multimedia Protocol Adapter”, Computer Communication Review, 1993. (“Rüttsche93”)
Ex. 1019	Padlipsky, M. A., “A Proposed Protocol for Connecting Host Computers to Arpa-Like Networks Via Directly-Connected Front End Processors”, Network Working Group RFC #647, Nov. 1974. (“RFC 647”)
Ex. 1020	U.S. Patent No. 5,619,650 (“Bach”)
Ex. 1021	U.S. Patent No. 5,915,124 (“Morris”)
Ex. 1022	Cooper, E.C., et al., “Protocol Implementation on the Nectar Communication Processor”, School of Computer Science, Carnegie Mellon University, Sept. 1990. (“Cooper”)
Ex. 1023	Kung, H.T., et al., “A Host Interface Architecture for High-Speed Networks”, School of Computer Science, Carnegie Mellon University and Network Systems Corporation. (“Kung”)
Ex. 1024	Exhibit D to Declaration of Dr. Gregory L. Chesson in Support of Microsoft’s Opposition to Alacritech’s Motion for Preliminary Injunction: “Protocol Engine Handbook”, Protocol Engines Incorporated, Oct. 1990. (“Chesson”)

Exhibit #	Description
Ex. 1025	Kanakia, H., Cheriton, D.R., “The VMP Network Adapter Board (NAB): High-Performance Network Communication for Multiprocessors”, Communications Architectures & Protocols, Stanford University, Aug. 1988. (“Kanakia”)
Ex. 1026	Kung, H.T., Cooper, E.C., et al., “Network-Based Multicomputers: An Emerging Parallel Architectures”, School of Computer Science, Carnegie Mellon University. (“Kung and Cooper”)
Ex. 1027	Dalton, C., et al., “Afterburner: Architectural Support for High-Performance Protocols”, Networks & Communications Laboratories, HP Laboratories Bristol, July 1993. (“Dalton”)
Ex. 1028	Murphy, E., Hayes, S., Enders, M., <i>TCP/IP Tutorial and Technical Overview Fifth Edition</i> , Prentice-Hall, Inc. New Jersey, (1995). (“Murphy”)
Ex. 1029	MacLean, A.R., Barvick, S. E., “An Outboard Processor for High Performance Implementation of Transport Layer Protocols”, IEEE Globecom '91, Phoenix, AZ, Dec. 1991. (“MacLean”)
Ex. 1030	Clark, D.D., et al., “An Analysis of TCP Processing Overhead”, IEEE Communications Magazine, June 1989. (“Clark”)
Ex. 1031	U.S. Provisional Application 60/061,809 (“Alacritech 1997 Provisional Application”)
Ex. 1032	Culler, E.C., et al., “Parallel Computing on the Berkeley NOW”, Computer Science Division, University of California, Berkeley. (“Culler”)
Ex. 1033	“Gigabit Ethernet Technical Brief: Achieving End-to-End Performance”, Alteon Networks, Inc. First Edition, Sept. 1996. (“Alteon”)
Ex. 1034	Smith, J.A., Primmer, M., “Tachyon: A Gigabit Fibre Channel Protocol Chip”, Hewlett-Packard Journal, Article 12, Oct. 1996. (“Smith”)

Exhibit #	Description
Ex. 1035	Patterson, D.A., Hennessy, J.L., <i>Computer Architecture: A Quantitative Approach</i> , Morgan Kaufmann Publishers, Inc., San Mateo, CA (1990). (“Patterson”)
Ex. 1036	Internet Protocol, “Darpa Internet Protocol Specification”, RFC: 791, Sept. 1981. (“RFC 791”)
Ex. 1037	<i>Number Not Used</i>
Ex. 1038	Woodside, C. M., Ravindran, K. and Franks, R. G.. “The protocol bypass concept for high speed OSI data transfer.” IFIP Workshop on Protocols for High Speed Networks. 1990. (“Woodside”)
Ex. 1039	Joint Claim Construction and Pre-Hearing Statement Pursuant to Rule 4-3 (<i>Alacritech, Inc. v. Dell Inc, Intel Corporation, et al.</i>) (“JCCS”)
Exs. 1040-1050	<i>Numbers Not Used</i>
Ex. 1051	U.S. Patent No. 4,027,293
Ex. 1052	U.S. Patent No. 5,329,630
Exs. 1053-1061	<i>Numbers Not Used</i>
Ex. 1062	Rebuttal Declaration of Paul S. Min in Support of Plaintiff’s Claim Construction Brief (“Min Rebuttal Declaration”)
Exs. 1063-1076	<i>Numbers Not Used</i>
Ex. 1077	Deposition of Paul S. Min on March 21, 2017 (“Min Depo”)

Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts



Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research



With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips



Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.