UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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BITDEFENDER, INC.

Petitioner,

v.

UNILOC USA, INC.
Patent Owner

_____

Case IPR2017-01315
U.S. Patent 6,510,466

_____

DECLARATION OF DR. VAL DIEULIIS

AUGUST 2, 2017

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I, Dr. Val DiEuliis, hereby declare and state as follows:

1. Introduction

1. My name is Val DiEuliis, and I have been retained by Uniloc, USA, Inc., and Uniloc Luxembourg S.A. ("Uniloc" or the "Patent Owner"). My client Uniloc and its associated counsel, Etheridge Law Group, have asked me to study U.S. Patent No. 6,510,466 ("the '466 patent"), the Petition, and the proffered prior art in this case, in addition to other relevant documents. I document my findings in this declaration.

2. I have concluded that U.S. Patent No. 5,832,505 (Kasso; EX1009) combined with U.S. Patent No. 6,338,138 (Raduchel; EX1010) does not render obvious any challenged claim of the patent at issue, the'466 patent, for at least the reasons that the Petitioner fails to make a prima facie case of obviousness for several claim limitations in the independent claims. In addition, in my opinion, a POSITA would not have been motivated to apply Kasso's teachings in a way that would lead to the '466 patent or to combine Kasso with Raduchel.

3. The limited scope of my opinions and analysis in this declaration do not imply that I may not later express other opinions or

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report other results from other investigations concerning other issues raised by the Petitioner or their experts in this IPR.

2. Qualifications

4. I am an electrical engineer with over 45 years of experience developing, programming, and analyzing computer algorithms and software. I am experienced with and able to create, read, and interpret firmware and software in C, C++, Java, assembly language, HTML, and other computer programming languages. I have served as an expert witness in multiple cases for which I analyzed computer source code in various languages and testified at ITC hearings and two jury trials concerning my results.

5. During my career, I have developed and managed projects for various applications, including sensors, controls, communications, user interfaces, device firmware, handheld devices, medical devices and systems, and test systems for optical and magnetic disk systems.

6. I have designed, developed, and implemented hardware and software for digital communication networks, including factory networks and document capture and distribution networks; and communications links for various applications. See DiEuliis CV (See e.g., Website

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