

Vivek Subramanian

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Education

3 / 96 - 6 / 98

Ph.D. in Electrical Engineering, Stanford University *Stanford, California*

Honors earned: Graduate Fellowship, Eastman Kodak Company

9 / 94 - 3 / 96

MS in Electrical Engineering, Stanford University *Stanford, California*

8 / 90 - 5 / 94

BS in Electrical Engineering, Louisiana State University *Baton Rouge, Louisiana*

Honors earned: Summa cum laude, College Honors, Outstanding Senior, Junior and Sophomore, Honors College

Professional Experience

2000 - present

Department of Electrical Engineering & Computer Sciences, University of California *Berkeley, CA*

7/11 – present: Professor

7/05 – 7/11: Associate Professor

7/00-7/05: Assistant Professor

Research, teaching and service in EECS Department

Honors Earned:

2015 IEEE Kiyo Tomiyasu Award

Best in session and best in track awards, 2013 IMAPS Microelectronics Conference

Outstanding paper award, 2012 IMAPS Microelectronics Conference

2008 Printed Electronics Champion, Printed Electronics USA Conference, November 2008

Outstanding Teaching Award, EECS Department, UC Berkeley, 2005

Best paper award, 2004 IEEE Device Research Conference

Nominated to MIT's Technology Review top 100 young innovators list (TR100), 2002

National Science Foundation Young Investigator (CAREER) Award, FY2002,

Nominated to National Academy of Engineering's *Frontiers of Engineering*, 2002

Winner of 2002 Paul Rappaport Award for best paper in an IEEE EDS Journal

2009 - present

Adjunct Professor, Sunchon National University

Sunchon, Korea

2013 to date: Adjunct Professor, BK21 Program

2009-2013: Adjunct Professor and Principal Investigator, WCU Program

Initiated, managed, and contributed to large multinational research program focused on printed electronics, including displays, energy devices, and RFID

2000 - present

Independent Consultant

Orinda, CA

Consultant to the semiconductor industry and its associated fields in the following areas:

Memory technology and design, Silicon Process Technology

Display / Imager and Flexible Electronics Technology, RFID Technology

Intellectual Property Consulting

Technology Evaluation / Venture Capital due diligence

2014- present

Founder and CTO, Dragonfly Technology Inc.

San Bruno, CA

Senior leadership role in venture-funded startup company working on wireless networking

- 2004 – 2013 **Founding Scientific Advisor, Kovio, Inc.**
Sunnyvale, CA
Scientific advising to printed electronics startup company
- 2008 - 2011 **Chief Technical Advisor, QuSwami, Inc.**
San Francisco, CA
Scientific advising to energy conversion device startup company
Served as CTO from July 2010-June 2011
- 1998 - 2000 **Consulting Assistant Professor, Electrical Engineering Department, Stanford University.**
Stanford, CA
Advisory role for research group of Prof. Krishna C. Saraswat
- 1998-2000 **Visiting Research Engineer, Electrical Engineering Department, University of California.**
Berkeley, CA
Research into 25nm MOSFET technologies for giga-scale integration
- 1998 - 2000 **Founder, Matrix Semiconductor, Inc.**
Santa Clara, CA
Co-founder and technical advisor of startup company working on high-density memory technology
- Honors Earned:**
Nominated to Scientific American's SA50 List for Visionary Technology
Finalist for 2003 World Technology Award for Information Technology Hardware
Winner, 2005 EDN Innovation Award
- 1998 **Co-instructor, Electrical Engineering Department, Stanford University.**
Stanford, CA
Co-teaching of EE311, Advanced Integrated Circuit Fabrication Processes
- 1997 **Intern, Advanced Product Research and Development Laboratory, Motorola Inc.**
Austin, TX
Research into process development issues affecting SiGe SEMFET devices
- 1996 **Head Teaching Assistant EE410: IC Fabrication Laboratory, Stanford University**
Stanford, CA
Coordination and instruction of EE410, graduate level laboratory course.
- 1994-1998 **Research Assistant, Electrical Engineering, Stanford University**
Stanford, CA
Research into crystallization of amorphous Si and SiGe films using low thermal budget processes.

Professional Affiliations and Activities

Technical Program Committee, IEEE Electronic Components and Technology Conference, 2014 to date
Chair, Scientific Advisory Board, iPACK, Royal Institute of Technology (KTH), Sweden, 2011 to date
Tampere Institute of Technology Faculty Search Committee, Finland, 2013
University of Oulu Faculty Search Committee, Finland, 2013
Served as external thesis committee member for several universities world-wide, including University of Cape Town (South Africa), Tampere Institute of Technology (Finland), Technical University of Eindhoven (Netherlands), Indian Institute of Science (India), Indian Institute of Technology (India), 2009 to date
Associate Editor, IEEE Journal of Display Technology, 2008 to date
Technical Program Chair, Large Area, Organic, and Printed Electronics Conference, 2012-2013
Scientific Committee, Large Area, Organic, and Printed Electronics Conference, 2009-2011
Scientific Committee, International Conference on Printed and Flexible Electronics, 2009-2013
IEEE Electron Devices Society Organic Electronics Committee, 2003 - 2005
Executive Committee, International Electron Device Meeting, 2003 to 2009
Technical Program Committee, International Electron Device Meeting, 2001-2002
Technical Program Committee member, Device Research Conference, 2000-2002
Technical Program Committee member, VLSI-TSA Conference, 2005
Member, Institute of Electrical and Electronic Engineers