

CERTIFICATE OF TRANSLATION

I Roger P. Lewis, whose address is 42 Bird Street North, Martinsburg WV 25401, declare and state the following:

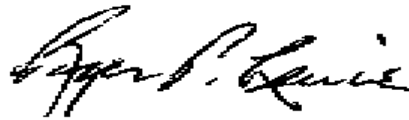
I am well acquainted with the English and Japanese languages and have in the past translated numerous English/Japanese documents of legal and/or technical content.

I hereby certify that the Japanese translation of the attached document identified as:

JP7-330112 and related Office Actions

is true, and that all statements of information and belief are believed to be true, and that these and similar statements are punishable by fines or imprisonment, or both, under Section 1001 of Title 18 of the United States Code.

SINCERELY,



ROGER P. LEWIS

Date: June 10, 2016

[Document Name] Patent Application
[Docket Number] 2020270244
[Submission Date] December 19, 1995
[Submitted to] Commissioner of Japan Patent Office
[International Patent Classification] H01L 21/76
[Title of the Invention] SEMICONDUCTOR DEVICE AND MANUFACTURING
METHOD THEREOF
[Number of claims] 12
[Inventor]
 [Address or Domicile] Matsushita Electric Industrial Co., Ltd.
 1006, Kadoma, Kadoma-shi, Osaka-fu
 [Name] Toshiki Yabu
[Inventor]
 [Address or Domicile] Matsushita Electric Industrial Co., Ltd.
 1006, Kadoma, Kadoma-shi, Osaka-fu
 [Name] Takashi Uehara
[Inventor]
 [Address or Domicile] Matsushita Electric Industrial Co., Ltd.
 1006, Kadoma, Kadoma-shi, Osaka-fu
 [Name] Mizuki Segawa
[Inventor]
 [Address or Domicile] Matsushita Electric Industrial Co., Ltd.
 1006, Kadoma, Kadoma-shi, Osaka-fu
 [Name] Takashi Nakabayashi
[Inventor]
 [Address or Domicile] Matsushita Electric Industrial Co., Ltd.
 1006, Kadoma, Kadoma-shi, Osaka-fu
 [Name] Kyoji Yamashita

[Inventor]

[Address or Domicile] Matsushita Electric Industrial Co., Ltd.
1006, Kadoma, Kadoma-shi, Osaka-fu

[Name] Takaaki Ukeda

[Inventor]

[Address or Domicile] Matsushita Electric Industrial Co., Ltd.
1006, Kadoma, Kadoma-shi, Osaka-fu

[Name] Masatoshi Arai

[Inventor]

[Address or Domicile] Matsushita Electric Industrial Co., Ltd.
1006, Kadoma, Kadoma-shi, Osaka-fu

[Name] Takakazu Yamada

[Inventor]

[Address or Domicile] Matsushita Electric Industrial Co., Ltd.
1006, Kadoma, Kadoma-shi, Osaka-fu

[Name] Michikazu Matsumoto

[Applicant]

[Identification Number] 000005821

[Name or Title] Matsushita Electric Industrial Co., Ltd.

[Representative] Yoichi Morishita

[Agent]

[Identification Number] 100077931

[Patent Attorney]

[Name or Title] Hiroshi Maeda

[Appointed Agent]

[Identification Number] 100094134

[Patent Attorney]

[Name or Title] Hirotake Koyama

[Indication of Fees]

[Payment Method] Deposit

[Deposit Account Number] 014409

[Amount deposited] 21,000 JPY

[List of Submitted Items]

[Item Name] Specification 1

[Item Name] Drawings 1

[Item Name] Abstract 1

[General Power of Attorney Number] 9006026

[Necessity of Proof] Necessary

[Document Name] Specification

[Title of the Invention] SEMICONDUCTOR DEVICE AND MANUFACTURING
METHOD THEREOF

[Claims]

1. A semiconductor device, comprising:
 - a semiconductor substrate,
 - an element formation region placed in a portion of the substrate,
 - groove-type element isolation that surrounds the element formation region, and that comprises a height difference part, which becomes higher in a step manner than the semiconductor substrate of the element formation region, with the element formation region, and that is made from an insulating material, and
 - height difference-part sidewalls formed on side surfaces of the height difference part between the element formation region and the groove-type element isolation.
2. The semiconductor device according to claim 1, wherein
 - the height difference-part sidewalls are formed with an insulating material.
3. The semiconductor device according to claim 1, wherein
 - a MISFET, comprising: a gate electrode and electrode-part sidewalls on both side surfaces of the gate electrode is formed in the element formation region, and
 - the height difference-part sidewalls are formed at the same time as the electrode-part sidewalls.
4. The substrate device according to claim 3,
 - the electrode-part sidewalls are formed with an L-shaped silicon nitride film with substantially constant thickness that is formed via a protective oxide film throughout the side surfaces of the gate electrode onto the semiconductor substrate; and
 - the height difference-part sidewalls are formed with an L-shaped silicon nitride film with substantially constant thickness that are formed via a protective oxide film throughout side surfaces between the element formation region and the groove-type element isolation onto the semiconductor substrate.

Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts



Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research



With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips



Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.