CLYDE "KIP" M. BROWN, Jr., P.E.

Professional Summary

Mr. Brown has 45 years experience in the silicon industry. He is an engineer, an entrepreneur and a teacher. He was a founder of three "successful" start-up companies and taught graduate (UC Berkeley) and undergraduate (Cogswell College) evening courses for 15 years. He is a Registered Professional Engineer in the state of California and he holds 10 patents. He was Chair of the **IEEE-CNSV** (www.californiaconsultants.org) 2014/2015 and Technical Co-Chair of the IEDEC conference (www.iedec.org).

Mr. Brown has provided expert witness litigation support in fourteen cases including patent, trade secret, breach of contract and product quality. He has generated numerous documents, been deposed and testified at Markman hearing, arbitration hearing, trial by Judge, trial by jury and ITC (9 trials). He was also appointed as a Neutral Referee by Santa Clara Superior Court.

Expertise

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- Analog Mixed signal IC design
- Medical electronics
- Mixed signal ASIC design
- Low voltage CMOS & Bipolar
- A-D converter design
- SRAM & EPROM design
- High-speed video RAM design
- IC design tool development

- High voltage mixed signal design
- **BiCMOS** design
- Power conversion
- Linear monolithic IC design •
- Electro-optical design .
- DSP u-Processor development •
- Analog switch technology
- Low noise op-amp design

Employment History

From:	2003	CMBJR CONSULTING Inc.
To:	Present	Cupertino, CA

Present Cupertino, CA

Position: Consultant / President

- Expert witness IP consulting
- Start up guidance
- Analog mixed signal IC chip design
- Smart power products; design and process development
- Medical and low power/low noise single battery IC design

From: To:	1980 2003 Position:	 The Engineering Consortium, Inc. (incorporated 1983) Santa Clara, CA President, Founder and CTO Design and fabrication of mixed signal ASICs using CMOS, Bipolar, BiCMOS and DMOS technologies Designed first WDRC hearing aid (K-Amp with Etymotic Research) and first single battery HA class D power amp (D- Amp for Knowles) Designed and produced the industry standard (single battery) programming chip for hearing aids (DSD – TEC941) Designed and produced the first single chip hearing aid IC, the HSOCtm (low noise analog, digital, EEPROM at one volt) Developed full DSP u-processor, single battery, hearing aid SOC Unique experience in low voltage CMOS and Bipolar applications including medical devices and hearing aids On-chip optical sensors and sensor interfaces Designed and developed circuitry used in over 30% of hearing aid market worldwide (65% of hearing aids in US) Designed and developed full line of high voltage mixed signal "Smart Power" products ("Top 100 Product of the year")
From: To:	1987 1988 Position:	 Micrel Sunnyvale, CA Consultant / Design Manager Smart Power Joint development (process and design) Smart Power Gate array ("Top 100 products" award) Various Custom High voltage, mixed signal product designs
From: To:	1983 1985 Position:	 MOS Electronics (now Mosel Vitelic) Sunnyvale, CA Founder and Director of Custom Integrated Circuits Interfaced with venture, business and customer contacts Negotiated contracts. Developed a workstation based CAD/CAE technology Design/development of sub-micron SRAM and EPROM chips Designed high-speed video RAM DAC
From: To:	1979 1993 Position:	U.C. Berkeley Berkeley, CA Graduate Extension Instructor (evening) Integrated circuit design courses
From: To:	1973 1994 Position:	Cogswell Polytechnical College Sunnyvale, CA Instructor - Evening Classes Analog design courses, Senior Project

From: To:	1980 1983 Position:	 Fairchild Semiconductor Linear Division Mountain View, CA Telecommunications Products Design Manager: Key Technologist Major contributor to circuit design and process development programs including: Analog CMOS LSI telecom products Switched capacitor filters and CODEC's Data acquisition products High voltage and standard linear bipolar products Silicon gate CMOS process development Bipolar-DMOS process test pattern Thin film and wafer level thin film laser trimming Library & auto place and route tool development
From: To:	1976 1980 Position:	 Raytheon Semiconductor Mountain View, CA Linear Design Manager and BiCMOS Design Manager Set up and staffed a design team to pursue new product development including all aspects from conception through design and development Product introduction applications and sustaining support Key technical contributor to numerous products including: low noise op amps, V/F converters, converter products, references, CMOS, BiCMOS process development and advanced bipolar device technology
From: To:	1978 1979 Position:	Sage Enterprises Mountain View, CA Director of Engineering / Founder Principal analog designer and manager of all engineering functions pertaining to new product development and the company's line of thermal impedance and "custom automatic parameter tester" products
From: To:	1972 1976 Position:	 Intersil, Inc, Cupertino, CA Hybrid Design Manager Design responsibility for all hybrid and monolithic products in the "Hybrid" product line Senior technical contributor responsible for design of numerous products including: CMOS thin film DAC, Bipolar-PMOS driver circuits, CMOS analog switches, dual slope A-D converter and bipolar amplifiers
From: To:	1971 1972 Position:	Siliconix, Inc., Santa Clara, CA Senior Design Engineer Design custom and standard product linear monolithic IC's in bipolar, bipolar-PMOS and NMOS Processes

From:	1968	GTE Sylvania
To:	1971	Mountain View, CA
	Position:	Electro-Optics Device Engineer
		System and circuit design responsibility for electro-optic projects
		(R&D, military and commercial)

Litigation Support Experience (in which reports, depositions and/or trial testimony have been produced)

Date:	1985 Case: Project: Status:	Cooley Godward, LLP for Systron Donner Systron Donner v. BEI– Arbitration Expert witness consulting in analog mixed signal and semiconductor device design Closed
Date:	1997-98 Case: Project: Status:	San Jose Superior Court CV764261-National Semiconductor v. Allouche et al Court appointed Neutral Referee Identified Trade secrets – trial testimony monitored offenders
Date:	2003/4 Case: Project: Status:	Jenkins & Gilchrist 4:02cv362 - STMicroelectronics, Inc. v. Broadcom Corp Expert witness analog mixed signal and semiconductor device design, Patent litigation – Markman testimony, Invalidity, Non- infringement, declarations and depositions. Settled
Date:	2003/5 Case: Project: Status:	Wilson Sonsini Goodrich & Rosati for Monolithic Power Systems CV-00-4071 (CW) (EDL) & CV-01-3995 (CW); O2 micro v. MPS Patent infringement, Trade secret, Invalidity, Non-Infringement and theft of Trade secret reports, depositions, Trial testimony Trial completed
Date:	2003-04 Case: Project: Status:	Latham & Watkins for NSC CV 807339 – ZF Microsolutions Inc. v. NSC Breach of contract, trade secret –Jury trial testimony Expert reports, analog mixed signal, microprocessor and semiconductor device design, project management. Trial completed Settled before retrial
Date:	2004-05 Case: Project: Status:	Kenyon & Kenyon for Taiwan Sumidia 2-03CV-007TJW – O2Micro v. TSE Patent infringement Markman, invalidity report, non-infringement, jury trial testimony Trial Concluded

Date:	2005 Case: Project: Status:	Weil Gotshal & Manges LLP for Thompson CSF ITC Inv. No. 337-TA-534 – CSF v. BEN Q ITC Patent infringement, Markman, Reverse engineering, Expert reports, Tutorials, ITC Trial testimony Settled after trial
Date:	2006-7 Case: Project: Status:	Sidley Austin LLP for Samsung SDI CV05-8493-AG(SJHx) – Samsung v. Matsushita Patent infringement, Expert reports, depositions Settled pre trial
Date:	2007 Case: Project: Status:	Hunton Williams LLP for Energy Transportation Group C.A. NO. 05-422-(GMS) – ETG v. Sonic et. all Patent infringementClaim construction, infringement analysis, infringement & validity reports, Trial testimony Trial completed
Date:	2007 Case: Project: Status:	Orrick LLP – Power control products Patent infringement investigation and Patent valuation Patent portfolio analysis, reverse engineering, prior art evaluation Consulting completed
Date:	2007 Case: Project: Status:	Melick, Porter & Shea LLP for Vigilant Ins. Co., et. al. 07-cv-10517-WGY– Vicor v. Federal Insurance Company Insurance claim—Technical analysis / Root analysis of failures, reports, deposition Trial completed
Date:	2008 Case: Project: Status:	Greenberg Traurig, LLP for Epic AAA Case No. 50 494 T00438 07 – Epic vs. Ali Theft of trade secrets, report, arbitration testimony Judgment in favor Epic
Date:	2011 Case: Project: Status:	Bergeson, LLP for Unicond CALIF SCV240886 – Nguyen v. Huang & Unicond of Taipei et al Theft of trade secrets, Breach of contract Settled pre trial
Date:	2012 Case: Project: Status:	Jones Day, LLP for Gennum 3:08- CV-05489 FLW (DNJ)– Oticon v. Sebotek, Gennum et all Patent litigation, Markman Settled
Date:	2014 Case: Project Status:	Ropes & Gray LLP for Spansion Inv. 337-TA-909 (U.S.I.T.C.)– Macronix International Co., Ltd., et al. v. Spansion, Inc., et al ITC Patent litigation, Reports, depositions, Trial testimony, IPR Pending Judgment (settled)

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