CLYDE "KIP" M. BROWN, Jr., P.E.

Professional Summary

Mr. Brown has 45 years experience in the silicon industry. He is an engineer, an entrepreneur and a teacher. He was a founder of three "successful" start-up companies and taught graduate (UC Berkeley) and undergraduate (Cogswell College) evening courses for 15 years. He is a Registered Professional Engineer in the state of California and he holds 10 patents. He was Chair of the IEEE-CNSV (www.californiaconsultants.org) 2014/2015 and Technical Co-Chair of the IEDEC conference (www.iedec.org).

Mr. Brown has provided expert witness litigation support in fourteen cases including patent, trade secret, breach of contract and product quality. He has generated numerous documents, been deposed and testified at Markman hearing, arbitration hearing, trial by Judge, trial by jury and ITC (9 trials). He was also appointed as a Neutral Referee by Santa Clara Superior Court.

Expertise

- Analog Mixed signal IC design
- Medical electronics
- Mixed signal ASIC design
- Low voltage CMOS & Bipolar
- A-D converter design
- SRAM & EPROM design
- High-speed video RAM design
- IC design tool development

- High voltage mixed signal design
- BiCMOS design
- Power conversion
- Linear monolithic IC design
- Electro-optical design
- DSP u-Processor development
- Analog switch technology
- Low noise op-amp design

Employment History

From: 2003   CMBJR CONSULTING Inc.
To: Present   Cupertino, CA
    Position: Consultant / President
- Expert witness – IP consulting
- Start up guidance
- Analog mixed signal IC chip design
- Smart power products; design and process development
- Medical and low power/low noise single battery IC design

From:	1980	The Engineering Consortium, Inc. (incorporated 1983)
To:	2003	Santa Clara, CA
	Position:	President, Founder and CTO

- Design and fabrication of mixed signal ASICs using CMOS, Bipolar, BiCMOS and DMOS technologies
- Designed first WDRC hearing aid (K-Amp with Etymotic Research) and first single battery HA class D power amp (D-Amp for Knowles)
- Designed and produced the industry standard (single battery) programming chip for hearing aids (DSD – TEC941)
- Designed and produced the first single chip hearing aid IC, the HSOC^{tm} (low noise analog, digital, EEPROM at one volt)
- Developed full DSP u-processor, single battery, hearing aid SOC
- Unique experience in low voltage CMOS and Bipolar applications including medical devices and hearing aids
- On-chip optical sensors and sensor interfaces
- Designed and developed circuitry used in over 30% of hearing aid market worldwide (65% of hearing aids in US)
- Designed and developed full line of high voltage mixed signal "Smart Power" products ("Top 100 Product of the year")

From:	1987	Micrel
To:	1988	Sunnyvale, CA
	Position:	Consultant / Design Manager

- Smart Power Joint development (process and design)
- Smart Power Gate array ("Top 100 products" award)
- Various Custom High voltage, mixed signal product designs

From:	1983	MOS Electronics (now Mosel Vitelic)
To:	1985	Sunnyvale, CA
	Position:	Founder and Director of Custom Integrated Circuits

- Interfaced with venture, business and customer contacts
- Negotiated contracts.
- Developed a workstation based CAD/CAE technology
- Design/development of sub-micron SRAM and EPROM chips
- Designed high-speed video RAM DAC

From:	1979	U.C. Berkeley
To:	1993	Berkeley, CA
	Position:	Graduate Extension Instructor (evening)
		Integrated circuit design courses

From:	1973	Cogswell Polytechnical College
To:	1994	Sunnyvale, CA
	Position:	Instructor - Evening Classes
		Analog design courses, Senior Project

From:	1980	Fairchild Semiconductor Linear Division
To:	1983	Mountain View, CA
	Position:	Telecommunications Products Design Manager: Key Technologist

Major contributor to circuit design and process development programs including:
- Analog CMOS LSI telecom products
- Switched capacitor filters and CODEC's
- Data acquisition products
- High voltage and standard linear bipolar products
- Silicon gate CMOS process development
- Bipolar-DMOS process test pattern
- Thin film and wafer level thin film laser trimming
- Library & auto place and route tool development

From:	1976	Raytheon Semiconductor
To:	1980	Mountain View, CA
	Position:	Linear Design Manager and BiCMOS Design Manager

- Set up and staffed a design team to pursue new product development including all aspects from conception through design and development
- Product introduction applications and sustaining support
- Key technical contributor to numerous products including: low noise op amps, V/F converters, converter products, references, CMOS, BiCMOS process development and advanced bipolar device technology

From:	1978	Sage Enterprises
To:	1979	Mountain View, CA
	Position:	Director of Engineering / Founder

Principal analog designer and manager of all engineering functions pertaining to new product development and the company's line of thermal impedance and "custom automatic parameter tester" products

From:	1972	Intersil, Inc,
To:	1976	Cupertino, CA
	Position:	Hybrid Design Manager

- Design responsibility for all hybrid and monolithic products in the "Hybrid" product line
- Senior technical contributor responsible for design of numerous products including: CMOS thin film DAC, Bipolar-PMOS driver circuits, CMOS analog switches, dual slope A-D converter and bipolar amplifiers

From:	1971	Siliconix, Inc.,
To:	1972	Santa Clara, CA
	Position:	Senior Design Engineer

Design custom and standard product linear monolithic IC's in bipolar, bipolar-PMOS and NMOS Processes

From:	1968	GTE Sylvania
To:	1971	Mountain View, CA
	Position:	Electro-Optics Device Engineer
		System and circuit design responsibility for electro-optic projects (R&D, military and commercial)

Litigation Support Experience (in which reports, depositions and/or trial testimony have been produced)

Date:	1985	Cooley Godward, LLP for Systron Donner
	Case:	Systron Donner v. BEI– Arbitration
	Project:	Expert witness consulting in analog mixed signal and semiconductor device design
	Status:	Closed

Date:	1997-98	San Jose Superior Court
	Case:	CV764261-National Semiconductor v. Allouche et al
	Project:	Court appointed Neutral Referee
	Status:	Identified Trade secrets – trial testimony -- monitored offenders

Date:	2003/4	Jenkins & Gilchrist
	Case:	4:02cv362 - STMicroelectronics, Inc. v. Broadcom Corp
	Project:	Expert witness -- analog mixed signal and semiconductor device design, Patent litigation – Markman testimony, Invalidity, Non-infringement, declarations and depositions.
	Status:	Settled

Date:	2003/5	Wilson Sonsini Goodrich & Rosati for Monolithic Power Systems
	Case:	CV-00-4071 (CW) (EDL) & CV-01-3995 (CW); O2 micro v. MPS
	Project:	Patent infringement, Trade secret, Invalidity, Non-Infringement and theft of Trade secret reports, depositions, Trial testimony
	Status:	Trial completed

Date:	2003-04	Latham & Watkins for NSC
	Case:	CV 807339 – ZF Microsolutions Inc. v. NSC
	Project:	Breach of contract, trade secret –Jury trial testimony Expert reports, analog mixed signal, microprocessor and semiconductor device design, project management.
	Status:	Trial completed -- Settled before retrial

Date:	2004-05	Kenyon & Kenyon for Taiwan Sumidia
	Case:	2-03CV-007TJW – O2Micro v. TSE -- Patent infringement
	Project:	Markman, invalidity report, non-infringement, jury trial testimony
	Status:	Trial Concluded

Date: 2005   Weil Gotshal & Manges LLP for Thompson CSF
Case: ITC Inv. No. 337-TA-534 – CSF v. BEN Q
Project: ITC Patent infringement, Markman, Reverse engineering, Expert reports, Tutorials, ITC Trial testimony
Status: Settled after trial

Date: 2006-7   Sidley Austin LLP for Samsung SDI
Case: CV05-8493-AG(SJHx) – Samsung v. Matsushita
Project: Patent infringement, Expert reports, depositions
Status: Settled pre trial

Date: 2007   Hunton Williams LLP for Energy Transportation Group
Case: C.A. NO. 05-422-(GMS) – ETG v. Sonic et. all
Project: Patent infringement--Claim construction, infringement analysis, infringement & validity reports, Trial testimony
Status: Trial completed

Date: 2007   Orrick LLP – Power control products
Case: Patent infringement investigation and Patent valuation
Project: Patent portfolio analysis, reverse engineering, prior art evaluation
Status: Consulting completed

Date: 2007   Melick, Porter & Shea LLP for Vigilant Ins. Co., et. al.
Case: 07-cv-10517-WGY– Vicor v. Federal Insurance Company
Project: Insurance claim—Technical analysis / Root analysis of failures, reports, deposition
Status: Trial completed

Date: 2008   Greenberg Traurig, LLP for Epic
Case: AAA Case No. 50 494 T00438 07 – Epic vs. Ali
Project: Theft of trade secrets, report, arbitration testimony
Status: Judgment in favor Epic

Date: 2011   Bergeson, LLP for Unicond
Case: CALIF SCV240886 – Nguyen v. Huang & Unicond of Taipei et al
Project: Theft of trade secrets, Breach of contract
Status: Settled pre trial

Date: 2012   Jones Day, LLP for Gennum
Case: 3:08- CV-05489 FLW (DNJ)– Oticon v. Sebotek, Gennum et all
Project: Patent litigation, Markman
Status: Settled

Date: 2014   Ropes & Gray LLP for Spansion
Case: Inv. 337-TA-909 (U.S.I.T.C.)– Macronix International Co., Ltd., et al. v. Spansion, Inc., et al
Project ITC Patent litigation, Reports, depositions, Trial testimony, IPR
Status: Pending Judgment (settled)

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