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(12) **United States Patent**
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(54) **ANALOG DATA GENERATING AND PROCESSING DEVICE HAVING A MULTI-USE AUTOMATIC PROCESSOR**

(58) **Field of Classification Search**
USPC 710/1, 15, 63
See application file for complete search history.

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(*) **Notice:** Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 1333 days.
This patent is subject to a terminal disclaimer.

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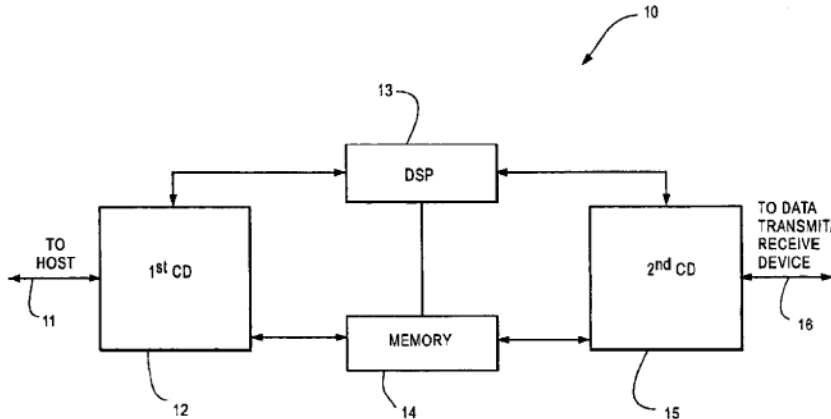
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(57) **ABSTRACT**
An interface device (10) provides fast data communication between a host device with input/output interfaces and a data transmit/receive device, wherein the interface device (10) comprises a processor means (13), a memory means (14), a first connecting device (12) for interfacing the host device with the interface device, and a second connecting device (15) for interfacing the interface device (10) with the data transmit/receive device. The interface device (10) is configured by the processor means (13) and the memory means (14) in such a way that, when receiving an inquiry from the host device via the first connecting device (12) as to the type of a device attached to the host device, regardless of the type of the data transmit/receive device, the interface device sends a signal to the host device via the first connecting device (12) which signals to the host device that it is communicating with an input/output device.

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