UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

ZTE (USA), INC. Petitioner,

v.

PAPST LICENSING GMBH & CO. KG Patent Owner.

Case IPR2017-00713 Patent 6,895,449

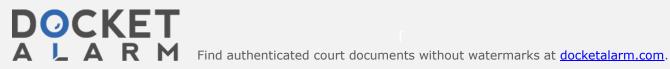
DECLARATION OF THOMAS A. GAFFORD IN SUPPORT OF PATENT OWNER RESPONSE

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United States Patent and Trademark Office
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I. Introduction

- I, Thomas A. Gafford, declare as follows:
- 1. I have been retained on behalf of Papst Licensing GmbH & Co. KG ("Papst"), and its counsel, Fitch Even Tabin & Flannery LLP, as an expert in this proceeding. I am personally knowledgeable about the matters stated herein and am competent to make this declaration.
- 2. I understand that Petitioner filed a Petition for *Inter Partes* Review regarding certain claims of United States Patent No. 6,895,449 ("the '449 patent"), which was accompanied by the Declaration of Kevin Almeroth. (Ex. 1003.) I am aware that the Petition challenges claims 1, 16, and 17 as unpatentable under 35 U.S.C. § 103(a) over U.S. Patent No. 5,758,081 to Aytac (Ex. 1005) in combination with the American National Standard for Information Systems, Small Computer System Interface-2, ANSI X3.131-1994 (1994) ("SCSI Specification") (Ex. 1006), The MS-DOS Encyclopedia (Ex. 1007), and alleged Admitted Prior Art.
- 3. I have been asked to provide my conclusions and bases thereof regarding several aspects of the issues in dispute. Based on my investigation in this matter, I conclude that Petitioner and Mr. Almeroth have not shown that the challenged claims of the '449 patent are unpatentable over Aytac in combination with the SCSI Specification, the MS-DOS Encyclopedia, and



the alleged Admitted Prior Art.

4. I receive compensation at my standard hourly rate of \$550 per hour for my time working on this matter, plus expenses. I have no financial interest in Papst or in the '449 patent, and my compensation is not dependent on the outcome of this trial or any of the related district court proceedings involving the '449 patent. The conclusions I present are due to my own judgment.

II. Background And Qualifications

- 5. My qualifications as an expert in the field of computer peripherals and data transfer between a computer and peripheral devices, relevant to the subject matter of the analog data generating and processing devices ("ADGPD") claimed in the '449 patent, are provided in the paragraphs below. A copy of my latest curriculum vitae (CV) is attached as Appendix A, which provides further details regarding my background and qualifications. This CV identifies a list of all cases in which I have testified at trial or at deposition.
- 6. I have over forty years of experience with electronics and electrical engineering, including extensive knowledge and experience with analog and digital electronic circuitry, digital computer technology, computer peripherals, control systems, digital communications, operating



systems, and related software and hardware components. My technical expertise relevant to the subject matter of the ADGPD claimed in the '449 patent includes my understanding of computer peripherals, analog and digital circuitry, interface devices, device drivers, file systems, SCSI standards, data buses, and operating systems.

- 7. As a summary of my employment and education history, I worked as a Sergeant and Instructor for the United States Air Force as a maintenance technician for air defense computer systems from 1967–1970. After leaving the Air Force, I earned my Bachelor of Science in Electrical Engineering in 1972 from the University of Washington. After graduating, I was a candidate for a Master of Science degree in Electrical Engineering at Stanford University from 1972–1973, and I worked from 1973–1976 as an Engineer at Stanford University's Artificial Intelligence Laboratory. My duties included the design, construction, and debugging of motor controls and analog to digital sensor electronics for robotics and computer interfaces.
- 8. After leaving Stanford, I founded G Systems in 1976, which managed the hardware and software design and development of computer transaction processing systems for a variety of applications and customers. Projects included writing communications software and device drivers, design of hardware and software interfaces for disk controllers, designing an



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