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UTILITY PATENT APPLICATION **TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

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Express Mail Label No.	EV541427914 US	0) j
Title	Flexible Interface).Ld
First Inventor	TASLER	
Attorney Docket No.	SCHO0102D-C	

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents.	ADDRESS TO: Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450	
1. X Fee Transmittal Form (e.g., PTO/SB/17) (Submit an original and a duplicate for fee processing) 2. X Applicant claims small entity status. See 37 CFR 1.27. 3. X Specification [Total Pages 23] (preferred arrangement set forth below) - Descriptive title of the invention - Cross Reference to Related Applications - Statement Regarding Fed sponsored R & D - Reference to sequence listing, a table, or a computer program listing appendix - Background of the Invention - Brief Summary of the Invention - Brief Description of the Drawings (if filed) - Detailed Description - Claim(s)	7. CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix) 8. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) a. Computer Readable Form (CRF) b. Specification Sequence Listing on: i. CD-ROM or CD-R (2 copies); or ii. Paper c. Statements verifying identity of above copies ACCOMPANYING APPLICATION PARTS	
	7 CFR 1.76:	
- Cross Reference to Related Applications - Statement Regarding Fed sponsored R & D - Reference to sequence listing, a table, or a computer program listing appendix - Background of the Invention - Brief Summary of the Invention - Brief Description of the Drawings (# filed) - Detailed Description - Claim(s) - Preliminary Amendment - Return Receipt Postcard (MPEP 503) - Claim(s) - Continuation - Detailed Description - Claim(s) - Detailed Description - Continuation - Detailed Description - Claim(s) - Detailed Desc		
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City	State Zip Code	
	elephone Fax	
Name (Print/Type) Michael A. Glenn	Registration No. (Attorney/Agent) 30,176	
Signature 7	Date Name 14 2005	

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SUBMITTED BY	UBMITTED BY							
Signature	n	Registration No. (Attorney/Agent)	30,176	Telephone	(650) 474-8400			
Name (Print/Type)	Michael A. Glenn			Date	March 11, 2005			

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Kristianne Serrano

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- Copy of Revocation of Prior and Grant of New Power of Attorney (1 page);
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- Abstract of the Disclosure 4. X Drawing(s) (35 U.S.C. 113) [Total Sheets 2] 5. Oath or Declaration [Total Sheets 3] a. Newly executed (original or copy) b. Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional with Box 18 completed) i. DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) name in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b). 6. Application Data Sheet. See 37 CFR 1.76	9. Assignment Papers (cover sheet & document(s)) 10. 37 CFR 3.73(b) Statement						
specification following the title, or in an Application Data Sheet under 37 X Continuation Divisional Continuation	7 CFR 1.76: tion-in-part (CIP) of prior application No.:10/219,105						
- Detailed Description - Claim(s) - Abstract of the Disclosure 4. \[\int Drawing(s) (35 U.S.C. 113) \] [Total Sheets \(2 \) 1 - Detailed Poscription - Claim(s) - Abstract of the Disclosure 4. \[\int Drawing(s) (35 U.S.C. 113) \] [Total Sheets \(2 \) 1 - Detailed Vexecuted (original or copy) - Assignment Papers (cover sheet & document(s)) - 37 CFR 3.73(b) Statement \[\int \] Power of (when there is an assignee) - Attorney - Attorney - Attorney - Attorney - Attorney - Information Disclosure - Copies of IDS - Copy from a prior application (37 CFR 1.63(d)) - (for continuation/divisional with Box 18 completed) - (for continuation/divisional with Box 18 completed) - (for continuation Disclosure \(\) Copies of IDS - Certified Copy of Priority Document(s) - (if foreign priority is claimed) - (if oreign pr							
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Flexible Interface

Description

The present invention relates to the transfer of data and in particular to interface devices for communication between a computer or host device and a data transmit/receive device from which data is to be acquired or with which two-way communication is to take place.

Existing data acquisition systems for computers are very limited in their areas of application. Generally such systems can be classified into two groups.

In the first group host devices or computer systems are attached by means of an interface to a device whose data is to be acquired. The interfaces of this group are normally standard interfaces which, with specific driver software, can be used with a variety of host systems. An advantage of such interfaces is that they are largely independent of the host device. However, a disadvantage is that they generally require very sophisticated drivers which are prone to malfunction and which limit data transfer rates between the device connected to the interface and the host device and vice versa. Further, it is often very difficult to implement such interfaces for portable systems and they offer few possibilities for adaptation with the result that such systems offer little flexibility.

The devices from which data is to be acquired cover the entire electrical engineering spectrum. In a typical case, it is assumed that a customer who operates, for example, a diagnostic radiology system in a medical engineering environment reports a fault. A field service technician of the system manufacturer visits the customer and reads system log files generated by the diagnostic radiology system by means a portable computer or laptop for example. If the fault cannot be localized or if the fault is intermittent, it will be necessary for the service technician to read not only an error log file but also data from current operation. It is apparent that in this case fast data transfer and rapid data analysis are necessary.

Another case requiring the use of an interface could be, for example, when an electronic measuring device, e.g. a multimeter, is attached to a computer system to transfer the data measured by the multimeter to the computer. Particularly when long-term measurements or large volumes of data are involved is it necessary for the interface to support a high data transfer rate.

From these randomly chosen examples it can be seen that an interface may be put to totally different uses. It is therefore desirable that an interface be sufficiently flexible to permit attachment of very different electrical or electronic systems to a host device by means of the interface. To prevent operator error, it is also desirable that a service technician is not required to operate different interfaces in different ways for different applications but that, if possible, a universal method of operating the interface be provided for a large number of applications.

To increase the data transfer rates across an interface, the route chosen in the second group of data acquisition systems for the interface devices was to specifically match the interface very closely to individual host systems or computer systems. The advantage of this solution is that high data transfer rates are possible. However, a disadvantage is that the drivers for the interfaces of the second group are very closely matched to a single host system with the result that they generally cannot be used with other host systems or their use is very ineffective. Further, such types of interface have the disadvantage that they must be installed inside the computer casing to achieve maximum data transfer rates as they access the internal host bus system. They are therefore generally not suitable for portable host systems in the form of laptops whose minimum possible size leaves little internal space to plug in an interface card.

A solution to this problem is offered by the interface devices of IOtech (business address: 25971 Cannon Road, Cleveland, Ohio 44146, USA) which are suitable for laptops such as the WaveBook/512 (registered trademark). The interface devices are connected by means of a plug-in card, approximately the size of a credit card, to the PCMCIA interface which is now a standard feature in laptops. The plug-in card converts the PCMCIA interface into an interface known in the art as IEEE 1284. The said plug-in card provides a special printer interface which is enhanced as regards the data transfer rate and delivers a data transfer rate of approximately 2 MBps as

compared with a rate of approx. 1 MBps for known printer interfaces. The known interface device generally consists of a driver component, a digital signal processor, a buffer and a hardware module which terminates in a connector to which the device whose data is to be acquired is attached. The driver component is attached directly to the enhanced printer interface thus permitting the known interface device to establish a connection between a computer and the device whose data is to be acquired.

In order to work with the said interface, an interface-specific driver must be installed on the host device so that the host device can communicate with the digital signal processor of the interface card. As described above, the driver must be installed on the host device. If the driver is a driver developed specifically for the host device, a high data transfer rate is achieved but the driver cannot be easily installed on a different host system. However, if the driver is a general driver which is as flexible as possible and which can be used on many host devices, compromises must be accepted with regard to the data transfer rate.

Particularly in an application for multi-tasking systems in which several different tasks such as data acquisition, data display and editing are to be performed quasi-simultaneously, each task is normally assigned a certain priority by the host system. A driver supporting a special task requests the central processing system of the host device for processor resources in order to perform its task. Depending on the particular priority assignment method and on the driver implementation, a particular share of processor resources is assigned to a special task in particular time slots. Conflicts arise if one or more drivers are implemented in such a way that they have the highest priority by default, i.e. they are incompatible, as happens in practice in many applications. It may occur that both drivers are set to highest priority which, in the worst case, can result in a system crash.

EP 0685799 A1 discloses an interface by means of which several peripheral devices can be attached to a bus. An interface is connected between the bus of a host device and various peripheral devices. The interface comprises a finite state machine and several branches each of which is assigned to a peripheral device. Each branch comprises a data manager, cycle control, user logic and a buffer. This known interface

device provides optimal matching between a host device and a specific peripheral device.

The specialist publication IBM Technical Disclosure Bulletin, Vol. 38, No. 05, page 245; "Communication Method between Devices through FDD Interface" discloses an interface which connects a host device to a peripheral device via a floppy disk drive interface. The interface consists in particular of an address generator, an MFM encoder/decoder, a serial/parallel adapter and a format signal generator. The interface makes it possible to attach not only a floppy disk drive but also a further peripheral device to the FDD host controller of a host device. The host device assumes that a floppy disk drive is always attached to its floppy disk drive controller and communication is initiated if the address is correct. However, this document contains no information as to how communication should be possible if the interface is connected to a multi-purpose interface instead of to a floppy disk drive controller.

It is the object of the present invention to provide an interface device for communication between a host device and a data transmit/receive device whose use is host device-independent and which delivers a high data transfer rate.

This object is achieved by an interface device according to claim 1 or 12 and by a method according to claim 15.

The present invention is based on the finding that both a high data transfer rate and host device-independent use can be achieved if a driver for an input/output device customary in a host device, normally present in most commercially available host devices, is utilized. Drivers for input/output devices customary in a host device which are found in practically all host devices are, for example, drivers for hard disks, for graphics devices or for printer devices. As however the hard disk interfaces in common host devices which can be, for example, IBM PCs, IBM-compatible PCs, Commodore PCs, Apple computers or even workstations, are the interfaces with the highest data transfer rate, the hard disk driver is utilized in the preferred embodiment of the interface device of the present invention. Drivers for other storage devices such as floppy disk drives, CD-ROM drives or tape drives could also be utilized in order to implement the interface device according to the present invention.

As described in the following, the interface device according to the present invention is to be attached to a host device by means of a multi-purpose interface of the host device which can be implemented, for example, as an SCSI interface or as an enhanced printer interface. Multi-purpose interfaces comprise both an interface card and specific driver software for the interface card. The driver software can be designed so that it can replace the BIOS driver routines. Communication between the host device and the devices attached to the multi-purpose interface then essentially takes place by means of the specific driver software for the multi-purpose interface and no longer primarily by means of BIOS routines of the host device. Recently however drivers for multi-purpose interfaces can also already be integrated in the BIOS system of the host device as, alongside classical input/output interfaces, multi-purpose interfaces are becoming increasingly common in host devices. It is of course also possible to use BIOS routines in parallel with the specific driver software for the multi-purpose interface, if this is desired.

The interface device according to the present invention comprises a processor means, a memory means, a first connecting device for interfacing the host device with the interface device, and a second connecting device for interfacing the interface device with the data transmit/receive device. The interface device is configured by the processor means and the memory means in such a way that the interface device, when receiving an inquiry from the host device via the first connecting device as to the type of a device attached to the host device, sends a signal, regardless of the type of the data transmit/receive device, to the host device via the first connecting device which signals to the host device that it is communicating with an input/output device. The interface device according to the present invention therefore simulates, both in terms of hardware and software, the way in which a conventional input/output device functions, preferably that of a hard disk drive. As support for hard disks is implemented as standard in all commercially available host systems, the simulation of a hard disk, for example, can provide host device-independent use. The interface device according to the present invention therefore no longer communicates with the host device or computer by means of a specially designed driver but by means of a program which is present in the BIOS system (Basic Input/Output System) and is normally precisely matched to the specific computer system on which it is installed,

...

or by means of a specific program for the multi-purpose interface. Consequently, the interface device according to the present invention combines the advantages of both groups. On the one hand, communication between the computer and the interface takes place by means of a host device-specific BIOS program or by means of a driver program which is matched to the multi-purpose interface and which could be regarded as a "device-specific driver". On the other hand, the BIOS program or a corresponding multi-purpose interface program which operates one of the common input/output interfaces in host systems is therefore present in all host systems so that the interface device according to the present invention is host device-independent.

In the following, preferred embodiments of the present invention will be explained in more detail with reference to the drawings enclosed, in which:

- Fig. 1 shows a general block diagram of the interface device according to the present invention; and
- Fig. 2 shows a detailed block diagram of an interface device according to a preferred embodiment of the present invention.

Fig. 1 shows a general block diagram of an interface device 10 according to the present invention. A first connecting device 12 of the interface device 10 can be attached to a host device (not shown) via a host line 11. The first connecting device is attached both to a digital signal processor 13 and to a memory means 14. The digital signal processor 13 and the memory means 14 are also attached to a second connecting device 15 by means of bi-directional communication lines (shown for all lines by means of two directional arrows). The second connecting device can be attached by means of an output line 16 to a data transmit/receive device which is to receive data from the host device or from which data is to be read, i.e. acquired, and transferred to the host device. The data transmit/receive device itself can also communicate actively with the host device via the first and second connecting device, as described in more detail in the following.

Communication between the host system or host device and the interface device is based on known standard access commands as supported by all known operating systems (e.g. DOS, Windows, Unix). Preferably, the interface device according to the present invention simulates a hard disk with a root directory whose entries are "virtual" files which can be created for the most varied functions. When the host device system with which the interface device according to the present invention is connected is booted and a data transmit/receive device is also attached to the interface device 10, usual BIOS routines or multi-purpose interface programs issue an instruction, known by those skilled in the art as the INQUIRY instruction, to the input/output interfaces in the host device. The digital signal processor 13 receives this inquiry instruction via the first connecting device and generates a signal which is sent to the host device (not shown) again via the first connecting device 12 and the host line 11. This signal indicates to the host device that, for example, a hard disk drive is attached at the interface to which the INQUIRY instruction was sent. Optionally, the host device can send an instruction, known by those skilled in the art as "Test Unit Ready", to the interface device to request more precise details regarding the queried device.

Regardless of which data transmit/receive device at the output line 16 is attached to the second connecting device, the digital signal processor 13 informs the host device that it is communicating with a hard disk drive. If the host device receives the response that a drive is present, it then sends a request to the interface device 10 to read the boot sequence which, on actual hard disks, normally resides on the first sectors of the disk. The digital signal processor 13, whose operating system in stored in the memory means 14, responds to this instruction by sending to the host device a virtual boot sequence which, in the case of actual drives, includes the drive type, the starting position and the length of the file allocation table (FAT), the number of sectors, etc., known to those skilled in the art. Once the host device has received this data, it assumes that the interface device 10 according to a preferred embodiment of the present invention is a hard disk drive. In reply to an instruction from the host device to display the directory of the "virtual" hard disk drive simulated by the interface device 10 with respect to the host device, the digital signal processor can respond to the host device in exactly the same way as a conventional hard disk would, namely by reading on request the file allocation table or FAT on a sector specified in the boot sequence, normally the first writable sector, and transferring it to the host device, and subsequently by transferring the directory structure of the virtual hard

disk. Further, it is possible that the FAT is not read until immediately prior to reading or storing the data of the "virtual" hard disk and not already at initialization.

In a preferred embodiment of the present invention, the digital signal processor 13, which need not necessarily be implemented as a digital signal processor but may be any other kind of microprocessor, comprises a first and a second command interpreter. The first command interpreter carries out the steps described above whilst the second command interpreter carries out the read/write assignment to specific functions. If the user now wishes to read data from the data transmit/receive device via the line 16, the host device sends a command, for example "read file xy", to the interface device. As described above, the interface device appears to the host device as a hard disk. The second command interpreter of the digital signal processor now interprets the read command of the host processor as a data transfer command, by decoding whether "xy" denotes, for example, a "real-time input" file, a "configuration" file or an executable file, whereby the same begins to transfer data from the data transmit/receive device via the second connecting device to the first connecting device and via the line 11 to the host device.

Preferably, the volume of data to be acquired by a data transmit/receive device is specified in a configuration file described in the following by the user specifying in the said configuration file that a measurement is to last, for example, five minutes. To the host device the "real-time input" file then appears as a file whose length corresponds to the anticipated volume of data in those five minutes. Those skilled in the art know that communication between a processor and a hard disk consists of the processor transferring to the hard disk the numbers of the blocks or clusters or sectors whose contents it wishes to read. By reference to the FAT the processor knows which information is contained in which block. In this case, communication between the host device and the interface device according to the present invention therefore consists of the very fast transfer of block numbers and preferably of block number ranges because a virtual "real-time input" file will not be fragmented. If the host device now wants to read the "real-time input" file, it transfers a range of block numbers to the interface device, whereupon data commences to be received via the second connecting device and data commences to be sent to the host device via the first connecting device.

In addition to the digital signal processor instruction memory, which comprises the operating system of the digital signal processor and can be implemented as an EPROM or EEPROM, the memory means 14 can have an additional buffer for purposes of synchronizing data transfer from the data transmit/receive device to the interface device 10 and data transfer from the interface device 10 to the host device.

Preferably, the buffer is implemented as a fast random access memory or RAM buffer.

Further, from the host device the user can also create a configuration file, whose entries automatically set and control various functions of the interface device 10, on the interface device 10 which appears to the host device as a hard disk. These settings can be, for example, gain, multiplex or sampling rate settings. By creating and editing a configuration file, normally a text file which is simple to understand with little prior knowledge, users of the interface device 10 are able to perform essentially identical operator actions for almost any data transmit/receive devices which can be attached to the second connecting device via the line 16, thus eliminating a source of error arising from users having to know many different command codes for different applications. In the case of the interface device 10 according to the present invention it is necessary for users to note the conventions of the configuration file once only in order to be able to use the interface device 10 as an interface between a host device and almost any data transmit/receive device.

As a result of the option of storing any files in agreed formats in the memory means 14 of the interface device 10, taking into account the maximum capacity of the memory means, any enhancements or even completely new functions of the interface device 10 can be quickly implemented. Even files executable by the host device, such as batch files or executable files (BAT or EXE files), and also help files can be implemented in the interface device, thus achieving independence of the interface device 10 from any additional software (with the exception of the BIOS routines) of the host device. On the one hand, this avoids licensing and/or registration problems and, on the other hand, installation of certain routines which can be frequently used, for example an FFT routine to examine acquired time-domain data in the frequency

domain, is rendered unnecessary as the EXE files are already installed on the interface device 10 and appear in the virtual root directory, by means of which the host device can access all programs stored on the interface device 10.

In a preferred embodiment of the present invention in which the interface device 10 simulates a hard disk to the host device, the interface device is automatically detected and readied for operation when the host system is powered up or booted. This corresponds to the plug-and-play standard which is currently finding increasingly widespread use. The user is no longer responsible for installing the interface device 10 on the host device by means of specific drivers which must also be loaded; instead the interface device 10 is automatically readied for operation when the host system is booted.

For persons skilled in the art it is however obvious that the interface device 10 is not necessarily signed on when the computer system is powered up but that a special BIOS routine or a driver for a multi-purpose interface can also be started on the host device during current operation of the computer system in order to sign on or mount the interface device 10 as an additional hard disk. This embodiment is suitable for larger workstation systems which are essentially never powered down as they perform, e.g. mail functions or monitor processes which run continuously, for example, in multi-tasking environments.

In the interface device according to the present invention an enormous advantage is to be gained, as apparent in the embodiment described in the following, in separating the actual hardware required to attach the interface device 10 to the data transmit/receive device from the communication unit, which is implemented by the digital signal processor 13, the memory means 14 and the first connecting device 12, as this allows a plurality of dissimilar device types to be operated in parallel in identical manner. Accordingly, many interface devices 10 can be connected to a host device which then sees many different "virtual" hard disks. In addition, any modification of the specific hardware symbolized by the second connecting device 15 can be implemented essentially without changing the operation of the interface device according to the present invention. Further, an experienced user can intervene at any time on any level of the existing second connecting device by making use of the above mentioned

option of creating a configuration file or adding or storing new program sections for the second connecting device.

An important advantage of the interface device 10 of the present invention is that it also permits extremely high data transfer rates by using, for data interchange, the host device-own BIOS routines which are optimized for each host device by the host device manufacturer or BIOS system manufacturer, or by using driver programs which are normally optimized and included by the manufacturers of multi-purpose interfaces. Furthermore, due to the simulation of a virtual mass storage device, the data is managed and made available in such a way that it can be transferred directly to other storage media, e.g. to an actual hard disk of the host device without, as it were, intervention of the host device processor. The only limitation to long-term data transfer at high speed is therefore imposed exclusively by the speed and the size of the mass storage device of the host device. This is the case as the digital signal processor 13 already formats the data read by the data transmit/receive device via the second connecting device 15 into block sizes suitable for a hard disk of the host device, whereby the data transfer speed is limited only by the mechanical latency of the hard disk system of the host device. At this point, it should be noted that normally data flow from a host device must be formatted in blocks to permit writing to a hard disk and subsequent reading from a hard disk, as known by those skilled in the art.

The said data transfer rate can be increased further by setting up a direct memory access (DMA) or RAM drive in the host system. As those skilled in the art know, the setting up of a RAM drive requires processor resources of the host device, with the result that the advantage of writing the data to a hard disk drive of the host device essentially without the need for processor resources is lost.

As described above, a data buffer can be implemented in the memory means 14 to permit independence in terms of time of the data transmit/receive device attached to the second connecting device from the host device attached to the first connecting device. This guarantees error-free operation of the interface device 10 even for time-critical applications in multi-tasking host systems.

Fig. 2 shows a detailed block diagram of an interface device 10 according to the present invention.

A digital signal processor (DSP) 1300 is, in a manner of speaking, the heart of the interface device 10. The DSP can be any DSP but preferably has a 20-MB on-chip random access memory (RAM). Certain instruction sets, for example, can be stored in the RAM already integrated in the DSP. An 80-MHz clock generator is attached to the DSP 1300 in order to synchronize the DSP. The DSP implements a fast Fourier transformation (FFT) in real time and also optional data compression of the data to be transferred from the data transmit/receive device to the host device in order to achieve greater efficiency and to permit interoperation with host devices which have a smaller memory.

In the preferred embodiment of the interface device 10 shown in Fig. 2, the first connecting device 12 of Fig. 1 contains the following components: an SCSI interface 1220 and a 50-pin SCSI connector 1240 for attachment to an SCSI interface present on most host devices or laptops. The SCSI (small computer system interface) interface 1220 translates the data received via the SCSI connector 1240 into data understood by the DSP 1300, as known by those skilled in the art. Further, the first connecting device 12 comprises an EPP (enhanced parallel port) with a data transfer rate of approx. 1 MBps which delivers a more moderate data transfer rate of 1 MBps by comparison to the data transfer rate of 10 MBps of the SCSI interface. The EPP 1260 is connected to a 25-pin D-shell connector 1280 to permit attachment to a printer interface of a host device for example. Optionally, the first connecting device 12 also comprises a 25-pin connector 1282 which permits the attachment of 8 digital outputs and 8 digital inputs 1284 at a host device.

Preferably, the second connecting device comprises 8 BNC inputs with the calibration relay 1505, a block 1510 with 8 device amplifiers with an overvoltage protection of ±75 V, this block being connected in turn to 8 sample/hold (S&H) circuits 1515. The calibration relays are relays which permit controlled changeover between a test voltage and a calibration reference voltage. Each sample/hold circuit is connected to a corresponding input of an 8-channel multiplexer 1520 which feeds its output signals

via a programmable amplifier 1525 into an analog/digital converter (ADC) with 12 bit and 1.25 MHz 1530 and to the DSP 1300. The ADC 1530 is controlled by means of a 20-bit timer 1535, as known by persons skilled in the art. The programmable amplifier 1525 and the 8-channel multiplexer 1520 are controlled via an amplifier channel selection circuit 1540 which is in turn controlled by the DSP 1300.

The complete interface device 10 is supplied with power by an external AC/DC converter 1800 which delivers a digital supply voltage of ±5 V and is attached to a DC/DC converter 1810 which can deliver analog supply voltages of ±5 V and ±15 V as required for the interface device 10. Further, the DC/DC converter controls a precision voltage reference 1820 which controls the 8 BNC inputs 1505 and the ADC 1530 as well as a digital/analog converter (DAC) 1830 which permits, via an output amplifier block with 4 output amplifiers 1840 and a 9-pin connector 1850, analog output direct from the DSP 1300 to an output device, e.g. printer device or monitor device, which can be attached via the 9-pin connector 1850, thus providing the option of monitoring the data transferred to the host device or also, for example, of viewing an FFT to obtain rapid and comprehensive data analysis without using processor time of the host device.

In Fig. 2 the memory means 14 of Fig. 1 is implemented by an EPROM 1400 which, in a preferred embodiment of the present invention, contains the operating system of the digital signal processor 1300. A random access memory with an access time of 15 ns and a size of 512 KB or optionally 1024 KB 1420 serves as a data buffer to achieve independence in terms of time of the output line 16 from the output lines 11a, 11b and 11c to the data transmit/receive device and to the host device respectively. As described above, in a preferred embodiment of the present invention the digital signal processor 1300 already contains a 20-KB on-chip RAM 1440 which can store certain instruction sets, functions and also smaller application software units.

The connection, symbolized by the line 16, of the interface device 10 to any data transmit/receive device implements, by means of the blocks 1505-1535, an analog input with a sampling rate of 1.25 MHz and quantization of 12 bits. There are 8 channels with an overvoltage protection of \pm 75 V. By means of the programmable

amplifier 1525 the channels can be programmed independently of each other in voltage ranges up to a maximum of \pm 10 V. Unused channels can be grounded internally to reduce channel intermodulation. The block 1515 is implemented as a monolithic high-precision, high-speed sample/hold amplifier for simultaneous sampling of all channels. The precision voltage reference 1820 provides a high-precision, temperature-compensated monolithic energy gap voltage reference for auto-calibration of each channel and each gain. Further, offset fine adjustment for each channel is implemented by the same.

The blocks 1830, 1840 and 1850 implement a direct analog output for the digital signal processor 1300, and the DAC 1830 provides a data transfer rate of 625 kHz and a quantization of 12 bits. The block 1840 comprises 4 channels with a common output latch.

Further, the interface device 10 comprises a digital input/output device implemented by the blocks 1284 and 1282. Here there are 8 digital inputs, 8 digital outputs with a common latch, and the digital port can be attached preferably to a side panel of the interface device 10 so that the port itself can easily be accessed.

The digital signal processor 1300 provides on-board digital data processing. In particular, it is a high-performance DSP with a clock speed of 80 MHz and a 20-bit timer 1535.

As described above, the first connecting device 12 comprises the SCSI interface 1220 with a peak transfer rate of 10 MBps. An optional PCMCIA-to-SCSI adapter permits high-speed communication with laptop computers which are desirable and in widespread use, particularly by mobile service technicians. The EPP 1260 with its associated connector 1280 permits data transfer at a more moderate rate.

As described above, the interface device 10 is supplied with power by means of an external AC/DC adapter which has a universal power input (85 - 264 VAC, 47 - 63 Hz). Interference suppression complies with the standards EN 55022, curve B and FFC, Class B). Further, it is also in accordance with international safety regulations

(TÜV, UL, CSA). The interface device 10 is externally shielded and achieves a value of 55 dB at 30 - 60 MHz and a value of approximately 40 dB at 1 GHz, and therefore complies with the MILSTD 285-1 standard.

As described above, communication between the host device and the multi-purpose interface can take place not only via drivers for input/output device customary in a host device which reside in the BIOS system of the host device but also via specific interface drivers which, in the case of SCSI interfaces, are known as multi-purpose interface ASPI (advanced SCSI programming interface) drivers. This ASPI driver, which can also be referred to as an ASPI manager, is specific to a special SCSI host adapter, i.e. to a special multi-purpose interface, and is normally included by the manufacturer of the multi-purpose interface. Generally speaking, this multi-purpose interface driver has the task of moving precisely specified SCSI commands from the host system program to the host system SCSI adapter. For this reason, the command set is almost identical to that of the SCSI interface itself. Essentially, only status and reset commands for the host adapter have been added.

The ASPI driver can be used if the hard disk was not already addressable at boot time or if the SCSI-related BIOS routines of the host computer were still disabled. Here too, the steps needed to initialize the interface device, preferably as a virtual hard disk, are similar to the steps taken when initializing at boot time.

In general terms, the ASPI manager comprises two sides. One side is the proprietary, hardware-oriented side. It is responsible for converting all commands into a form required by the corresponding multi-purpose interface. The hardware-oriented side of the ASPI driver is therefore matched to a very specific type of multi-purpose interface or SCSI interface. The other side is known as the user software side. This side is totally independent of the proprietary operating characteristics of the SCSI adapter and is therefore identical for all SCSI interfaces. This permits SCSI programming which is however independent of the individual SCSI adapter types.

In contrast to communication between the host device and the interface device according to the present invention on the basis of a BIOS driver, the use of such an ASPI driver for communication between the host device and the interface device

according to the present invention allows various further possibilities of the SCSI multi-purpose interface to be exploited. In the case described above, the interface device which preferably signs on and behaves as a virtual hard disk is detected by the BIOS driver of the host computer at boot time and is configured as a hard disk. This step does not however support active requests sent by the interface device to the host computer. If however the virtual hard disk wishes to write data actively to, for example, a hard disk of the host computer or wishes to initiate communication with the processor of the host computer, the host computer must recognize the request of the virtual hard disk and tolerate a further issuer of instructions on its bus. If the interface device behaves solely like a virtual hard disk, it would always receive and never issue commands. The BIOS has no objections to an additional issuer of commands that actively wishes to place data on the bus of the host device but the BIOS does not support the host device in recognizing corresponding requests of the interface device or in granting the interface device permission to access the bus.

Using the ASPI manager the interface device according to the present invention can now obtain active access to an SCSI hard disk of the host device connected to the same SCSI bus which, in contrast to the interface device, cannot be a virtual but a real SCSI mass storage device or also a further interface device according to the present invention. Thereupon, the interface device according to the present invention can write the desired data to the SCSI hard disk of the host computer totally independently of the host computer or can communicate with the same in some other manner. The interface device according to the present invention therefore initially behaves passively as a virtual hard disk and then, as required and using the driver software for the multi-purpose interface, actively on the same SCSI bus. This means however that the interface device according to the present invention, using a driver software for the multi-purpose interface which comprises the BIOS routines customary in host devices and simultaneously provides the option of active participation, can, regardless of the type of the data transmit/receive device attached to the second connecting device, behave initially as a virtual and at the same time passive hard disk but can, as required, participate actively on the bus so as to be able to initiate communication directly with other SCSI hard disks of the host device by bypassing the processor of the host device.

Using a standard interface of a host device, the interface device according to the present invention permits communication with any host device. By simulating an input/output device to the host device and, in a preferred embodiment, by simulating a virtual mass storage device, the interface device 10 is automatically supported by all known host systems without any additional sophisticated driver software. The simulation of a freely definable file structure on the "virtual" hard disk provides simple operation and expansion options and, through the implementation of any programs, independence from special software implemented on the host device. Help files included on the interface device 10 and plug-and-play support ensure ease of use even in portable, flexible host devices. Despite the very simple user interface, experienced users are free at any time to intervene in the functions of the interface device 10 on system level. The interface device 10 thus provides a universal solution which can cover the entire spectrum of possible data transmit/receive devices.

Claims

An interface device (10) for communication between a host device, which
comprises drivers for input/output devices customary in a host device and a multipurpose interface, and a data transmit/receive device comprising the following
features:

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a processor means (13; 1300, 1320);
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a memory means (14; 1400, 1420, 1440);

a first connecting device (12; 1220, 1240, 1260, 1280) for interfacing the host device with the interface device (10) via the multi-purpose interface of the host device; and

a second connecting device (15; 1505 - 1535) for interfacing the interface device (10) with the data transmit/receive device,

wherein the interface device (10) is configured by the processor means (13; 1300, 1320) and the memory means (14; 1400, 1420, 1440) in such a way that the interface device, when receiving an inquiry from the host device as to the type of a device attached to the multi-purpose interface of the host device, sends a signal, regardless of the type of the data transmit/receive device attached to the second connecting device (15; 1505 – 1535) of the interface device (10), to the host device which signals to the host device that it is an input/output device customary in a host device, whereupon the host device communicates with the interface device (10) by means of the driver for the input/output device customary in a host device.

2. An interface device (10) according to claim 1,

wherein the drivers for input/output drivers customary in a host device comprise a

hard disk driver, and the signal indicates to the host device that the host device is communicating with a hard disk.

3. An interface device (10) according to claim 1 or 2,

wherein the memory means comprises a buffer (1420) to buffer data to be transferred between the data transmit/receive device and the host device.

4. An interface device (10) according to one of the preceding claims,

wherein the multi-purpose interface of the host device is an SCSI interface and the first connecting device also comprises an SCSI interface (1220).

5. An interface device (10) according to one of the preceding claims,

wherein the second connecting device comprises an analog input (1505) with a subsequent A/D converter (1530) in order to transfer analog data to the host device from a data transmit/receive device connectable to the analog device (1505).

6. An interface device (10) according to one of the preceding claims,

wherein the processor means (13) is a digital signal processor (1300).

7. An interface device (10) according to one of the claims 2 to 6,

wherein the data to be transferred from the data transmit/receive device to the host device in the interface device (10) is formatted in a suitable format for a hard disk present in the host device.

8. An interface device (10) according to one of the claims 2 to 7,

which further comprises a root directory and virtual files which are present on the

signaled hard disk drive and which can be accessed from the host device.

9. An interface device (10) according to claim 8,

wherein the virtual files comprise a configuration file in text format which are stored in the memory means (14) and using which the user can configure the interface device (10) for a specific data transmit/receive device.

10. An interface device (10) according to claim 8 or 9,

wherein the virtual files comprise batch files or executable files for the microprocessor means which are stored in the interface device (10) in order to perform data processing, independently of the host device, of data received via the second connecting device (15; 1505 - 1535).

11. An interface device (10) according to claim 8 or 9,

wherein the virtual files comprise batch files or executable files for the host device which are stored in the interface device (10).

12. An interface device (10) for communication between a host device, which comprises a multi-purpose interface and a specific driver for this interface, and a data transmit/receive device comprising the following features:

a processor means (13; 1300, 1320);

a memory means (14; 1400, 1420, 1440);

a first connecting device (12; 1220, 1240, 1260, 1280) for interfacing the host device with the interface device (10) via the multi-purpose interface of the host device; and

a second connecting device (15; 1505 - 1535) for interfacing the interface device (10) with the data transmit/receive device,

where the interface device (10) is configured using the processor means (13; 1300, 1320) and the memory means (14; 1400, 1420, 1440) in such a way that the interface device, when receiving an inquiry from the host device as to the type of a device attached at the multi-purpose interface of the host device, sends a signal, regardless of the type of the data transmit/receive device attached to the second connecting device (15; 1505 – 1535) of the interface device (10), to the host device which signals to the host device that it is an input/output device customary in a host device, whereupon the host device communicates with the interface device (10) by means of the specific driver for the multi-purpose interface.

13. An interface device according to claim 12,

wherein, in addition to the first connecting device of the interface device, there is a further input/output device at the multi-purpose interface of the host device, and wherein the interface device can communicate directly with the hard disk via the specific driver for the multi-purpose interface.

14. An interface device according to claim 12 or 13,

wherein the multi-purpose interface is an SCSI interface, and wherein the specific driver for the multi-purpose interface is an ASPI manager.

15. A method of communication between a host device, which comprises drivers for input/output devices customary in a host device and a multi-purpose interface, and a data transmit/receive device via an interface device (10) comprising the following steps:

interfacing of the host device with a first connecting device (12; 1220, 1240, 1260, 1280) of the interface device (10) via the multi-purpose interface of the host device;

interfacing of the data transmit/receive device with a second connecting device (15; 1505 - 1535) of the interface device (10);

inquiring by the host device at the interface device (10) as to the type of device to which the multi-purpose interface of the host device is attached;

regardless of the type of the data transmit/receive device attached to the second connecting device of the interface device (10), responding to the inquiry from the host device by the interface device (10) in such a way that it is an input/output device customary in a host device, whereupon the host device communicates with the interface device (10) by means of the usual driver for the input/output device.

16. A method according to claim 15,

wherein the drivers for input/output devices customary in a host device comprise a driver for a storage device and in particular for a hard disk drive.

Flexible Interface

ABSTRACT

An interface device (10) provides fast data communication between a host device with input/output interfaces and a data transmit/receive device, wherein the interface device (10) comprises a processor means (13), a memory means (14), a first connecting device (12) for interfacing the host device with the interface device, and a second connecting device (15) for interfacing the interface device (10) with the data transmit/receive device. The interface device (10) is configured by the processor means (13) and the memory means (14) in such a way that, when receiving an inquiry from the host device via the first connecting device (12) as to the type of a device attached to the host device, regardless of the type of the data transmit/receive device, the interface device sends a signal to the host device via the first connecting device (12) which signals to the host device that it is communicating with an input/output device.

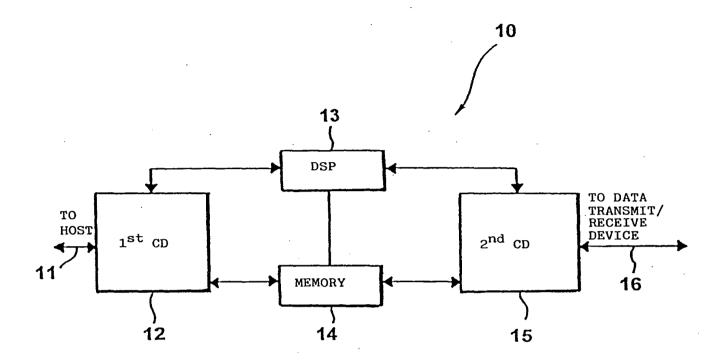


FIG.1

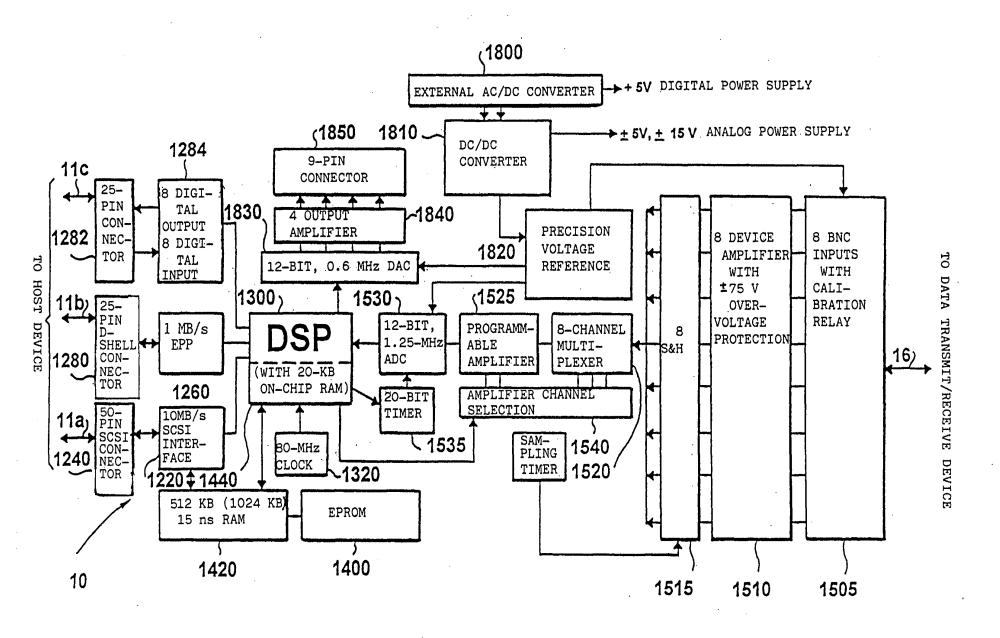


FIG.2

Declaration and Power of Attorney For Patent App's Pay Erklärung Für Patentanmeldungen Mit Vollmacht

German Language Declaration

Als nachstehend benannter Erfinder erkläre ich hiermit an Eides Statt:	As a below named inventor, I hereby declare that:
dass mein Wohnsitz, meine Postanschrift, und meine Staats- angehörigkeit den im Nachstehenden nach meinem Namen aufgeführten Angaben entsprechen,	My residence, post office address and citizenship are as stated below next to my name,
dass ich, nach bestem Wissen der ursprüngliche, erste und alleinige Erlinder (falls nachstehend nur ein Name angegeben ist) oder ein ursprünglicher, erster und Miterfinder (falls nachstehend mehrere Namen aufgeführt sind) des Gegenstandes bin, für den dieser Antrag gestellt wird und für den ein Patent heanmagt wird (2. die Erfindung mit dem Titel:	I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patentis sought on the invention entitled
	Flexible Interface
deren Beschreibung	the specification of which
(zutreffendes ankreuzen)	(check one)
☐ hier beigefügt ist.	is attached hereto.
unter der	was filed onas
Anmeldungsseriennummer	Application Serial No.
eingereicht wurde und amabgeändert).	and was amended on(if applicable)
Ich bestätige hiermit, dass ich den Inhalt der obigen Paten- tanmeldung einschliesslich der Ansprüche durchgesehen und verstanden habe, die eventuell durch einen Zusatzantrag wie oben erwähnt abgeändert wurde.	I hereby state that I have reviewed and understand the con- tents of the above identified specification, including the daims, as amended by any amendment referred to above.
Ich erkenne meine Pflicht zur Offenbarung irgendwelcher Informationen, die für die Prüfung der vorliegenden Anmeldung in Einklang mit Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) von Wichtigkeit sind, an.	I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).
Ich beanspruche hiermit ausländische Prioritätsvorteile ge- mäss Abschnitt 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 119 aller unten angegebenen Ausland- sanmeldungen für ein Patent oder eine Erfindersurkunde, und habe auch alle Auslandsanmeldungen für ein Patent oder eine Erfindersurkunde nachstehend gekennzeichnet, die ein Anmeldedatum haben, das vor dem Anmeldedatum der An- meldung liegt, für die Priorität beansprucht wird.	I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:
Page	1 of 3

Form PTO-FB-240 (8-83)

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German Language Declarzion

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Prior loreign application Priorität beansprucht	ons	·	Priority Cl.	aimed
19708755.8 (Number) (Nummer)	Germany (Country) (Land)	04/3/97 (March 4, 1997) (Day/MonttVYear Fled) (Tag/Monal/Janr eingereicht)	Yes Ja	Na Nein
PCT/EP98/01187 (Number) (Nummer)	Germany (Country) (Land)	03/03/98 (March 3, 1998) (Day/Month/Year Filed) (Tag/Monat/Jahr eingereicht)	X Yes Ja	No Nein
(Number) (Nummer)	(Country) (Land)	(Day/MonitvYear Filed) (Tag/Monat/Jahr eingereicht)	Yes Ja	No Nein

Ich beanspruche hiemit gemäss Absatz 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 120, den Vorzug
aller unten aufgeführen Anmeldungen und Ialls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in einer
früheren amerikanischen Patentanmeldung laut dem ersten
Paragraphen des Absatzes 35 der Zivilprozessordnung der
Vereinigten Staaten, Paragraph 112 offenbart ist, erkenne
ich gemäss Absatz 37, Bundesgesetzbuch, Paragraph 1,5ō(a)
meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung und dem
nationalen oder PCT internationalen Anmeldedatum dieser
Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35. United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35. United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37. Code of Federal Regulations. §1.55(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.) (Anmeldeseriennummer)	(Filing Date) (Anmeldedatum)	(Status) (patentiert, anhängig, aufgegeben)	(Status) (patented, pending, abandoned)
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(Application Senal No.) (Anmeldesenennummer)	(Filing Date) (Anmeldedatum)	(Status) (patentiert, anhängig, aulgegeben)	(Status) (patented, pending, abandoned)

Ich erkläre hiemit, dass alle von mir in der vorliegenden Erklärung gemachten Angaben nach meinem besten Wissen und Gewissen der vollen Wahrheit entsprechen, und dass ich diese eidesstattliche Erklärung in Kenntnis dessen abgebe, dass wissentlich und vorsätzlich lalsche Angaben gemäss Paragraph 1001, Absatz 18 der Zivilprozessordnung der Vereinigten Staaten von Amerika mit Geldstrafe belegt und/oder Gelängnis bestraft werden koennen, und dass derartig wissentlich und vorsätzlich falsche Angaben die Gültigkeit der vorliegenden Patentanmeldung oder eines darauf erteilten Patentes gefährden können.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by line or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Page 2 of 3

German Language Declaration



VERTRETUNGSVOLLMACHT: Als benannter Erlinder beauftrage ich hiermit den nachstehend benannten Patentanwälte) und/ oder die nachstehend benannten Patentanwälte) und/ oder Patent-Agenten mit der Verlolgung der vorliegenden Patentanmeldung sowie mit der Abwicklung aller damit verbundenen Geschäfte vor dem Patent-und Warenzeichenamt: (Name und Registrationsnummer anführen)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attomey(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

Donald M. Duft	17,484	William P. Wilbar	43,265
James M. Graziano	28,300	Thomas Swenson	36,696
Carl A. Forest	28,494	Curtis A. Vock	38,356
Dan Cleveland, Jr.	36,106	Kirk D. Williams	42,229
Michael J. Setter	37,936	Steven W. Weinrieb	26,520

Teleiongespräche bille richten an: (Name und Teleionnummer)

Direct Telephone Calls to: (name and telephone number)

Postanschrift:

Send Correspondence to:

Carl A. Forest, Ph.D.

c/o DUFT, GRAZIANO & FOREST, P.C.

1790-30th Street

- Suite 140 -

Boulder, Colorado 80301-1018, U.S.A.

<u> </u>	· · · · · · · · · · · · · · · · · · ·
Voller Name des anzigen oder ursprünglichen Erlinders:	Full name of sole or first inventor
	Michael TASLER
Untérschrift des Erlinders Datum	Laurana de manuero
±	richael Cusle April 27, 1999
Wohnsitz	Residence
	Würzburg, Germany
Staatsangehöngkeit	Citizenship
, -	German
Postanschrift	Post Office Address
•	Cronthalstraße 6c
•	D-97074 Würzburg, Germany
Voller Name des zweilen Milorlinders (laus zutrellend)	Full name of second joint inventor, if any
Unterschrift des Erlinders Datum	Second Inventor's signeture Date
Wahnsitz	Residence
Slaatsangehörigkeit	Citizenship
Postanschrift	Post Office Address

(Bitte entsprechende Informationen und Unterschriften im Falle von dritten und weiteren Miterlindem angeben).

(Supply similar information and signature for third and subsequent joint inventors.)

Page 3 of 3

Form PTO-F8-240 (8-83)

Patent and Trademark Office-U.S. DEPARTMENT OF COMMERC



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Michael Tasler, et al.

Docket No. SCHO0102D

Serial No.: 10/219,105

Group Art Unit: Unassigned

Filed: 08/15/2002

Examiner: Unassigned

Title: Flexible Interface for Communication Between a Host and an

Analog I/O Device Connected to the Interface...

Revocation of Prior and Grant of New Power of Attorney

As Assignee of record of the entire interest of the above-identified patents, all powers of attorney previously given are hereby revoked and the following attorneys and agents are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office connected therewith.

MICHAEL A. GLENN, Reg. No. 30,176 DONALD M. HENDRICKS, Reg. No. 40,355 KIRK WONG, Reg. No. 43,284 CHRISTOPHER PEIL, Reg. No. 45,005 JULIA THOMAS, Reg. No. 52,283

Please send all correspondence for this application as follows:

GLENN PATENT GROUP
3475 Edison Way, Suite L
Menlo Park, CA 94025
Please direct any calls to 650-474-8400.

Please change the Attorney Docket No. to SCHO0102D.

In accordance with 37 CFR 3.73, the assignee hereby certifies that the evidentiary documents with respect to its ownership have been reviewed and that, to the best of assignee's knowledge and belief, title is in the assignee seeking to take this action.

01/10/2003 Date

Name: Michael Tasler

Title:

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PATENT APPLI	CATION	SERIAL	NO.		

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE FEE RECORD SHEET

03/17/2005 DTESSEM1 00000058 071445 11078778

01 FC:2011 150.00 DA 02 FC:2111 250.00 DA 03 FC:2311 100.00 DA 04 FC:2203 180.00 DA

PTO-1556 (5/87)

PATENT APPLICATION FEE DETERMINATION RECORD

Effective December 8, 2004

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MAR 1 5 2006

WELSH & KATZ, LTD.

120 South Riverside Plaza 22nd Floor Chicago, Illinois 60606 Phone (312) 655-1500 Facsimile Number (312) 655-1501

FACSIMILE COVER SHEET

From:

Jeffrey W. Salmon, Esq.

Date: March 15, 2006

To:

Group 2181

Commissioner for Patents

United States Patent & Trademark Office

Washington, D.C. 20231

Fax:

(571) 273-8300

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Number of pages including this cover letter:

COMMENTS:

File No. 9576/96908, 09(10)

Items being filed via this facsimile are listed below:

O Transmittal Letter (1 Page)

Executed Revocation of Power of Attorney with New Power of Attorney, Change of Correspondence Address, and Statement Under 37 C.F.R. 3.73(b) (2 Pages)

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		TAL LETTER atent Pending)	RECEIVED CENTRAL FAX CENT	rizo	ocket No. 76/96910
In Re Application C	of: Michael Tasler		MAR 1 5 2006		
Application No. 11/078,778	Filing Date 03/11/2005	Examiner Harold J. Kim	Customer No. 24728	Group Art Unit 2181	Confirmation No.
Title: Flexible Int	erface	,	<u> </u>		
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A check in the Director as described	ne amount of is hereby authorized I below.	is attached. to charge and credit Do	eposit Account No.	23-0920	· ·
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'IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

U.S. Application Number:	11/078,778)
Filing Date:	Mar. 11, 2005)
First Named Inventor:	Michael Tasler, Wurzburg (DE))
Title of Invention:	Flexible Interface)
Art Unit:	2181	, ,
Examiner Name:	Kim, Harold J	}
Attorney Docket Number:	9576/96910)

REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY, CHANGE OF CORRESPONDENCE ADDRESS, AND STATEMENT UNDER 37 C.F.R. 3.73(b)

- 1. Papst Licensing GmbH & Co. KG hereby revokes all previous powers of attorney given in the above-identified application.
- 2. Papst Licensing GmbH & Co. KG hereby appoints the Practitioners named below as its attorneys or agents to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

Name	Registration Number
Jeffrey W. Salmon	37,435
Richard L. Wood	22,839
Joseph R. Marcus	25,060
Gerald T. Shekleton	27,466
Edward P. Gamson, Ph.D.	29,381
Kathleen A. Rheintgen	34,044
Eric D. Cohen	38,110
John P. Christensen	34,137
Louise T. Walsh	45,195
Paul M. Vargo, Ph.D.	29,116
Richard J. Gurak	41,050
Daniel M. Gurfinkel	24,177

3. Please change the correspondence address for the above-identified patent to:

Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 120 S. Riverside Plaza, 22nd Floor Chicago IL, 60606 USA Telephone: (312) 655-1500 Facsimile: (312) 655-1501 Bmail: jwsalmon@welshkatz.com

- 4. Papst Licensing GmbH & Co. KG, a German Corporation, is the assignee of the entire right, title and interest the above-captioned patent by virtue of a chain of title from the inventor as set forth hereafter:
- (a) From: Michael Tasler to Labortechnik Tasler GmbH. The assignment document was recorded in the Patent and Trademark Office on July 23, 2001 at reel/frame no. 012023/0515.
- (b) From: Labortechnik Tasler GmbH to Papst Licensing GmbH & Co KG. A copy of this assignment is attached as Exhibit A hereto and, by separate filing, is being submitted to the U.S. Patent & Trademark Office for recordation.
- 5. I have reviewed all of the documents in the chain of title to this application and, to the best of my knowledge and belief, title is in Papst Licensing GmbH & Co. KG.
- 6. I aver that I am empowered to sign this document on behalf of Papst Licensing GmbH & Co. KG.
- 7. I hereby declare that all statements made herein of my own knowledge are true and that all statements made upon information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Papst Licensing GmbH & Co. KG

By: P. Zam

Title: PRESIDENT

Date: Mars 8 2016

ASSIGNMENT

WHEREAS, LTT Labortechnik Tasler GmbH, is a corporation of Germany, has a P.O. address of Friedrich-Bergius-Ring 15, 97076 Würzburg, Germany, ("Assignor") and is the owner of the entire right, title, and interest in and to: United States Patent No. 6,470,399 B1, United States Patent No. 6,895,449 B2, and United States Patent Application No. 11/078,778 (hereinafter "Patent Rights").

WHEREAS, Papst Licensing GmbH & Co. KG, a German Corporation, having its principal place of business at Bahnhofstrasse 33, 78112 St. Georgen, Germany ("Assignee"), is desirous of acquiring the entire interest in and to the Patent Rights.

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, and in consideration of the covenants and obligations hereinafter set forth to be well and truly performed, the parties hereby agree as follows.

- I. Assignor hereby, sells, assigns, and transfers to Assignee, its lawful successors and assigns, Assignor's entire right, title and interest in and to the Patent Rights. Assignee hereby shall take, acquire and hold such right, title and interest in and to the Patent Rights.
- 2. Assignor hereby furthermore sells, assigns, and transfers to Assignee all claims for past, present, and future infringement of the inventions covered by Patent Rights, including without limitation all rights to recover damages and the right to grant releases for past infringement of the Patent Rights.
- 3. Assignor hereby further covenants and agrees, to render such assistance to Assignee as may be necessary to perfect the title to Patent Rights in said Assignee, its successors and assigns, to enable Assignee to prosecute all divisional, continuation, reexamination, and reissue applications, and to enable Assignee to obtain and enforce proper patent protection for the Patent Rights.
- 4. Assignor hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would conflict with this assignment and sale.
- 5. Assignor further covenants that (i) Assignee will, upon its request, be provided promptly with all pertinent facts and documents relating to the Patent Rights as may be known and/or accessible to Assignor, (ii) Assignor shall testify as to the same in any interference or litigation related thereto, and (iii) Assignor shall promptly execute and deliver to Assignee or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain or enforce the Patent Rights and/or any U.S. patent rights evolving therefrom. Assignor shall use his best efforts to cooperate in good faith with Assignee.

LTT Labortschnik Tasler GmbH

By: Z: Scuel a. sh

Title: General Manager

Date: March & Loca

Papst Licensing GmbH & Co. KG

Title: PRESIDENT

Date: March. 8. 2006

Exhibit A

NO. 1073 P. 5

WELSH & KATZ LTD. 312 655 1502

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/06

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING

AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

I hereby certify that this paper is being deposited with the United States Postal Service as EXPRESS MAIL POST OFFICE TO ADDRESSEE service under 37 C.F.R. 1.10 on the date indicated below and is addressed to: Commissioner for Patents, Mail Stop – Fee Argendment, P.O. Box 1450, Alexandria, VA 22313-1450:

Date: March 28, 2006

Exp. Mail No. EV555557389US

Attorney

Docket No.: 0757/96910

PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450
Alexandria, Virginia 22313-0001

Dear Sir:

Please enter this preliminary amendment prior to examination of the above-captioned application.

03/29/2006 FMETEKI1 00000099 230920 - 11078778

01 FC:2202 -

1400.00 DA

Filed: 03/11/05

Date: March 28, 2006

Page - 2 -

IN THE TITLE:

Please amend the title to read as follows: Flexible Interface Analog Data Generating And Processing Device For Use With A Personal Computer.

Filed: 03/11/05

Date: March 28, 2006

Page -3

IN THE CLAIMS:

Please cancel claims 1-16 without prejudice as to the subject matter claimed therein, and please add the following claims 17-93:

17. (new) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals as to what type of device is operatively connected thereto, the analog data generating and processing device comprising:

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives;

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

an input/output port that is adapted to be operatively connected to the multipurpose interface of the personal computer, a response signal being automatically and without user intervention sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the

Filed: 03/11/05

Date: March 28, 2006

Page – 4 –

input/output port, the receipt and processing of the receipt signal by the personal computer causing it to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, user selected ones of the digitized sets of analog data can be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer by means of a driver that is associated with the personal computer.

- 18. (new) A combination comprising the analog data generating and processing device of claim 17 and a personal computer.
- 19. (new) The analog data generating and processing device of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (new) The analog data generating and processing device of claim 19, wherein the electromagnetic radiation received by the sensor is representative of an object that is physically separated from and can be located not in substantial proximity to the housing.
- 21. (new) The analog data generating and processing device of claim 20, wherein the electromagnetic radiation is generated by a medical device.
- 22. (new) The analog data generating and processing device of claim 21, wherein the medical device comprises a diagnostic radiological system.

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- 23. (new) The analog data generating and processing device of claim 20, wherein the sensor comprises an electronic measuring device.
- 24. (new) The analog data generating and processing device of claim 23, wherein the electronic measuring device comprises a multi-meter.
- 25. (new) The analog data generating and processing device of claim 20, wherein the driver is adapted for use with a mass storage device.
- 26. (new) The analog data generating and processing device of claim 25, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 27. (new) The analog data generating and processing device of claim 26, wherein the driver is adapted for use with a hard disk drive.
- 28. (new) The analog data generating and processing device of claim 20, wherein the driver is located in a memory of the personal computer.
- 29. (new) The analog data generating and processing device of claim 28, wherein the personal computer memory comprises a BIOS of the personal computer.
- 30. (new) The analog data generating and processing device of claim 20, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 31. (new) The analog data generating and processing device of claim 30, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having

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a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

- 32. (new) The analog data generating and processing device of claim 31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 33. (new) The analog data generating and processing device of claim 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (new) The analog data generating and processing device of claim 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (new) The analog data generating and processing device of claim 20, wherein the processor comprises a digital signal processor.
- 36. (new) The analog data generating and processing device of claim 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 37. (new) The analog data generating and processing device of claim 36, wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 38. (new) The analog data generating and processing device of claim 37, wherein at least one of the virtual files comprises a configuration file stored in the memory.

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39. (new) The analog data generating and processing device of claim 38, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

- 40. (new) The analog data generating and processing device of claim 39, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (new) The analog data generating and processing device of claim 20, wherein a wire based connection is used to operatively connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (new) The analog data generating and processing device of claim 20, wherein a second set of instructions are stored in the memory which are adapted to cause the response signals to be generated.
- 43. (new) The analog data generating and processing device of claim 20, wherein a third set of instructions are stored in the memory that allow user selected ones of the digitized sets of analog data to be transferred to a memory of the personal computer.
- 44. (new) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals as to what type of device is operatively connected thereto, the analog data generating and processing device comprising:

means for receiving analog wave signals that are generated by a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data;

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means for causing the digitized sets of analog data to be individually stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

means for receiving from the multi-purpose interface of the personal computer the periodic inquiry signals, and for automatically and without user intervention responding thereto by sending a signal to the multi-purpose interface that causes the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored; and

means for transferring user selected ones of the digitized sets of analog data to the personal computer by means of a driver that is associated with the personal computer.

- 45. (new) A combination comprising the analog data generating and processing device of claim 44 and a personal computer.
- 46. (new) The analog data generating and processing device of claim 44, wherein the analog wave signals comprise electromagnetic radiation.
- 47. (new) The analog data generating and processing device of claim 46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.
- 48. (new) The analog data generating and processing device of claim 47, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (new) The analog data generating and processing device of claim 48, wherein the medical device comprises a diagnostic radiological system.

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- 50. (new) The analog data generating and processing device of claim 47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (new) The analog data generating and processing device of claim 50, wherein the electronic measuring device comprises a multi-meter.
- 52. (new) The analog data generating and processing device of claim 47, wherein the driver is adapted for use with a mass storage device.
- 53. (new) The analog data generating and processing device of claim 52, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 54. (new) The analog data generating and processing device of claim 53, wherein the driver is adapted for use with a hard disk drive.
- 55. (new) The analog data generating and processing device of claim 47, wherein the driver is located in a memory of the personal computer.
- 56. (new) The analog data generating and processing device of claim 55, wherein the personal computer memory comprises a BIOS of the personal computer.
- 57. (new) The analog data generating and processing device of claim 47, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 58. (new) The analog data generating and processing device of claim 57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having

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a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

- 59. (new) The analog data generating and processing device of claim 58, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 60. (new) The analog data generating and processing device of claim 47, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 61. (new) The analog data generating and processing device of claim 47, wherein the means for receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.
- 62. (new) The analog data generating and processing device of claim 61, wherein the means for transferring comprises at least a portion of a digital signal processor.
- 63. (new) The analog data generating and processing device of claim 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (new) The analog data generating and processing device of claim 63 wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 65. (new) The analog data generating and processing device of claim 64, wherein at least one of the virtual files comprises a configuration file stored in the memory.

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66. (new) The analog data generating and processing device of claim 64, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

- 67. (new) The analog data generating and processing device of claim 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 68. (new) The analog data generating and processing device of claim 47, wherein a wire based connection is used to operatively connect the multi-purpose interface of the personal computer with the means for receiving from the multi-purpose interface.
- 69. (new) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals as to what type of device is operatively connected thereto, the analog data generating and processing device comprising:

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals originate from a source that is external to the analog data generating and processing device and that is not located in substantial proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

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a connecting device operatively connected to the processor and the memory, the connecting device being adapted to be operatively connected to the multi-purpose interface of the personal computer and to receive therefrom the periodic inquiry signals;

wherein a response signal is automatically and without user intervention sent to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one inquiry signal therefrom, receipt and processing of the response signal by the personal computer causing the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and when the processor and memory are operatively connected to the circuit, user selected ones of the digitized sets of analog data can be transferred to the personal computer by means of a driver that is associated with the personal computer.

- 70. (new) A combination comprising the analog data generating and processing device of claim 69 and a personal computer.
- 71. (new) The analog data generating and processing device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.
- 72. (new) The analog data generating and processing device of claim 71, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.

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- 73. (new) The analog data generating and processing device of claim 72, wherein the electromagnetic radiation is generated by a medical device.
- 74. (new) The analog data generating and processing device of claim 73, wherein the medical device comprises a diagnostic radiological system.
- 75. (new) The analog data generating and processing device of claim 72, wherein the sensor comprises an electronic measuring device.
- 76. (new) The analog data generating and processing device of claim 75, wherein the electronic measuring device comprises a multi-meter.
- 77. (new) The analog data generating and processing device of claim 72, wherein the driver is adapted for use with a mass storage device.
- 78. (new) The analog data generating and processing device of claim 77, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 79. (new) The analog data generating and processing device of claim 78, wherein the driver is adapted for use with a hard disk drive.
- 80. (new) The analog data generating and processing device of claim 72, wherein the driver is located in a memory of the personal computer.
- 81. (new) The analog data generating and processing device of claim 80, wherein the personal computer memory comprises a BIOS of the personal computer.
- 82. (new) The analog data generating and processing device of claim 72, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.

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- 83. (new) The analog data generating and processing device of claim 82, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 84. (new) The analog data generating and processing device of claim 83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 85. (new) The analog data generating and processing device of claim 72, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 86. (new) The analog data generating and processing device of claim 72, wherein the connecting device is adapted to be operatively connected to a SCSI interface of the personal computer.
- 87. (new) The analog data generating and processing device of claim 72, wherein the processor comprises a digital signal processor.
- 88. (new) The analog data generating and processing device of claim 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (new) The analog data generating and processing device of claim 88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.

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90. (new) The analog data generating and processing device of claim 89, wherein at least one of the virtual files comprises a configuration file stored in the memory.

- 91. (new) The analog data generating and processing device of claim 90, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 92. (new) The analog data generating and processing device of claim 91, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 93. (new) The analog data generating and processing device of claim 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

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REMARKS

Claims 1-16 have been cancelled without prejudice as to the subject matter claimed therein. New claims 17-93 are being submitted herewith for the Examiner's consideration.

An Information Disclosure statement is being filed herewith for the Examiner's consideration. It is respectfully submitted that the claims presented in this preliminary amendment are patentable over all of the prior art references included in the IDS, either taken along or in a purported combination, for a number of different reasons, including those that are discussed in greater detail hereinafter.

The Examiner is respectfully requested to review the following eight references in detail, all of which are listed in the IDS. Portions of each reference that one may argue allegedly are relevant to the subject matter of the currently pending claims, together with an identification of each reference, are presented hereinafter:

- Operating A Scanner Which Emulates A Disk Drive," is not prior art to any of the claims submitted herewith. The earliest US filing date of this patent (March 20, 1997) is sixteen days after the earliest effective filing date of the currently pending claims, which is the March 4, 1997 filing date of German application no. 197 08 755. The Examiner's confirmation of this is earnestly solicited.
- 2) US Patent No. 5,508,821 is entitled "Image Scanner And Image Forming Apparatus With An Interface For Connection With An External Computer."

 Column 4, lines 21-23 of this patent state that the "image scanner 20 emulates the file system of 'UNIX' as if it were a hard disc. Accordingly, the image scanner 20

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looks like the hard disc from the workstation 21 can be handled as a hard disk."

In the summary of the invention of this patent, it is stated that an "object" of the invention is to provide an "image scanner" that "requires no preparation of any new device driver."

- 3) US Patent No. 5,131,089 is entitled "Solid State disk Drive Emulation."

 The abstract of this patent states that the "system permits software written for use with floppy disks to be used with solid state memory devices such as RAM cards or ROM without modification of the software."
- 4) US Patent No. 4,642,759 is entitled "Bubble Memory Disk Emulation."
- A two page printout of text included with Windows 95 is submitted herewith concerning the "RAMDRIVE.SYS" command. This document states that this command allows a computer's RAM memory to simulate a hard disk drive.
- Figure 1 of US Patent No. 5,724,574 discloses a hardware arrangement that includes, for example, a high speed scanner 24, a local area network 10, an optical disk based document server 15, and a number of workstations 18.
- 7) An article entitled "Optical Server Uses Network Protocol For Plug-And-Play Integration" was published in 1993. Page two of this article states that "emulation of the magnetic file system with a WORM-specific file system in this manner has several distinct advantages. The principal advantage is that the WORM disk appears to applications and utilities as just another disk."
- 8) The manual for Polaroid's Digital Camera model no. PDC-2000 indicates

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that it was published in 1996. The Examiner is asked to assume, for the sake of argument, that this is the case. Applicant reserves the right to challenge this in all forums and proceedings other than the examination of this application.

Page 11 of the manual states that the "PDC-2000 camera is a Small Computer Systems Interface (SCSI) device," that one can "connect up to seven SCSI devices to your computer," and that the "PDC-2000 camera's SCSI ID is preset to 4 at the factor."

Page 83 of the manual states that to "transfer and work with pictures from the PDC-2000 camera on your PC, you use the PDC-2000 TWAIN driver . . ." or one can install "PDC-2000 Direct" software.

The currently pending claims clearly are supported by the specification as originally filed. As one example, all of the currently pending claims generally require that a sensor of some kind be adapted to be exposed to analog wave signals (*e.g.*, electromagnetic radiation) that originate at least in part from a source that is external to and not located in substantial proximity to the housing in which the sensor is contained. These claim features are supported, for example, by the "diagnostic radiology system" disclosed at page 1, paragraph 4, line 3 of the specification of the instant application.

An example of such a "diagnostic radiology system" is, for example, an x-ray machine, the x-rays being one example of the claimed "analog wave signals." As readily apparent to one of ordinary skill in the relevant art, typical x-ray machines include two housings — one in which an x-ray generator is mounted and a second one in which an x-ray transducer is mounted. The x-ray generator is physically separated from and not located in substantial proximity to the

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transducer so that, for example, a patient can position his or her leg between the generator and the transducer. The transducer creates a set of analog data that comprises an x-ray so that, for example, a user can determine whether the patient's leg is broken.

It should be noted that the scope of the currently pending claims *is not* limited to "diagnostic radiology systems" and or to systems that only produce "x-rays." In this regard, other "modes" of practicing the claimed invention include, for example, the CCD device of a camera that is exposed to ambient light, and that creates therefrom a set of analog data representative of a picture. Further "modes" of practicing the claimed invention include, for example, a camera having a CCD that is adapted to receive ambient light as well as light from the camera's flash, and that creates therefrom a set of analog data representative of a picture. Other types of "sensors" within the scope of the present invention include, for example, dictaphone transducers that change analog voice signals into analog vocal signals.

For the Examiner's information, the inclusion of the above-described subject matter in the currently pending claims is one reason that the Examiner should find these claims patentable over, for example, the prior art of record that discloses the use of document scanners (e.g., US Patent Nos. 5,508,821, 5,532,825 and 5,724,574). In contrast to the currently pending claims, the scanner references teach a light source that is located inside the scanner and that is located in substantial proximity to the CCD of the scanner. Such sensors *are not* adapted to process analog wave signals such as, for example, ambient light or other electromagnetic radiation that is present outside of the scanner housing or that is reflected off of or pass through objects that are not located in substantial proximity to the scanner. For this reason alone, the currently pending claims should be found to be patentable over the scanner references.

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A second example of how the currently pending claims clearly are supported by the originally filed specification is as follows. One aspect of the currently pending claims is that analog data can be generated, digitized and stored in memory *irrespective* of whether communication with a personal computer already has been initiated. Support for this claim element is found, for example, at the third full paragraph on page 11 of the specification, which states:

"As described above, a data buffer can be implemented in the memory means 14 to permit independence in terms of time of the data transmit/receive device attached to the second connecting device from the hose device attached to the first connecting device."

A still further aspect of the currently pending claims that is fully supported by the originally filed specification is as follows. All of the claims presented in this preliminary amendment generally require that the analog data generating and processing device (e.g., an x-ray machine or a digital camera having a flash) be operatively connected to a multi-purpose interface of a personal computer (by, for example, a wire-based connection), and further be automatically and without user intervention recognized by the personal computer as being a device that has digital data which is stored therein and which is selectively retrievable therefrom. Support for these claim features is found, for example, at page 7, lines 8-12 of the specification, which recite:

"The digital signal processor 13 receives this inquiry instruction via the first connecting device and generates a signal which is sent to the host device (not shown) again via the first connecting device 12 and the host line 11. This signal

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indicates to the host device that, for example, a hard disk drive is attached at the interface to which the INQUIRY instruction was sent."

It is respectfully submitted that no prior art reference of record, either taken alone or in a purported combination, teaches or suggests the combinations claimed in the currently pending claims for a number of different reasons. As one example, US Patent No. 5,508,821 does not teach or suggest, for example, the above-noted "automatic recognition" feature because, for example, the system disclosed therein is UNIX based. As readily apparent to one of ordinary skill in the relevant art, such UNIX based systems affirmatively require user intervention in order to operate and use the scanner disclosed in the '821 patent.

As a further example of the patentability of the currently pending claims, the camera disclosed in the Polaroid manual submitted (assuming, for argument's sake that it is prior art) cannot be automatically recognized without human intervention. In this regard, user intervention always is required because, for example, a user needs to make sure that the camera's SCSI identification number does not conflict with the ID number of any other device in a daisy chain of which the camera forms a part. For this reason alone, for example, the currently pending claims should be found to be patentable over the Polaroid camera manual (assuming for argument's sake that it is prior art).

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It is respectfully submitted that the new claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited. In this regard, the Examiner is respectfully requested to contact the undersigned attorney upon entry of this amendment.

Respectfully submitted,

Jeffrey W/Salmon Attorney for Applicant Registration No. 37,435

March 28, 2006 Welsh & Katz, Ltd. 120 South Riverside Plaza 22nd Floor Chicago, IL 60606 Telephone (312) 655-1500 Facsimile (312) 655-1501 03-29-06

PTO/SB/21,(09-04) Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE he Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. **Application Number** 11/078,778 TRADEN Filing Date 03/11/05 **TRANSMITTAL** First Named Inventor Michael Tasler **FORM** Art Unit 2181 **Examiner Name** Harold J. Kim (to be used for all correspondence after initial filing) Attorney Docket Number 0757/96910 Total Number of Pages in This Submission **ENCLOSURES** (Check all that apply) After Allowance Communication to TC Fee Transmittal Form Drawing(s) Appeal Communication to Board Licensing-related Papers Fee Attached of Appeals and Interferences Appeal Communication to TC Petition Amendment/Reply (Appeal Notice, Brief, Reply Brief) Petition to Convert to a After Final **Proprietary Information Provisional Application** Status Letter Affidavits/declaration(s) Power of Attorney, Revocation Change of Correspondence Address Other Enclosure(s) (please Identify Extension of Time Request below): Terminal Disclaimer Request for Refund Express Abandonment Request CD, Number of CD(s) Information Disclosure Statement Landscape Table on CD Certified Copy of Priority Remarks Document(s) Form PTO-1449 Response to Missing Parts/ Incomplete Application 34 Attached References Response to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name Welsh & Katz, Ltd. Signature Printed name W. Salmon

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•	Applicant claims small entity status. See 37 CFR 1.27

Signature

Name (Print/Type)

Ce	omplete If Known	
Application Number	11/078,778	
Filing Date	03/11/05	
First Named Inventor	Michael Tasler	
Examiner Name	Harold J. Kim	
Art Unit	2181	
Attorney Docket No.	0757/96910	

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FEE CALCULATION	(All the fees	below are du	e upon filing	or may be sub	ject to a surc	harge.)	
1. BASIC FILING, SE	ARCH, AND	EXAMINATIO	N FEES				
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	<u>s</u>	mall Entity		Small Entity		Small Entity	
Application Type	<u>Fee (\$)</u>	<u>Fee (\$)</u>	<u>Fee (\$)</u>	<u>Fee (\$)</u>	<u>Fee (\$)</u>	Fee (\$)	Fees Paid (\$)
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	,
2. EXCESS CLAIM FE	EES					<u>s</u>	mall Entity
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<u>Total Claims</u> 76 - 20 or Hi	Extra Cla	<u>ims Fee</u> x 25	(\$) <u>Fees Pa</u> = 1400	10 (2)		Fee (\$)	Fee Paid (\$)
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HP = highest number of indep	pendent claims p	aid for, if greater th	nan 3				
3. APPLICATION SIZ							
If the specification							
listings under 37 (or small entit	y) for each addi	tional 50
sheets or fraction					Air- Abrasa	Fac (\$)	Fee Paid (\$)
<u>Total Sheets</u> - 100 =	Extra Sheets	/50=		itional 50 or frac up to a whole nu		Fee (\$)	
4. OTHER FEE(S)					,		Fee Paid (\$)
Non-English Spec	rification \$	130 fee (no sn	nall entity disce	ount)			
Other (e.g., late fi							
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SUBMITTED BY	1 1	Λ //					

Date 28 Mar 4 2006 This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option E (USA)

(Attorney/Agent)

Registration No. 37,435



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING

PROCESSING AND DEVICE

FOR USE WITH A PERSONAL

COMPUTER (As Amended)

I hereby certify that this paper is being deposited with the United States Postal Service as EXPRESS MAIL POST OFFICE TO ADDRESSEE service under 37 C.F.R. 1.10 on the date indicated below and is addressed to: Commissioner for Patents, Mail Stop -Fee Angendment, P.O. Box 1450, Alexandria, VA 22313-1450:

Exp. Mail No. EV555557389US

Attorney

Docket No.: 0757/96910

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents **Box IDS- NON FEE** P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. Listed Documents are U.S. patents, foreign patents and/or published papers. Copies of all references other than US Patents are being submitted herewith.

It is submitted that no fees are due in connection with this Information Disclosure Statement because it is being submitted prior to the issuance of the first Office Action.

Documents for which the supplied date of publication lists the year of publication without the month were published sufficiently earlier than the effective U.S. filing date and any foreign priority date, so that the particular month of publication is not in issue. Pursuant to §609 of the MPEP, it is understood that the month of publication is not required when the particular month of publication is not in issue.

No inferences should be drawn that the attached list represents a comprehensive investigation, or that any material disclosed is equivalent to the subject invention. In addition, none of the documents that have publication dates prior to the priority date of the above application anticipate the invention in this application.

The cited document(s) disclose numerous specific features. There has been no attempt to list each and every feature disclosed by each document. The Examiner is requested to review the document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosures of the art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the instant patent application. The recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence.

Respectfully submitted,

W. Salmon, Esq. Registration No. 37,435

Dated: March 28, 2006

Enclosures: Form PTO-1449

WELSH & KATZ, LTD.

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Chicago, Illinois 60606

(312) 655-1500 Telephone:

Facsimile: (312) 655-1501 Form PTO-1449 (Rev. 8-88)

U.S. Department of Commerce Patent and Trademark Office

Attorney Docket No. Serial No. 0757/96910 11/078,778 **Applicant**

INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)

applicant.

Michael Tasler Filing Date Group No. 03/11/05 2181

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Form PTO-144	9 U.S. Departmer	nt of Commerce	Attorney Docket No.	Serial No.		
(Rev. 8-88)	Patent and Trademark Office		0757/96910	11/078,778		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

INFORMATION DISCLOSURE STATEMENT

ATTACHED REFERENCES

1 OF 3

WELSH & KATZ, LTD.

120 South Riverside Plaza 22nd Floor

Chicago, Illinois 60606

Telephone: (312) 655-1500 (312) 655-1501 Facsimile:

(11)	Publication	number:	53145535	Α
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(43)	Date	of	publication	of	application:	18	.12.78	
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		(43) Date of publication of application: 18.12.78			
(51) Int. Cl G06F 3/00 G06F 11/00					
(21) Application	number: 52060716	(71) Applicant	TOSHIBA CORP		
(22) Date of filing: 25.05.77		(72) Inventor:	SANO YOSHINOBU		
(54) UNIVERSAL INTERFACE			st one universal interface by rewriting		
(57) Abstract		freely the contents of the CPU memory or the program incorporated into RAM.			

PURPOSE: To ensure a simulation for many input/output COPYRIGHT: (C)1978, JPO&Japio

公開特許公報

昭53-145535

⑤ Int. Cl.²G 06 F 3/00

20特

G 06 F 11/00

識別記号

庁内整理番号 6711 -5B 6676 -5B **6**3公開 昭和53年(1978)12月18日

発明の数 1 審査請求 有

(全 4 頁)

ᡚユニパーサルインターフェイス

願 昭52-60716

②出 願 昭52(1977)5月25日

⑩発 明 者 佐野義信

東京都府中市東芝町1番地 東京 芝浦電気株式会社府中工場内

⑪出 願 人 東京芝浦電気株式会社

川崎市幸区堀川町72番地

個代 理 人 弁理士 鈴江武彦 外2名

明 細 電

1.発明の名称

ユニバーサルインターフェイス

2. 特許請求の範囲

3. 発明の詳細な説明

この発明は、プログラムを記憶したメモリ装 蟹の内容を書き変えることにより、任意の機器 をシュミレートできるようにしたユニバーサル インターフェイスに関する。

最近は電子計算機関係の各種のデバイスの開 発が大いに行なわれているが、それはハードゥ エアとソフトウェアの開発に分類される。そし て、開発を短期間で仕上げるためには、両者の 開発を同じに行なりととが必要である。しかし、 ソフトウエアはそのハードウエアが製作されな いかぎり、実機でデバッグできない。したがつ てハードウェアの開発遅れがソフトウェア開発 遅れに重なり、デバイス開発が大いに遅れてし まりととが多々生じる。そのために、デバイス をシュミレートできるインターフェイスを作成 して、上記の問題点を解決している例がある。 しかし、それらはそのデバイス固有のインター フェイスであり、デバイスが異なれば異なつた インターフエイスを必要とする。加えて、その 性質上、ハードウエアが岩成すれば、不用に近 いものとなつてしまり。

との発明は、上記従来の欠点を除去するため になされたもので、任意のデバイスがシュミレ

を作成する必要がなく、開発費用の減少と期間 - の短縮化を期するととのできるユニバーサルイ ンターフェイスを提供することを目的とする。 以下、との発明のユニバーサルインターフェ イスの実施例について図面に基づき説明する。 第1図はその一実施例の全体的構成を示すプロ ツク図である。との第1図における1はホスト コンピュータであつて、開発ソフトウェアを実 行する電子計算機である。またUIはユニバー サルインターフェイスを示すもので、との第1 図の実施例では、インターフェイス部2、コン トローラ部 3 および R A M (ランダム・アクセ ス・メモリ) 4 とから構成されている。 R A M にはプログラムが配憶されている。コントロー ラ部 3 は R A M の配像内容を費き変えることに よりホストコンピュータ1からの信号に対して. 希望する信号、データをホストコンピユータ1 に返すととができるようになつている。

また、インターフエイス部2はホストコンピ

3

いま、第2図において、ホストコンピュータ 1からのコントロールライン信号はコントロー ルラインFF6にセットされる。コントローラ 部3はこのコントロールラインFF6にセット された内容を読み込み、RAM(に内蔵された プログラムにしたがつて解訳する。これにより、

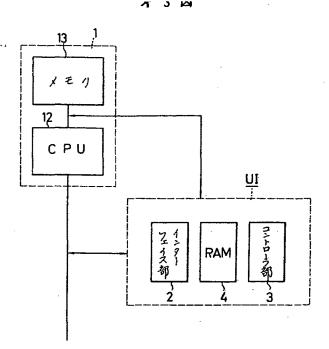
ホストコンピュータ1とコントローラ部3間 にはデータライン1を通して入出力パツファ回路8が接続されている。この入出力パツファ回路8はホストコンピュータ1からまたはコントローラ部3からのデータを一時保存するもので

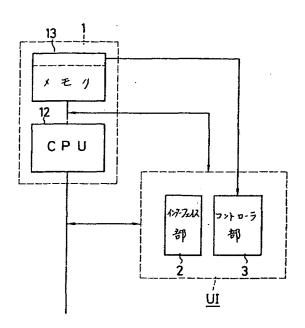
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コントローラ部 3 は入出力パッファ回路 8 との信号の授受を行なつたり、メモリパス 1 1 を使用して入出力の処理を行なつたり、あるいはホストコンピュータ 1 への信号の返避などの処理を行なり。

ととで、具体的を例を挙げて説明すると、たとれ、コントロールライン5の中のアドレスラインがアクティブになったとき、の内容が扱コントロールラインFF6の内容が扱コントロールラインFF6の内容が扱コントロールラインを知り、これがプロの路8の内容を読み込んで、これがブロールである。その結果、等しければ、SYNCを返すようなブログラムをRAM4に入れておく。

さて、第3図は第1図および第2図の実施例を要約して示したプロック図であり、この第3 図からも明らかなように、第1図および第2図の実施例では、ユニパーサルインターフェイス U I はインターフェイス部2、コントローラ部





(11) Publication number: 61034652 A

(43) Date of publication of application: 18.02.86

(51) Int. Cl G06F 13/12

(21) Application number: 59155321 (71) Applicant HITACHI LTD

(22) Date of filing: 27.07.84 (72) Inventor: TAGUCHI HISAO

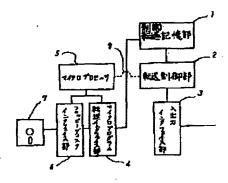
(54) ARTIFICIAL INPUT/OUTPUT CONTROL DEVICE

(57) Abstract:

PURPOSE: To obtain an artificial input/output device for simulating many kinds of input/output control devices without providing an exclusive circuit, so as to correspond to the input/output control device to be simulated, by replacing a microprogram by one set of artificial input/output control device.

CONSTITUTION: A microprogram corresponding to an input/output control device to be simulated is stored in advance in a floppy disk 7. By a microprocessor 5, the microprogram corresponding to the input/output control device to be simulated is stored in a control storage part 1. Subsequently, the program in the storage part 1 is read out and executed under the control of a transfer control part 2, and an interface operation as an artificial input/output control device is executed. Also, several kinds of interface operations can be executed continuously by controlling (8) the transfer control part 2 by the microprocessor 5.

COPYRIGHT: (C)1986,JPO&Japin



⑩ 公 開 特 許 公 報 (A) 昭61-34652

(5) Int Cl. 4

識別記号

庁内整理番号

❸公開 昭和61年(1986)2月18日

G 06 F 13/12

7165-5B

審査請求 未請求 発明の数 1 (全3頁)

擬似入出力制御装置

②特 願 昭59-155321

20出 願 昭59(1984)7月27日

70発 明 者 田

ター生

明夫

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砂代 理 人 弁理士 髙橋

外1名

明 細 書

1 発明の名称 擬似入出力制御装置

2 特許請求の範囲

- 1. データ処理システムと、入出力インタフェイスを介して接続され、入出力制御装置の入出力インタフェイス動作を提似し、制御部を、マイクロブログラム制御方式で構成した擬似、入出力装置において、マイクロブログラムを書き替えることにより、複数種類の入出力制御装置の動作を擬し、マイクロブログラムを書き替えることにより、複数種類の入出力制御装置の動作を擬しても手段を有することを特徴とする擬似入し出力制御装置。
- 3 発明の詳細な説明

[発明の利用分野]

本発明は、擬似入出力制御装置に関するものであり、特に、制御方式をマイクロブログラム方式とし、さらにマイクロブログラムを、書き替え可能な配億部に格納する事により、多種の入出力制御装置を擬似可能とした入出力制御装

1

置に関する。

[発明の背景]

従来の擬似入出力制御装置は、一般に、その制御を布線論理か、あるいは、読み出し専用記憶素子内に書き込まれたブログラムによる、マイクロブログラム制御で行っていた。そのため、数種の入出力制御装置を一装置で擬似するには、設切する装置の異なる機能対応に、論理を起むか、あるいは、投似する装置ごとに対応したマイクロブログラムを格納した読み出し専用記憶部を設ける必要がある。しかしこの方法では、銀似する装置対応に専用回路及び、専用部

- 品が増え経済性が悪い。
- (2) 装置の拡張性が悪い。拡張性を良くしようとすると、むだな空間を多く必要とする。 等の欠点がある。

〔発明の目的〕

本発明の目的は、1台の提似入出力制御装置でマイクロブログラムを入れ替えることにより、 提似する入出力制御装置対応に、専用回路を設

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る 擬似入 出力 装 置 を 提供する こと に ある。 〔 祭 明 の 紙 要 〕

本発明は、制御方式をマイクロブログラム方式とし、さらに、マイクロブログラムの格納部を、書き替え可能な配憶素子にて構成し、専用・回路を設けず、マイクロブログラムを変更するとにより、多種の入出力制御装置を換似可能としたことを特徴とするものである。

[発明の実施例]

第1 図に本発明の一実施例を示す。図において1は、転送制御用マイクロブログラムを格納.
する制御記憶部であり、2 は、1 の制御記憶部
内のマイクロブログラムにより、入出力インタフェイス動作の制御する転送制御にある。又、4
は1 の制御記憶部内のマイクロブログラムを、
は1 の制御記憶部内のマイクロブログラムを、
が2 よの書き替えを制御する書き替え制御部、
の1 は、 接似対応装置とに作成されたマイクロ

. 3 .

前述のように、書き替え可能な記憶素子により 構成されているため、その内容をクリアしたの ち、他のマイクロブログラムを格納することに より、複数の入出力制御装置の擬似が可能であ る。

さらに8の点線で示すように、5のマイクロブロセッサにより、2の転送制御部を制御することにより、数種のインタフェイス動作を連続的に、行う事が可能となり、データ処理装置のより送出される入出力装置の番地により、マイクロブログラムを自律的に決定し、書き込える機能を持させれば、被試験データ装理装置に対し、数種の入出力装置とのインタフェイス試験を自動的に行える。

〔発明の効果〕

本発明によれば、1 装置で、多数の入出力制 御装置を擬似する擬似入出力装置を、擬似する 入出力制御装置対応に、専用のハードウェアを 設けることなく実現できるため、経済性の良い 擬似入出力制御装置を提供できるとともに、新 イス部であり、7はその記憶媒体である。なお、本実施例では、5の書き替え制御部をマイクロ・ブロセッサ、7の記憶媒体をフロッピーディス・クで構成している。

1 の制御記憶部は、書き替え可能な記憶索子により構成されており、5 のマイクロブロセッ・サにより、7、6、4 の経路で、内部のプログ・ラムを書き替えることが可能となっている。

本級似入出力制御装置を動作するには、まず 準備作業として、7のフロッピーディスク内に、 級似する入出力制御装置に対応したマイクロブ ログラムを格納して動作させるには、まず 似公立 力制御装置として動作させるには、まず 似公立 力制御装置として動作させるには、アログラム た方法に対応したマイクロが出したマイクロが出した。 入出力制御装置に対応したマイクロがの制御により、 1の制御記憶部内のマイクロがの対しない、 1の制御記憶部内に、2の制御記憶部は、 インタフェイス動作を行う。1の制御記憶部は、

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たな入出力制御装置を擬似しようとする際も、 マイクロブログラムの変更により、最も少ない。 やの優により行えるために、拡張 性の優れた擬似入出力装置を提供できる。 さらに、本実施例のごとく、マイクロブロセッサに より、擬似入出力装置の動作を総括的に制御する。 とにより、容易に自動検査を提供できる。 となりうる擬似入出力制御装置を提供できる。

4 図面の簡単な説明

第1図は本発明の一実施例のプロック図であ ス

1 …制御記憶部、 2 … 転送制御部

3 … 入出力インタフェイス部

4…マイクロプログラム転送インタフェイス部

5 … マイクロブロセッサ

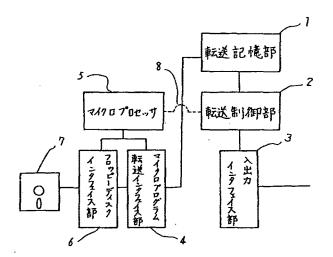
6 … フロッピーディスクインタフェイス部

7…フロッピーディスク

8 … 創御線。



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(54) DATA TRANSFER DEVICE

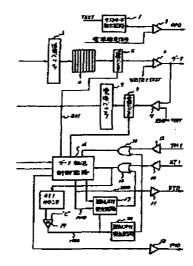
(57) Abstract:

PURPOSE: To facilitate the easy test of a data transfer device by using a means which separates the interface from a device at the remote side, a means which produces a false signal of the data transfer request signal sent from the device at the remote side and a means which receives by return the data to be sent to the device at the remote side.

CONSTITUTION: The OPO (operation-out) signal is first set at '0' to separate the interface at the side of a device of the remote side. A false STI generating circuit 17 is used for supply of a false STI (strobe-in) signal produced in its own device to the device of its own exactly in the same way as the actual STI signal sent from the device of the remote side. Thus the transmission data produced from its own device is received by return via a driver of a data bus and a receiver. At the same time, the data is produced in response to reception of the STI signal and supplied again to the self device. Thus the transfer of data is continued via a returned data bus, and a false TMI signal is produced when the data transferred reaches a prescribed quantity. Then the test is given to the end

of the transfer of data against reception of the TMI signal.

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❷発明の名称 デ

データ転送装置

②特 願 昭59-182116

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明 細 鸖

1 発明の名称

データ転送装置

2. 特許請求の範囲

相手装置との間でデータ転送要求信号と受付信号とにより非応答確認形式で送受しながらデータ 転送を行なりデータ転送装置において、

相手装置とのインタフェースを切離すための第 1の手段と、

相手装置からの前記データ転送要求信号の疑似 信号を当該装置内で発生する第2の手段と、

前記データ転送要求信号により相手装置へ送出するデータを折返し受信する第3の手段とを設け、

前記第1の手段により相手装置とのインタフェースを切り離し前記第2および第3の手段により 当該装置の試験を行なえるようにしたことを特徴 とするデータ転送装置。

2. 発明の詳細な説明

(技術分野)

本発明はデータ転送装置、とくに、情報処理システム中で用いられる非応答確認方式のインタフェースをもつデータ転送装置に関するものである。 (従来技術)

情報処理システム中で用られるデータ転送装置を試験する場合には一般に、データ転送を行なり相手側装置と接続し、または試験器等を接続してインタフェースの試験を行なっている。

しかし、相手側装置に接続してインタフェース の試験を行ない障害が検出された場合にはその障 害が相手相置側にあるのかまたは自己装置側にあ るのかの切り分けが困難である。

また試験器等を接続して試験を行なりためには システムを停止させ、電源を落し、ケーブルを接 続しなければならない。

この他に、データの折返機能を設け、データパ スの試験のみを行なうという方法もあるが、この 方法ではデータパスだけの動作はテストされるが、

- 1 -

I信号にかわるTMI信号(データ転送終了要求信号)の受信によって終了する。あるいはまた、自己装置からのSTO信号にかわるTMO信号(データ転送終了信号)によって終了させることもできる。

さて、第3図は本発明の一実施例を示すプロック図である。

本実施例は、テストモード指示回路1,0P0 ドライバ2,送信バッファ3,セレクタ4,送信 レジスタ5,データドライバ6,データレシーバ 7,受信レジスタ8,受信バッファ9,STIレ シーバ10,STOドライバ11,TMIレシー パ12,TMOドライバ13,ORゲート14, 15,データ転送制御回路16,疑似STI発生 回路17,STIカウンタ18,比較器19およ び疑似TMI発生回路20を含んでいる。

本実施例による自己装置側のインタフェースの テストは以下のように行をわれる。

まずテストモード指示回路1亿テストモードを

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出により何等影響を受けない。

さて、制御回路16は、STO信号の発生とと もにこれと同期して、ライン1601を介してデー タパスに対するデータ送出の制御を行なり。すな わち、送信すべきデータは、送信パッファ3に、 1ワード分(32ピット分)が格納されているが、 セレクタ4によって、まずこの中の最上位の8ピット分が選択され、これに対するパリティが付加 されたものが、送信レジスタ5にラッチされ、デ ータドライバ6を介してデータパス上に送出され る。

かくしてデータパスに送出された転送データは、 テストモード動作のために折返し受信がイネーブ ルされているデータレシーパ7を介して受信レジ スタ8にラッチされる。これはパリティチェック がOKの場合にはパリティピットを除く8ピット 分が、32ピット幅をもつ受信パッファ9の最上 位の8ピット分として格納される。

さて、制御回路16からライン1600を介して 最初のSTO信号の供給を受けた疑似STI発生 れを保持させる。

との結果、前述のように、相手装置側は、データバス、STI、STO、TMI、TMO等の各信号線を相手装置側のインタフェースから切離し、 とれらの信号線の自由使用を自己装置側に許すことになる。

そとで自己装置側においては、疑似STI発生 回路17に指令して、最初の疑似STI信号を発 生させる。

この疑似STI信号はORゲート15を介して 相手装置側からのSTI信号と全く同様にテータ 転送制御回路16に供給される。

この結果、回路16は実際のSTI信号を受けたのと全く同様に動作し、前記STI信号から一定の時間間隔内にSTO信号を発生しこれをライン1600に送出する。これはSTOドライバ11を介し正常のSTO信号として相手装置側に送出されるとともに、疑似STI発生回路17にも供給される。勿論相手装置側はこのSTO信号の送

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回路17は、この供給されたSTO信号を一定時間だけ遅らせることによって、次の疑似STI信号を生成し、前述のように、これをORゲート15を介して、制御回路16に供給する。

この結果、制御回路16は、実際の次のSTI 信号を受信したのと同様に動作して、ライン1600 を介して次のSTO信号を送出するとともに、ラ イン1601を介して、次の8ピット分のデータと それに対するパリティピットを送信レジスタ5に ラッチレ、データドライバ6を介してデータバス に送出する。

前述のように、とれはテストモードのためにイネーブルされているデータレシーパ7を介して折返され、受信レジスタ8にラッチされ、受信バッファ9の次の8ビット分として格納される。

以上のようにして、次次に疑似るTI信号が発生され、これは実際のSTI信号と全く同様にデータ転送制御回路16に加えられ、この結果、制御回路16は、実際の場合と全く同様にSTO信号を生成し、また転送データの送出を行なり。送

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イネーブルされているデータレシーパ 7 を介して、 折返され、かくして送信パッファ 3 から送出されたワードデータは、受信パッファ 9 に折返され、 上位装置で両者を比較することによりデータパスのインタフェースがチェックされる。

オアゲート15を介して次次に入力される疑似 STI信号は、STIカウンタ18でカウントされており、このカウント出力は比較器19により 予め定めたカウント数 "C"と比較され両者が一致すると一致信号1900が出力される。この一致 信号は前記回路17に供給されて、回路17による次の疑似STI信号の生成を抑止するとともに、疑似TMI発生回路20に供給され、疑似STI信号にかわる疑似TMI信号を生成する。

こうして生成された疑似TMI信号は、ORゲート14を介して、実際の相手装置側からのTMI信号と全く同様にデータ転送制御回路16に供給され、これにより回路16のTMI信号受信に伴なうデータ転送終了動作がテストされる。

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はない。

例をは、上述の実施例においては、疑似STI 信号および疑似TMI信号の合成はそれぞれのレシーバの内側に設けたOBグートにより行なったが、これをそれぞれのレシーバの外側で行なりことによってこれらのレシーパをテストの対象内に入れることもできる。同様に疑似STI発生回路17のSTO信号入力をSTOドライバ11をテストの対象として含ませるようにすることもできる。

またデータバスに転送されるデータ形式は一例 を示したもので勿論とれに限定される必要はない。 (発明の効果)

以上のように本発明を用いると、相手装置側のインタフェースを切離して、相手装置と無関係に、しかもケーブル等の接続替や他の試験装置の接続を行なわずに、データパスのみならず制御信号を含む殆んどすべてのインタフェースを実際の使用状態でテストできる機能を有するデータ転送装置を提供できる。

信号を"0"とすることにより相手装置側のインタフェースを切離し、疑似STI発生回路17を用いて自己装置内で発生した疑似STI信号と全く同様で発生した疑似のSTI信号と全く同様で生した。
では、これに応答して自己装置に供給し、これに応答して「シークを関する」である。
では、これに応答して、まないでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいのでは、いいので

このようにして本実施例によると、相手装置に 頼ることなく、しかもケーブル等の接続替えを行 なわずに、データパスのみならず制御信号を含む 殆んどすべてのインタフェースを実際の使用状態 でテストできるデータ転送装置を提供できる。

なお、以上は本発明の一奥施例を示したもので

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これによりデータ転送装置の保守を容易にし、 信頼性の向上を達成できる。

4. 図面の簡単な説明

第1図は、非応答確認形式でデータ転送を行な りデータ転送方式の各種の信号を説明するための ブロック図、第2図は前記方式のデータ転送を説明するためのタイムチャートおよび第3図は本発 明の一実施例を示すブロック図である。

図において、1 ……テストモード指示回路、2 …… OPOドライバ、3 …… 送信バッファ、4 … …セレクタ、5 …… 送信レジスタ、6 …… データドライバ、7 …… データレシーバ、8 …… 受信レジスタ、9 …… 受信バッファ、10 …… STIレシーバ、11 …… STOドライバ、12 …… TMIレシーバ、13 …… TMOドライバ、14,15 …… ORゲート、16 …… データ転送制御回路、17 …… 疑似STI発生回路、18 …… STIカウンタ、19 ……比較器、20 …… 疑似TMI発生回路。

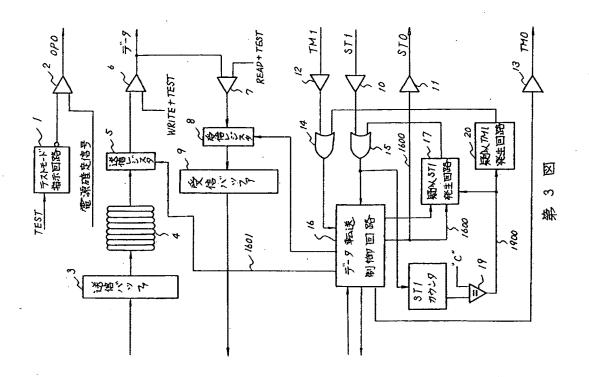
代理人 弁理士 内 原

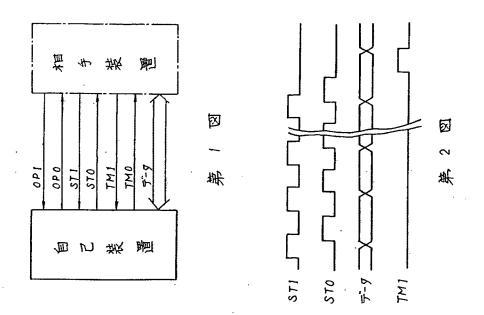
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(11) Publication number: 01303554 A

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(51) Int. Cl G06F 13/12

(21) Application number: 63133802 (71) Applicant: NEC CORP

(22) Date of filing: 31.05.88 (72) Inventor: HORIKAWA TAKASHI

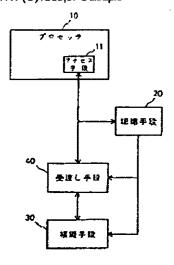
(54) INFORMATION PROCESSOR

(57) Abstract:

PURPOSE: To realize the application of an input/output device so far in applicable by setting a delivery means to transfer the requests given from a processor and the execution results of a simulation means between these simulation means and processor.

CONSTITUTION: The input/output accesses given to an input/output device from a processor 10 are recognized by a recognizing means 20 and gives an instruction to a delivery means 40 to store the access contents. At the same time, the means 20 starts a simulation means 30. The means 30 receives the access contents given from the processor 10 via the means 40 and simulates the action of the input/output device based on said access contents. In this case, the means 30 sometimes uses an existing similar input/output device to simulate the action of the input/output device. The result of this simulation of the means 30 is stored in the means 40 and received by the processor 10. Thus an access is possible even to such input/output device that has no control program.

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審査請求 未請求 請求項の数 1 (全4頁)

の発明の名称 情報処理装置

②特 願 昭63-133802

20出 願 昭63(1988)5月31日

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明 細 書

1.発明の名称

情報処理装置

2. 特許請求の範囲

(1) 入出力装置に対するアクセス手段を内蔵するプロセッサを持つ情報処理装置において、

特定の人出力装置に対するアクセスが行われて いることを認識する認識手段と、

入出力装置の動作を模擬する模擬手段と、

模擬手段とプロセッサとの間でプロセッサからの要求および模擬手段における実行結果をやりとりするための受渡し手段とを持たせたことを特徴とする入出力装置模擬機能を有する情報処理装置。3.発明の詳細な説明

〔産業上の利用分野〕

本発明は、入出力装置に対しプロセッサの入出 カアクセスにより制御を行う情報処理装置に関し、 特に入出力装置模擬機能を有する情報処理装置に 関する。

〔従来の技術〕

プロセッサから入出力装置をアクセスするためには、プロセッサの入出力でクセスにより入出力を選び解釈では、コマンドを送るか、入出力装置が解釈で置るチャネル・プログラムを作成して入出力装置を起動する必要がある。前者の方法は、主にパーソナル・コンピュータなどの小規模な計算機システムで採用されている方式である。

ところで、一般に、情報処理装置で複数の入出力装置を持つのが通常であるが、そのように複数の入出力装置を持つ場合、プロセッサの入出力装置を採用した情報処理技では、その入出力装置個々によって制御方法が異なる。このため、従来、この方式による情報処理装置では、個々の入出力装置毎に、制御している。

〔発明が解決しようとする課題〕

しかし、プロセッサの持つ入出力アクセスによ

装置では、上述のように入出力装置毎に制御プログラムを用意する必要があるため、制御プログラムが用意されていない入出力装置に対してはこれを使用することができない。

すなわち、制御方法が少しでも異なると、別個に制御プログラムは用意しなければならないのであり、入出力装置の動作が類似していても置い動作が既設の入出力装置のそれと類似していたと変動作が既設の入出力装置のそれと類似していたななりできる。このため、情報処理装置に入出力装置を制御するにようとを流用することができず、別個に制御プログラムを用意しなければならなかった。

本発明の目的は、情報処理装置が持っている制御プログラムにより類似の入出力装置を制御することを可能とし、もって、制御プログラムが用意されていない入出力装置に対してもアクセスを行うことが可能な情報処理装置を提供することにあ

もある。模擬手段により模擬された結果は受渡し 手段に格納され、プロセッサがこれを受け取る。

〔実施例〕

次に、本発明について図面を参照して説明する。 第1図は本発明の基本構成を示す機能プロック 図である。

本発明に従う情報処理装置は、プロセッサ10の他、認識手段20、模擬手段30及び受渡し手段40を備えている。

プロセッサ10は、入出力装置に対するアクセス 手段11を内蔵するプロセッサであり、また、認識 手段20は、特定の入出力装置に対するアクセスが 行われていることを認識する手段である。

模擬手段30は、入出力装置の動作を模擬する手段であり、受渡し手段40は、模擬手段30とプロセッサ10との間でプロセッサ10からの要求および模擬手段30における実行結果をやりとりするための手段である。

このように、入出力装置に対するアクセス手段 11を内蔵するプロセッサ10を持つ情報処理装置に

(課題を解決するための手段)

本発明は、入出力装置に対するアクセス手段を 内蔵するプロセッサを持つ情報処理装置において、 特定の入出力装置に対するアクセスが行われて いることを認識する認識手段と、

入出力装置の動作を模擬する模擬手段と、

模擬手段とプロセッサとの間でプロセッサからの要求および模擬手段における実行結果をやりと りするための受渡し手段とを持たせたことを特徴 としている。

(作用)

プロセッサが入出力装置に対し入出力アクセスを行うと、認識手段がこれを認識し受渡し手段にアクセスの内容を格納する様に指示するとともに、模擬手段を起動する。模擬手段では、受渡し手段よりプロセッサからのアクセス内容を受け取り、この内容にもとづいて入出力装置の動作を模擬することを置を使用して入出力装置の動作を模擬すること

おいて、特定の入出力装置に対するアクセスが行われていることを認識する認識手段20と、入出力装置の動作を模擬する模擬手段30と、模擬手段30とプロセッサ10からの要求および模擬手段30における実行結果をやりとりするための受渡し手段40を持たせることにより、入出力装置の動作を模擬する。

第2図は本発明の一実施例を示すプロック図で ある。

本実施例は、通常の入出力装置が2つの場合の例を示している。第2図においてプロセッサ10内部のアクセス手段11は既述したように入出力装置に対し入出力アクセスを行う部分である。また、プロセッサ10は受渡し手段40からウェイト信号S1を受け付る。この信号がアクティブの場合、プロセッサ10は入出力アクセスの完了をウェイト信号S1がインアクティブになるまで保留する。

この情報処理装置では、通常の入出力装置50、 51に対するアクセスは、アクセス手段11に接続されたバスを通して行われる。バスにはアドレス・ バス111 とデータ・バス112 がある。フロセッサ 10が入出力装置に対してアクセスを行うと、アク セス手段11はアドレス・バス111 に、この入出力 装置のアドレスを出力する。このアドレスにより アクセス対象である入出力装置が区別される。

入出力装置の動作を模擬する模擬手段30は通常 入出力装置に類似した入出力装置を持つ。類似入 出力装置60は、情報処理装置が持つ制御プログラ ムの対象となっている装置に類似した装置である が、直接対象となっていないため、従来の手法で は制御プログラムで扱えない装置である。

特定の入出力装置に対するアクセスが行われていること、すなわち後述のように目的とする入出力装置に対しプロセッサ10がアクセスしていることを認識する認識手段20、および模擬手段30とプロセッサ10との間でプロセッサ10からの要求、模擬手段30における実行結果をやりとりするための受渡し手段40もバスに接続される。

入出力装置の動作の模擬は、次のようにしてな される。

して、模擬手段30では、受渡し手段40よりプロセッサ10からのアクセス内容を受け取り、この内容にもとづいて人出力装置の動作を模擬する。

すなわち、まず、受渡し手段40では上記の情報を取り込むとともに、プロセッサ10のアクセスが入力アクセスの場合には、模擬手段30から模擬結果を渡されるまでウェイト信号S1をアクティブにしてプロセッサ10をウェイトさせる。

一方、模擬手段30が認識手段20により起動されると、上記の受渡し手段40よりアドレス・バス111とデータ・バス112の値およびプロセッサ10が行っているのが入力アクセスであるか出力アクセスであるのかの区別を受け取って、プロセッサ10からの発行された入出力アクセスを解釈する。模擬手段30では、この解釈結果に従い自分の制御する類似入出力装置を動作させる。

プロセッサ10のアクセスが出力アクセスの場合、 模擬手段30はプロセッサ10の出力するアドレスお よびデータを受け取り、これを解釈することによ りプロセッサ10が行う入出力装置の制御コマンド グラムにより類似の入出力装置を制御することを可能にするため、目的とする入出力装置に対しプロセッサ10がアクセスしていることを認識手段20で認識し、模擬手段30は入出力装置の動作を模擬し、受渡し手段40では、模擬手段30とプロセッサ10との間でプロセッサ10からの要求および模擬手段30における実行結果をやりとりする。

以下、具体的に説明すると、認識手段20はアドレス・バス111 を常に監視しており、模擬対象である入出力装置のアドレスがアドレス・バス111 に出力されると、受渡し手段40に対しアドレス・バス111 とデータ・バス112 の値およびプロセッサ10が行っているのが入力アクセスであるか出力アクセスであるのかの区別を取り込むように指示するとともに、模擬手段30を起動する。

このようにプロセッサ10が入出力装置に対し入出力アクセスを行うと、認識手段20がこれを認識し受渡し手段40にアクセスの内容を格納する様に指示するとともに、模擬手段30を起動する。しか

を認識する。さらに模擬手段30では、この結果に 従って、類似入出力装置に対し同様の役割を果た すコマンドを発行する。

プロセッサ10のアクセスが入力アクセスの場合、 模擬手段30ではプロセッサ10が受け取るべきデー 夕を作成し、これを受渡し手段40に模擬結果とし て渡す。受渡し手段40では、模擬結果を受け取る と、データ・バス112 にこれを出力するとともに ウェイト信号S1をインアクティブにする。プロ セッサ10は、ウェイト信号S1がインアクティブ になった時点におけるデータ・バス112 の値すな わち模擬結果を入力アクセスの結果として受け取る。

以上の操作により、プロセッサ10は制御プログラムが用意されていない入出力装置に対しても、動作が類似する入出力装置の制御プログラムを使用してアクセスを行うことが可能になる。

従って、また、情報処理装置に入出力装置を追加しようとするときでも、従来のように同種の装置を制御する制御プログラムを流用することがで

ればならないということもない。

(発明の効果)

以上説明したように、本発明によれば、プロセッサの入出力アクセスにより入出力装置を制御する方式を採用した情報処理装置において、制御プログラムが用意されていない入出力装置に対しても、動作が類似する入出力装置の制御プログラムを使用してアクセスを行うことが可能になる。

これにより、従来は使用できなかった人出力装 置も使用することが可能になる。

4. 図面の簡単な説明

第1図は本発明の基本構成を示す図、

第2図は本発明の一実施例に係る情報処理装置 の構成を示す図である。

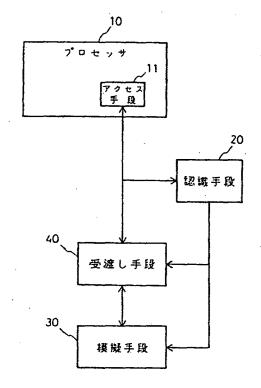
10…プロセッサ

11…アクセス手段

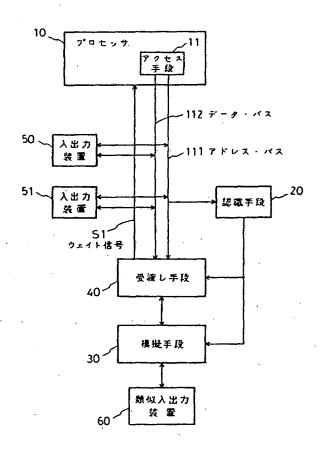
20…認識手段

30…模擬手段

40…受渡し手段



第1図



第 2 図

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(43) Date of publication of application: 27.11.89

(51) Int Cl

G05B 19/04 G06F 3/00

(21) Application number: 63124483

(22) Date of filing: 20.05.88

(71) Applicant:

KOYO ELECTRON IND CO LTD

(72) Inventor:

IKUTA KOJI

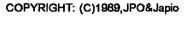
WATANABE YUICHI

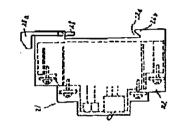
(54) PROGRAMMABLE CONTROLLER AND INPUT/OUTPUT INTERFACE

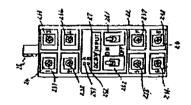
(57) Abstract:

PURPOSE: To attain the laborsaving of fitting by providing an input interface having a circuit to output a signal from input equipment to an output terminal when a signal to select it is given from a programmable controller main body.

CONSTITUTION: An input interface 21 having simulation signal generating means 261 and 262 to simulate a signal from input equipment and a circuit to output a signal from the input equipment to the output terminal when a signal to select it from a programmable controller main body is given is provided. When a simulation signal is generated from the input interface 21, this is inputted to the programmable controller main body. Thus, the simulation at the time of rising is executed. When the selecting signal from the programmable controller main body is received, the signal from the input equipment is inputted to the programmable controller main body and therefore, the control by it is executed. Thus, at the time of fitting, the drastical laborsaving can be executed.







(11) Publication number: 02114351 A

(43) Date of publication of application: 26.04.90

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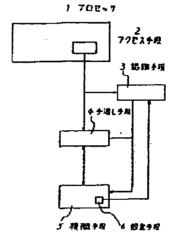
(54) INFORMATION PROCESSOR HAVING INPUT/OUTPUT DEVICE SIMULATING FUNCTION

(57) Abstract

PURPOSE: To periodically use an information processor whose using environment is drastically improved by changing an I/O device to be recognized without replacing a recognizing means constituted of a hardware.

CONSTITUTION: A simulating means 5 has a setting means 6 for the recognizing means 3. When the I/O device to be simulated is changed by the means 5, the means 6 is started to set up the means 3 to change the I/O device to be recognized and I/O access to the I/O device set up by the means 5 is recognized by a processor 1, so that the I/O device to be simulated can be changed. Consequently, I/O constitution can be changed during the operation of information processing and an information processor whose using environment is improved can be periodically used.

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会発明の名称

入出力装置模擬機能を有する情報処理装置

②特 昭63-268616

忽出 顧 昭63(1988)10月24日

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発明の名称

入出力装置模擬機能を有する情報処理装置

特許請求の範囲

入出力装置に対するアクセス手段を内蔵するプ ロセッサと、特定の入出力装置に対するアクセス が行なわれていることを認識する認識手段と、前 記入出力装置の動作を模擬する模擬手段と、該模 擬手段と前記プロセッサとの間でプロセッサから の要求および模擬手段における実行結果をやりと りするための受渡し手段とを持つ情報処理装置に おいて、前記模擬手段に認識手段に対する設定手 段を含むことにより、模擬対象入出力装置を変更 可能にしたことを特徴とする入出力装置模擬機能 を有する情報処理装置。

発明の詳細な説明

〔産業上の利用分野〕

本発明は、入出力装置模擬機能を有する情報処

理装置に関し、特に入出力装置に対しアロセッサ の入出力アクセスにより制御を行なう情報処理装 置に関する.

〔従来の技術〕

プロセッサから入出力装置をアクセスするため には、アロセッサの入出力アクセスにより入出力 装置にコマンドを送るか、入出力装置が解釈でき るチャネル・プログラムを作成して入出力装置を 起動する必要がある。前者の方法は、主にパーソ ナル・コンピュータなどの小規模な計算機シスム・ で採用されている方法であり、後者は汎用計算機 など比較的・大規模な計算機システムで採用され ている方式である。一般に、パーソナル・コンピ ュータでは、様々なオペレーティング・システム が提供されているが、同時刻においては単一のオ ペレーティング・システムしか動作しないため、 異なるオペレーティング・システムを使用するユ ーザはオペレーティング・システムを変更するた びにパーソナル・コンピュータを再起動する必要 があった。このような不便を解消するために、複

るマルチOSワークステーションが出現している。

一般に、オペレーティング・システムは単独で 動くことを前提に作られているため、入出力装置 を他のオペレーティング・システムと共用するこ とは考慮されていない。このため、マルチOSワ ークステーションにおいては、第2図に示すよう に、各オペレーティング・システム10が動作す るアロセッサ1が入出力装置に対して入出力アク セスを行なっていることをアクセス手段2を発行 する入出力装置アドレスにより認識する認識手段 3、ホスト・プロセッサ12上で動作しワークス テーション全体を管理するオペレーティング・シ ステム11に対して処理結果を通知するための受 渡し手段4、および、ホスト・オペレーティング システム11中に入出力装置の動作を模擬する棋 挺手段5を持たせることにより、各オペレーティ ング・システム10が入出力装置に対して発行す る入出力アクセスを模擬していた。模擬手段にお

するプロセッサと、特定の入出力装置に対するアクセスが行なわれていることを認識する模擬手段と、前記入出力装置の動作を模擬する模擬手段と、該模擬手段と前記プロセッサとの間でプロセッサとの間でプロセッサとのの要求および模擬手段における実行結果をやりとりするための受渡し手段とを持つ情報処理装置において、前記模擬手段に認識手段に対する設定手段を有している。

本発明は模擬手段で模擬対象入出力装置が変更されると設定手段を起動し、認識手段に対して認識する入出力装置を変更するように設定し、かつプロセッサから模擬手段より設定された入出力装置に対する入出力アクセスを認識することにより、模擬対象入出力装置の変更を可能にする。(実施例)

以下、本発明の実施例について図面を参照して説明する。

第2図は本発明の一実施例を示すブロック図である。第2図において、まず、本実施例により、 入出力装置の模擬を行なう方法について説明する なっている装置に類似した類似入出力装置13を 使用することもあり、また、このような入出力装 置を全く使用しないこともある。

(発明が解決しようとする課題)

〔課題を解決するための手段〕

本発明の入出力装置模擬機能を有する情報処理装置は、入出力装置に対するアクセス手段を内蔵

と、プロセッサ1内部のアクセス手段2は入出力 装置に対し入出力アクセスを行なう部分である。 また、本プロセッサ1はウェイト信号16を受け 付ける。この信号がアクティブの場合、プロセッ サ1は入出力アクセスの完了をウェイト信号16 がインアクティブになるまで保留する。

本情報処理装置では、通常の入出力装置に対するアクセスは、アクセス手段2に接続されたパスを通して行なわれる。パスにはアドレス・バス14とデータ・パス15がある。プロセッサ1が入出力装置に対してアクセスを行なうと、アクセス手段2はアドレス・バス14に、この入出力装置のアドレスを出力する。このアドレスによりアクセス対象である入出力装置が区別される。

認識手段3および受渡し手段4もバスに接続される。認識手段3はアドレス・バス14を常に監視しており、模擬対象である入出力装置のアドレスがアドレス・バス14に出力されると、認識手段出力25を有効にすることにより、受渡し手段4に対してアドレス・バス14とデータ・バス

。入力アクセスであるか出力アクセスであるのかの 区別を取り込むように指示するとともに、模擬手 段5を起動する。

受渡し手段1では上記の情報を取り込むとともに、プロセッサ1のアクセスが入力アクセスの場合には、模擬手段5から模擬結果を渡されるまでウェイト信号16をアクティブにしてプロセッサ1をウェイトさせる。

模擬手段与はシステム全体を管理するホスト・プロセッサ12に内蔵される。ここでは、プロセッサ1がアクセスを行なっている入出力装置の動作を模擬し、その結果を受渡し手段4を通じてプロセッサ1に反映させる。この模擬動作に際でしては、模擬対象入出力装置に類似した入出力装置を使用することもあり、また、全く使用しないこともある。

模擬手段5が認識手段3により起動されると、 受渡し手段4よりアドレス・バス14とデータ・ バス15の値およびプロセッサ1が行なっている

におけるデータ・バス15の値すなわち模擬結果 を入力アクセスの結果として受け取る。以上の操 作により、入出力装置の模擬動作を行なうことが 可能となる。

次に、模擬対象入出力装置を変更するときの操作を説明する。第3図は本発明の一実施例における認識手段3への設定方法を示すブロック図である。

認識手段3は、アドレス選択部20と判別部21から構成される。アドレス選択部20には、アクセス手段2から送られる入出力装置のアドレス・バス14と設定手段を改ってドレス・バス14と設定手続き、がある。判別部21へ送るアドレス・スを選択がある。20においては、書き込み作るアドレスを選択が無効となっている通常状態では、判別部21に対象が無効となっては、アドレスを選択とないを選択となったアドレスを選択とこれを選択となった。判別部21に対象では、アドレスで示される人出力装置がどうかを判別する。

の区別を受け取って、プロセッサ 1 からの発行された入出力アクセスを解釈する。模擬手段 5 では、この解釈結果に従い入出力装置の模擬を行なう。このとき必要であれば、自分の制御する類似 入出力装置 1 3 を動作させる。

アロセッサ1のアクセスが出力アクセスの場合、模擬手段5はアロセッサ1の出力するアドレスおよびデータを受け取り、これを解釈することによりアロセッサ1が行なう入出力装置の制御コマンドを認識する。さらに模擬手段5では、この結果に従って、類似入出力装置13に対し同様の役割を果すコマンドを発行する。

プロセッサ1のアクセスの場合、模擬手段5ではプロセッサ1が受け取るべきデータを作成し、これを受波し手段4に模擬結果として波す。受渡し手段4では、模擬手段を受け取ると、データ・バス15にこれを出力するとともにウェイト信号16をインアクティブにする。プロセッサ1は、ウェイト信号16がインアクティブになった時点

模擬対象入出力装置が変更されて新たに追加さ れると、設定手段6では変更された入出力装置の アドレスを設定アドレス・バス22、模擬対象で あることを示す値を設定データ・バス23に送出 し、書き込み指示24を有効にする。アドレス選 択部20では、書き込み指示24が有効になる と、設定アドレス・バス22から送られるアドレ スを判別部21に送る。判別部21では、費き込 み指示24が有効になると、アドレス選択部20 から送られるアドレスで示される入出力装置につ いての判別結果を設定データ・バス23で送られ る内容に設定する。判別部21では、この設定操 作以後に行なわれるアクセス手段2から当該入出 力装置に対する入出力アクセスに対しては、設定 操作により設定された値、すなわち、模擬対象で あることを示す値を出力する。この出力は、認識 手段出力25となり、受波し手段4および模擬手 段5を起動する。これにより、当該入出力装置を 模擬対象入出力装置に追加することが可能とな る.

以上の操作により、認識対象とする入出力装置 を追加または削除することが可能となる。

〔発明の効果〕

本発明は、入出力装置を模擬する情報処理装置において、ハードウェアで構成される認識手段を交換することなく認識対象入出力装置を変更することが可能となる。このため、情報処理装置の動作中に入出力装置構成の変更が可能となり、使用環境が格段に改善された情報処理装置を定期要す

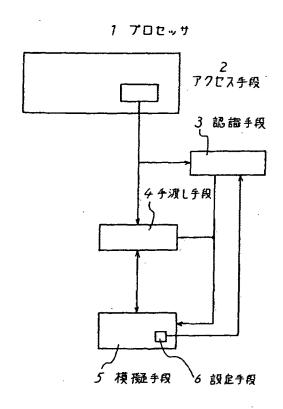
また、入出力装置の構成が異なる装置についても同一のハードウェアを使用することが可能になるため、ハードウェア設計コストおよび、入出力装置構成の変更による保守コストを大幅に削減することも可能になる。

図面の簡単な説明

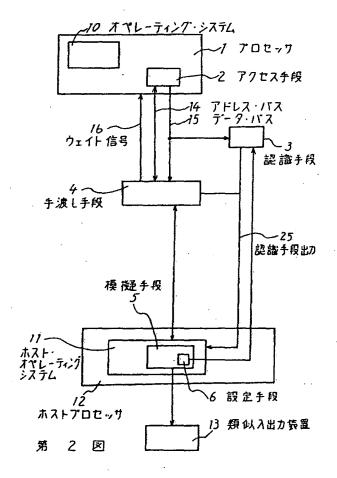
第1図は、本発明の一実施例における基本構成を示す図、第2図は、本発明の一実施例である情報処理装置の構成を示す図、第3図は、本発明の一実施例のける認識手段への設定方法を示すブロック図である。

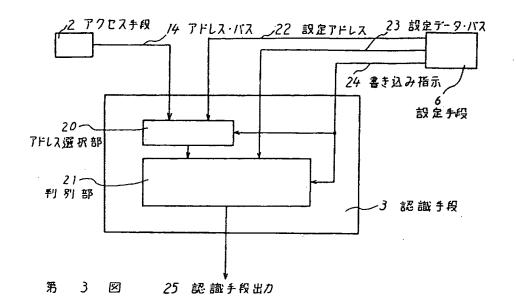
1 … プロセッサ、2 … アクセス手段、3 … 認識手段、4 … 受渡し手段、5 … 模擬手段、6 … 設定手段。

代理人 弁理士 内 原 智



第 1 図





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(54) INPUT/OUTPUT INTERFACE EXTENSION **CONTROL METHOD AND CHANNEL SIDE EXTENSION DEVICE AND INPUT/OUTPUT SIDE EXTENSION DEVICE THEREFOR**

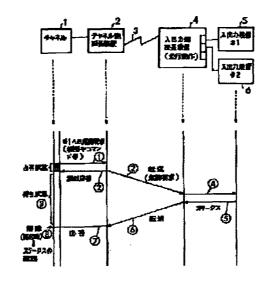
(57) Abstract:

PURPOSE: To improve channel working efficiency for plural input/output devices and line working efficiency for a chain command and to heighten the performance of the whole system.

CONSTITUTION: A channel side extension device 2 receiving a start-up request to a selection target input/output device #1 from a channel 1 sends a false response to the channel 1 without awaiting the status (information with respect to the execution of a command) of the device #1 with respect to the start-up request, and transfers the start-up request to an input/output side extension device #4. The channel 1 receiving the false response performs start-up processing for the device #1 by setting an awaiting state capable of issuing the start-up request to another input/output device, and canceling the state based on the status. The channel side extension device 2 sets the channel 1 in the awaiting state at a stage receiving data when the data is transferred after the start-up processing is performed. Also, the device 2 performs the advance execution of the chain read command by the input/output

side extension device 4, and the execution of the pre-fetch processing of the chain write command from the channel 1.

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最終頁に続く

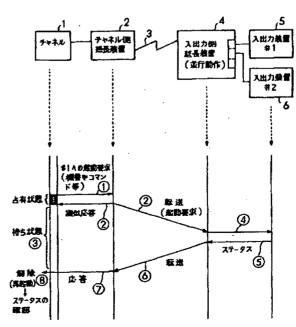
(54) 【発明の名称】 入出カインタフェース延長制御方法並びにそのためのチャネル側延長装置及び入出力側延長装置

(57)【要約】

【目的】 複数の入出力装置に対するチャネル使用効率 や連鎖コマンドに対する回線使用効率を向上させ、シス テム全体の性能を高めることを目的とする。

【構成】 選択対象入出力装置#1への起動要求をチャネル1から受け取ったチャネル側延長装置2は、この起動要求に関する#1のステータス(コマンドの実行に関する情報)を待たずに疑似応答をチャネル1に送り、かつ起動要求を入出力側延長装置4に転送する。疑似応答を受け取ったチャネル1は、#1に対する起動処理を、別の入出力装置への起動要求の発行が可能な形の待ち状態とし、前記ステータスに基づいてこれを解除する。起動処理後のデータ転送のときにもチャネル側延長装置2がこのデータを受け取った段階でチャネル1を待ち状態としている。また、連鎖リードコマンドの入出力側延長装置4での先行実行や、連鎖ライトコマンドのチャネル1からの先取り処理も実行している。

本発明の原理説明図(その1)



「テャネル1は、待ち状態の間、他の入出力差量6(き2)への 「起動要求債券を発行することができる

【特許請求の範囲】

【請求項1】 チャネルと入出力装置との間に、入出力インタフェース用のチャネル側延長装置および入出力側延長装置と、これら延長装置間を接続する回線とを設けた入出力インタフェース延長制御方法において、

前記入出力側延長装置として、前記入出力装置に対して並行動作する機能を備えたものを用い、

選択対象入出力装置への起動要求を前記チャネルから受け取った前記チャネル側延長装置は、前記起動要求に関する前記選択対象入出力装置の第1のステータスが転送されてくるのを待たずに第1の疑似応答を前記チャネルに送るとともに、前記起動要求を前記入出力側延長装置に転送し、

第1の疑似応答を受け取った前記チャネルは、前記選択 対象入出力装置に対する起動処理を、これとは別の前記 入出力装置への起動要求の発行が可能な形の待ち状態と し、また、第1のステータスに基づいて前記チャネル側 延長装置から送られる第1の応答で前記待ち状態を解除 するようにしたことを特徴とする入出力インタフェース 延長制御方法。

【請求項2】 前記選択対象入出力装置のビィジィを擬似的に示す信号を第1の疑似応答として用いたことを特徴とする請求項1記載の入出力インタフェース延長制御方法。

【請求項3】 前記起動処理終了後の、前記チャネルから前記選択対象入出力装置へのデータ転送に際し、

前記チャネル側延長装置は、前記チャネルからの前記データの受け取り終了に基づいて、前記データ転送に関する前記選択対象入出力装置の第2のステータスが転送されてくるのを待たずに第2の疑似応答を前記チャネルに送るとともに、前記入出力側延長装置へのデータ転送を行ない、

第2の疑似応答を受け取った前記チャネルは、前記選択対象入出力装置へのデータ転送処理を、これとは別の入出力装置へのデータ転送が実行できる形の仮終了状態とし、また、第2のステータスに基づいて前記チャネル側延長装置から送られる第2の応答で前記データ転送処理を終えることを特徴とする請求項1または2記載の入出力インタフェース延長制御方法。

【請求項4】 前記選択対象入出力装置におけるチャネルエンドを疑似的に示す信号を第2の疑似応答として用い、また前記選択対象入出力装置におけるデバイスエンドを示す信号を第2の応答として用いることを特徴とする請求項3記載の入出力インタフェース延長制御方法。

【請求項5】 チャネルと入出力装置との間に、入出力インタフェース用のチャネル側延長装置および入出力側延長装置と、これら延長装置間を接続する回線とを設けた入出力インタフェース延長制御方法において、

前記入出力側延長装置は、前記チャネル側から発行される連鎖指定の第1のリードコマンドのそれぞれを個々に

処理するとともにその総数を予測値として記憶しておき、続いて前記チャネル側から連鎖指定の第2のリードコマンドが発行されたときには、当該予測値に基づく所定数のリードコマンドを前記入出力装置に先行的に発行するとともにこれによって得られるリードデータを順次前記チャネル側延長装置に転送し、

前記チャネル側延長装置は、これらのリードデータを、 第2のリードコマンドの中の所定のものに順番に対応付 けた形で前記チャネルに転送するようにしたことを特徴 とする入出力インタフェース延長制御方法。

【請求項6】 前記チャネル側延長装置は、前記予測値よりも第2のリードコマンドの数N(Nは正の整数)の方が小さいために余分となった順番の前記リードデータを廃棄するとともにこのNの値を前記入出力側延長装置に通知し、

前記入出力側延長装置は、前記入出力装置をN番目の前記リードデータの読み出し直後の状態に設定するためのコマンドを発行することを特徴とする請求項5記載の入出力インタフェース延長制御方法。

【請求項7】 前記チャネル側延長装置は、前記予測値よりも第2のリードコマンドの数N(Nは正の整数)の方が大きいために前記リードデータの対応付けができない順番の当該リードコマンドを前記入出力側延長装置に発行し、

前記入出力側延長装置は、このリードコマンドを第1の リードコマンドのときのように個々に処理することを特 徴とする請求項5記載の入出力インタフェース延長制御 方法。

【請求項8】 前記入出力側延長装置は、前記予測値を、前記チャネル側延長装置から通知されるNに変更することを特徴とする請求項6または7記載の入出力インタフェース延長制御方法。

【請求項9】 チャネルと入出力装置との間に、入出力インタフェース用のチャネル側延長装置および入出力側延長装置と、これら延長装置間を接続する回線とを設けた入出力インタフェース延長制御方法において、

前記チャネル側延長装置は、自装置への第1のライトデータの転送処理についての擬似的な第1の終了ステータスを前記チャネルに通知した際に、ライトコマンド連鎖の指示がある場合には第1のライトデータの処理に関する前記入出力装置の応答を待たずに第2の終了ステータスを前記チャネルに通知して次の第2のライトコマンドおよび第2のライトデータの先取り処理へと移行し、また前記指示がない場合には前記応答に基づく第2の終了ステータスを前記チャネルに通知することを特徴とする入出力インタフェース延長制御方法。

【請求項10】 前記第1の終了ステータスとして前記 入出力装置のチャネルエンドを疑似的に示す信号を用 い、また前記第2の終了ステータスとして前記入出力装 置のデバイスエンドを示す信号を用いることを特徴とす る請求項9記載の入出力インタフェース延長制御方法。 【請求項11】 入出力装置への起動要求をチャネルから受け取って転送するチャネル側延長装置において、前記起動要求を受けた段階で、前記チャネルの選択対象入出力装置に対する起動処理を、これとは別の前記入出力装置への前記起動要求の発行が可能な形の、待ち状態にするための第1の疑似応答を前記チャネルに送り、前記起動要求に関する前記選択対象入出力装置の第1のステータスを受け取った段階で、前記待ち状態を解除するための、当該ステータスに基づく第1の応答を前記チャネルに送る機能を備えるようにしたことを特徴とする

【請求項12】 前記選択対象入出力装置のビィジィを 擬似的に示す信号を第1の疑似応答として用いる機能を 併せ持つことを特徴とする請求項11記載のチャネル側 延長装置。

チャネル側延長装置。

【請求項13】 前記起動処理終了後の前記選択対象入 出力装置へのデータ転送に際して、

前記チャネルからの前記データの受け取りが終了した段階で、前記選択対象入出力装置に対する前記データ転送処理を、これとは別の前記入出力装置へのデータ転送が可能な形の、仮終了状態とするための第2の疑似応答を前記チャネルに送り、

前記データ転送に関する前記選択対象入出力装置の第2 のステータスを受け取った段階で、前記データ転送処理 を終えるための、当該ステータスに基づく第2の応答を 前記チャネルに送る機能を併せ持つことを特徴とする請 求項11または12記載のチャネル側延長装置。

【請求項14】 前記選択対象入出力装置におけるチャネルエンドを疑似的に示す信号を第2の疑似応答として用い、また、前記選択対象入出力装置におけるデバイスエンドを示す信号を第2の応答として用いる機能を併せ持つことを特徴とする請求項13記載のチャネル側延長装置。

【請求項15】 チャネルからリードコマンドを取り込んで入出力側延長装置に発行するチャネル側延長装置に おいて

前記入出力側延長装置が、連鎖指定の第1のリードコマンドのそれぞれを個々に処理するとともにその総数を予測値として記憶し、続いて連鎖指定の第2のリードコマンドが発行された場合に、当該予測値に基づく所定数のリードコマンドを前記入出力装置に先行的に発行して得られるリードデータを転送してきたとき、当該リードデータを、第2のリードコマンドの中の所定のものに順番に対応付けた形で前記チャネルに転送する機能を備えるようにしたことを特徴とするチャネル側延長装置。

【請求項16】 前記予測値よりも第2のリードコマンドの数N(Nは正の整数)の方が小さいために余分となった順番の前記リードデータを廃棄するとともにこのNの値を前記入出力側延長装置に通知する機能を併せ持つ

ことを特徴とする請求項15記載のチャネル側延長装置。

【請求項17】 前記予測値よりも第2のリードコマンドの数N(Nは正の整数)の方が大きいために前記リードデータの対応付けができない順番の当該リードコマンドを前記入出力側延長装置に発行する機能を併せ持つことを特徴とする請求項15記載のチャネル側延長装置。 【請求項18】 チャネルからライトコマンドを取り込んで入出力側延長装置に発行するチャネル側延長装置において、

自装置への第1のライトデータの転送処理についての擬似的な第1の終了ステータスを前記チャネルに通知した際に、ライトコマンド連鎖の指示がある場合には第1のライトデータの処理に関する入出力装置の応答を待たずに第2の終了ステータスを前記チャネルに通知して次の第2のライトコマンドおよび第2のライトデータの先取り処理へと移行し、また前記指示がない場合には前記応答に基づく第2の終了ステータスを前記チャネルに通知する機能を備えるようにしたことを特徴とするチャネル側延長装置。

【請求項19】 前記第1の終了ステータスとして前記入出力装置のチャネルエンドを疑似的に示す信号を用い、また前記第2の終了ステータスとして前記入出力装置のデバイスエンドを示す信号を用いる機能を併せ持つことを特徴とする請求項18記載のチャネル側延長装置。

【請求項20】 チャネル側から発行されるリードコマンドを処理する入出力側延長装置において、

連鎖指定の第1のリードコマンドのそれぞれを個々に処理するとともにその総数を予測値として記憶しておき、続いて連鎖指定の第2のリードコマンドが発行されたときには、当該予測値に基づく所定数のリードコマンドを入出力装置に先行的に発行するとともにこれによって得られるリードデータのそれぞれを順次チャネル側延長装置に転送する機能を備えるようにしたことを特徴とする入出力側延長装置。

【請求項21】 前記予測値よりも第2のリードコマンドの数N(Nは正の整数)の方が小さいとき、前記入出力装置をN番目の前記リードデータの読み出し直後の状態に設定するためのコマンドを発行する機能を併せ持つことを特徴とする請求項20記載の入出力側延長装置。 【請求項22】 前記予測値よりも第2のリードコマンドの数N(Nは正の整数)の方が大きいとき、この多い分だけチャネル側延長装置から転送されるリードコマンドを第1のリードコマンドのときのように個々に処理する機能を併せ持つことを特徴とする請求項21記載の入出力側延長装置。

【請求項23】 前記予測値をNに変更する機能を併せ 持つことを特徴とする請求項21または22記載の入出 力側延長装置。

【発明の詳細な説明】

[0001]

【産業上の利用分野】本発明は、入出力インタフェース 延長制御方法並びにそのためのチャネル側延長装置及び 入出力側延長装置に関し、特にチャネルと入出力装置と の間に、入出力インタフェース用のチャネル側延長装置 および入出力側延長装置と、これら延長装置間を接続す る回線とを設けた通信システムにおいて、複数の入出力 装置に対する起動処理やその後のデータ転送処理、さら には連鎖指定されたリードコマンドやライトコマンドの 処理を効率的に行なうためのものである。

【0002】なお、本明細書で用いる「起動処理」とは 入出力装置の選択処理や、選択された入出力装置に対す るコマンド実行処理などのことであり、本発明が適用さ れる起動処理の単位としてはこれら両方の処理の場合に 加えて、個々の処理の場合もある。

【0003】一般に、複数の入出力装置をホストコンピュータから遠隔の地に高速回線を介して設置した通信システムにおいては、入出力装置に対する起動処理やデータ転送の際の回線上での時間遅延のため、ホストコンピュータのチャネルが選択対象入出力装置にその必要がないまま占有される時間が長くなりがちである。

【0004】また、連鎖指定されたリードコマンドやライトコマンドの各コマンドについても順次その終了ステータスを確認してから次のコマンドの処理に移行するため、1コマンド当たりの回線上での時間遅延が大きく、特に1コマンドのデータブロック長が小さい場合には回線使用効率が悪くなる。

【0005】このような状況において、チャネルのこの 占有時間の中の無駄な部分を他の入出力装置に対する起 動処理やデータ転送処理に用いてチャネルの利用効率を 高めることや、さらには連鎖指定されたコマンドをいわ ば先行処理する形で実行してスループットを高めること が要請され、本発明はこのような要請に応えるものであ る。

[0006]

【従来の技術】図14は、一般的な、入出力インタフェース延長制御システムの概要を示す説明図であり、41はホスト、42はチャネル(BMC:Block Multiplex Chanel)、43はチャネル側TAG/BUS、44はチャネル側延長装置(BMA:Block Multiplex Adapter)、45は回線、46は入出力側延長装置(IOA)、47は入出力側TAG/BUS、48,49は入出力装置をそれぞれ示している。以下、必要に応じてBMC42、BMA44、IOA46と記載する。

【0007】チャネル側延長装置44および入出力側延長装置46はチャネル42と複数の入出力装置48、49との間の送受信信号の入出力インタフェースとして動作し、

・チャネル側延長装置44は、例えば8本のバス線と複数 本のタグ線からなるチャネル側TAG/BUS43を介し てチャネル42から送られたパラレル信号をシリアル信号 に変換してこれを高速の回線45に送り出し、

・入出力側延長装置46は、これを元のパラレル信号に変換している。

【0008】図15は、選択対象入出力装置に対するチャネル(BMC)の起動処理、すなわち選択対象入出力装置の特定と選択した入出力装置への実行コマンドの指示についてのシーケンスを示す説明図であり、その内容は次のようになっている。

【0009】(111) BMC42は、選択対象入出力装置の 機番をBMA44に通知する。

(112) BMA44は、この機番を高速の回線45を経由して IOA46に転送する。

(113) IOA46は、この機番の入出力装置(IO#1) を選択する。

(114) IO#1は、自機番をIOA46に通知する。

(115) IOA46は、この通知内容を回線45を経由してB MA44に転送する。

(116) BMA44は、入出力装置の選択結果をBMC42に 通知する。

(117) BMC42は、IO#1に対するコマンドをBMA 44に通知する。

(118) BMA44は、このコマンドを回線45を経由して I OA46に転送する。

(119) IOA46は、このコマンドをIO#1に通知する。

(120) I O # 1 は、このコマンドの実行に関する応答を I O A 46に行なう。この応答に用いられるのは、コマンドの実行が正常終了したか、異常終了したかなどを示すステータスであり I O A 46についてのチャネルエンド (CE) やデバイスエンド (DE) などが含まれる。

(121) IOA46は、この応答内容を回線45を経由してBMA44に転送する。

(122) BMA44は、IO#1でのコマンドの実行に関する情報をBMC42に通知する。

【0010】このようにしてチャネルと選択対象入出力装置との間での起動処理が実行され、また、当該入出力装置における実行コマンドがその後のデータ処理をともなう、例えばライトコマンドなどであればチャネルからのデータ転送が続いて行われる。

【0011】一方、近年の回線技術の進歩などにともない入出力装置の遠隔設置化が進んで、例えばチャネル側延長装置と入出力側延長装置との間の回線が数100km におよぶこともある。

【0012】このような距離では1回の送信にともなう回線上での時間遅延は10ms~20msほどとなり、チャネルから入出力装置に対する処理の場合、それの開始から終了までに両者間で回線を介して順番に行われる送受信の回数だけ当該遅延時間を累積した値に対応の処理遅延が発生することになる。

【0013】従来、この処理遅延を少なくするため、

・チャネルから入出力装置に向けて発行された起動要求などに対し、チャネル側延長装置に相当する部分が入出力装置に代わるかたちで擬似応答または先行応答して、チャネルと入出力装置との間の送受信回数を減らすこと(特開昭62-299145号公報参照)

・チャネル側延長装置に相当する部分は、コマンド等を 入出力装置側に送出した段階で、これに対する応答が転 送されてくるのを待たずに次のコマンド等をチャネルか ら先読みして入出力装置側に送るようにすること(特開 平3-232042号公報参照)

などが提案されている。

【0014】また、連鎖指定されたリードコマンドやライトコマンドの処理に際し、チャネル側延長装置(BMA)44は、BMC42から送られる各コマンドを対応する入出力装置に転送してそこでの処理が終了した後の応答であるチャネルエンドやデバイスエンドを確認した上で次のコマンドを取り込むようにしている。

【0015】なお、連鎖指定されたコマンドが発行されるのはテープ装置やディスク装置などの連続した領域に対するリードやライトを順に実行する場合が一般的であり、連鎖したコマンドの個数はジョブ特性によって決まり同一ジョブ内では通常一定値である。

[0016]

【発明が解決しようとする課題】このように、入出力装置の遠隔設置化、すなわちチャネルと入出力装置とを接続する回線の長距離化にともなってチャネルからの起動要求やデータ転送に対する処理遅延が顕著になることを抑えるための各種手法が提案され、それぞれ所定の効果を得ている。

【0017】しかしながら、いずれの場合にも、チャネル自体は、選択対象入出力装置への機番、コマンドなどの処理要求の発行やデータ転送を開始してからそれについての終了ステータスを受け取るまでの間、当該入出力装置に占有されたままとなっており、この間にチャネルが他の入出力装置への処理要求を発行することなどはできない。

【0018】また、連鎖指定されたリードコマンドやライトコマンドの処理の際にも、コマンドごとにその実行と実行先からの終了ステータスの確認とを繰り返しているので、1コマンド当たりの回線上での時間遅延が大きく回線使用効率が悪いという問題点があった。このことは、1コマンドのデータブロック長が小さい場合に顕著となる。

【0019】そこで、本発明では、チャネルからの処理 要求やデータ転送を受けたチャネル側延長装置はこれに 対する選択対象入出力装置からの応答を待たずに擬似応 答をいったんチャネルに通知し、これを受けたチャネル は、この処理要求やデータ転送の処理を、これとは別の 入出力装置に対する当該処理が可能な形でのいわゆる待 ち状態とし、その後の選択対象入出力装置からの応答に 基づいてこの待ち状態を解除することにより、複数の入 出力装置に対するチャネルの使用効率を向上させ、入出 力装置全体の性能を高めることを目的とする。

【0020】さらには、連鎖指定されたリードコマンドやライトコマンドをいわば先行処理する形で実行する、例えば同一ジョブ内では連鎖リードコマンドの個数が通常一定であることに着目し、入出力側延長装置は、最初の連鎖リードコマンドの個数を記憶してその後の連鎖リードコマンド中の1番目のリードコマンドが発行されたことを確認した場合には、チャネル側からのその後のリードコマンドを待つことなしに、先に記憶した個数に基づく所定数のリードコマンドを自らが新たに発行することにより、連鎖コマンド全体の処理時間を短縮化してスループットを高めることを目的とする。

[0021]

【課題を解決するための手段】図1は本発明の原理説明図(その1)であり、1はチャネル、2はチャネル側延長装置、3は回線、4は入出力側延長装置、5および6は入出力装置をそれぞれ示し、入出力側延長装置4は各入出力装置に対し並行動作する機能を持っている。

【0022】チャネル1が任意の入出力装置(例えば‡1)の起動処理を行なうときのシーケンスは次のようになっている。

⊕チャネル1は、選択対象入出力装置の機番やコマンドなどの起動要求をチャネル側延長装置2に送出する。

②チャネル側延長装置2は、

- ・この起動要求を入出力側延長装置4に回線3を介して 転送し、
- ・擬似応答、例えば選択対象入出力装置が擬似的にBUSY である旨の信号をチャネル1に送出する。起動要求の内 容はチャネル側延長装置2に保持される。
- ③チャネル1は、この擬似応答に基づいて入出力装置5 (#1)に対する起動処理を待ち状態に設定する。この 待ち状態は、当該起動処理のためのチャネル側延長装置 2との結合をいったん解いて他の入出力装置に対する起 動処理が可能な形のものであり、入出力装置5からの本 来の応答で解除される。
- ④入出力側延長装置4は、起動要求に基づいて入出力装置5を選択しそこにコマンドを送出する。
- **⑤**入出力装置5は、コマンドの処理結果に関するステータスを入出力側延長装置4に送出する。
- ⑥入出力側延長装置4は、このステータスをチャネル側延長装置2に回線3を介して転送する。
- **の**チャネル側延長装置 2は、このステータスに基づいた 応答をチャネル1 に行なう。
- ❸チャネル1は、待ち状態を解除して①の再起動処理を 行ない、チャネル側延長装置2に保持されているコマン ドやステータスを確認する。

【0023】チャネル1は、この確認によって起動要求

時のコマンドがライトコマンドのようにその後のデータ 転送を必要とするもので、入出力装置5の応答が「ライト可能」であることを認識した場合にはデータ転送を開始する(図5参照)。

【0024】図2は本発明の原理説明図(その2)である。これは、任意の選択済の入出力装置5に発行される連鎖指定のリードコマンドに対する処理手順の概要であり、その内容は次のようになっている。

【0025】**①**′ チャネル1は、連鎖指定の第1のリードコマンドを発行する。

② 入出力側延長装置4は、チャネル1から受け取った 第1のリードコマンドのそれぞれを個々に処理するとと もに、当該リードコマンドの総数を例えばカウントする ことにより求め、予測値として記憶する。

③′チャネル1は、連鎖指定の第2のリードコマンドを 発行する。

④ 入出力側延長装置4は、第2のリードコマンド中の 先頭コマンドを受け取って処理し後で、前記予測値に基 づく所定数、例えばそれから1個だけ少ない数のリード コマンドを入出力装置5に発行する。

5 入出力側延長装置4は、入出力装置から送られるリードデータを順次チャネル側延長装置2に転送する。

⑥ チャネル側延長装置2は、転送されたリードデータを、すでにチャネル1から受け取っている第2のリードコマンドの中の所定のものに順番に対応付けた形で転送する。

【0026】図3は本発明の原理説明図(その3)である。これは、任意の選択済の入出力装置5に対する連鎖指定のライトコマンドなどをチャネル1から先取りして実行するときの処理手順の概要であり、その内容は次のようになっている。

【0027】**①**″ チャネル側延長装置2は、第1のライトコマンドおよび第1のライトデータをチャネル1から 取り込む。

②" チャネル側延長装置2は、第1の終了ステータス (擬似応答) をチャネル1に通知する。

③"チャネル側延長装置2は、チャネル1からライトコマンド連鎖の指示があるかどうかを判断し、「YES」の場合は次のステップに進み、「NO」の場合はステップ ⑤"に進む。

●" チャネル側延長装置2は、第1のライトデータの処理に関する入出力装置5の応答を待たずに第2の終了ステータス (擬似応答) をチャネル1に通知して次の第2のライトコマンドおよびライトデータを先取りする。

⑤" チャネル側延長装置2は、第1のライトデータの処理に関する入出力装置5の応答に基づく第2の終了ステータスをチャネル1に通知する。

【0028】なお、ステップ③"で「YES」の場合に擬似応答をチャネル1に返すのは、連鎖指定がある場合にチャネル1がこの擬似応答をCPUに通知することがな

いからである。

【0029】逆に、連鎖指定のない場合にまで擬似応答をチャネル1に返すとこれがCPUに通知され、CPUはジョブが正常終了したものと判断してしまい、このジョブが入出力装置の異常のために正常終了できない場合にはCPUの判断と実際の状況とが合致しないことになる。

[0030]

【作用】本発明は、このように、チャネルが発行する機番やコマンドなどの起動要求を受けたチャネル側延長装置が、この起動要求に関する選択対象入出力装置からのステータス応答を待たずに擬似応答をチャネルに通知して、当初の選択対象入出力装置に対するチャネルの起動処理をいったん待ち状態にすることにより、このいわばアイドル時間にチャネルが他の入出力装置への起動処理を新たに開始できるようにしている(図7、図8参照)。

【0031】また、チャネルから選択対象入出力装置へのデータ転送の場合にも同じように、チャネル側延長装置がこのデータを受け取った段階、すなわちデータ転送の実行終了のステータスを選択対象入出力装置から受け取っていない段階でチャネルのそれまでのデータ転送処理を仮終了させる。

【0032】そのため、チャネルは、従来のように、最初に選択した入出力装置に対する起動処理やデータ転送の開始から終了までの間、継続して当該入出力装置に占有されるといったことはなく、チャネルの使用効率の向上を図ることができる。

【0033】ことにチャネルの占有時間が、長距離にわたって設置された回線上の時間遅延や入出力装置の性能に依存しない構成になっているので、この占有時間をほばローカル接続の場合と同じ程度にすることができ、チャネルの使用効率の向上も顕著なものとなる。

【0034】さらには、入出力側延長装置は複数の入出力装置に対して並行動作できるようにしているので、チャネルの配下の複数の入出力装置を同時動作させることも可能である。

【0035】なお、擬似応答はコマンド対応のもののみに限定される必要はなく、この他に機番対応のものを用いるようにしてもよい。この場合、機番対応の擬似応答で待ち状態となっているチャネルから当該指示機番の入出力装置へのコマンドが発行されるのは、当該入出力装置からの応答によって当該待ち状態が解除された後である。

【0036】本発明は、また、同一ジョブ内では連鎖リードコマンドの個数が通常一定であることに着目して、入出力側延長装置(IOA)が、先ず起動1の連鎖リードコマンドの個数を記憶し、その後の起動2の連鎖リードコマンド中の1番目のリードコマンドをチャネル側延長装置から受け取って入出力装置でのその処理が終了し

たのを確認すると、チャネル側からのその後のリードコマンドを待つことなしに、先に記憶した個数に基づく所定数のリードコマンドを自らが新たに発行してリードデータを先行的に読み出してチャネル側延長装置に転送し、そこで当該リードデータの起動2の連鎖リードコマンドとの対応付けを行なうことにより、連鎖コマンド全体の処理時間を短縮化するようにしたものである(図9参照)。

【0037】そして、起動2の連鎖リードコマンドの実際の個数Nが起動1のそれよりも少ない場合には、チャネル側延長装置は、余分となったリードデータを廃棄してこのNの値を入出力側延長装置に通知し、これを受け取った入出力側延長装置は、記憶値をNに変更し、入出力装置をN番目のリードデータの読み出し直後の状態にするためのコマンドを入出力装置に発行している(図10参照)。

【0038】逆に、起動2の連鎖リードコマンドの実際の個数Nが起動1のそれよりも多い場合には、チャネル側延長装置は、入出力側延長装置に対してこの多い分だけのリードコマンドを発行するとともにこのNの値を通知し、これを受け取った入出力側延長装置は、記憶値をNに変更し、これらリードコマンドの処理を入出力装置に個々に指示している(図11参照)。

【0039】本発明は、また、チャネル側延長装置が、自装置への第1のライトデータの転送処理についての擬似的な第1の終了ステータス(チャネルエンド)を前記チャネルに通知した際に、ライトコマンド連鎖の指示がある場合には第1のライトデータの処理に関する入出力装置の応答を待たずに擬似的な第2の終了ステータス(デバイスエンド)をチャネルに通知して次の第2のライトコマンドや第2のライトデータの先取り処理へと移行し、また前記指示がない場合には前記応答に基づく第2の終了ステータス(デバイスエンド)を前記チャネルに通知するようにしている(図12、図13参照)。

【0040】ここで、チャネル側延長装置は、チャネルに対して、ライトコマンド連鎖の指示がない場合には擬似応答をチャネルに返さずに入出力装置での実際の処理結果に基づく第2の終了ステータスを通知しているので、入出力装置の異常発生のためジョブが正常に終了していないにもかかわらず、ホストが擬似的な第2の終了ステータスの通知によってジョブは正常終了したものと誤認するようなことはない。

[0041]

【実施例】図4~図13を参照して本発明の実施例を説明する。図4は、本発明の、入出力インタフェース延長制御システムの概要を示す説明図であり、

11は、ホスト

12は、チャネル1 に相当のBMC (Block Multiplex Chanel)

13は、チャネル側TAG/BUSケーブル

14は、チャネル側延長装置

15は、チャネル側延長装置中のBMA(Block Multiplex Adapter)

16は、送受信用ドライバからなるラインセット(LS)

17は、LANなどの回線

18は、入出力側延長装置

19は、送受信用ドライバからなるラインセット(LS) 20および21は、入出力側延長装置中のIOA(入出力ア ダプタ)

22および23は、チャネル1の配下の I / O装置 (入出力装置)

24は、入出力側TAG/BUSケーブル

をそれぞれ示している。チャネル側TAG/BUS13および入出力側TAG/BUS24の構成、作用などは図7のものと同様である。

【0042】IOA20とI/O装置22の結合と、IOA 21とI/O装置23の結合とは物理的に切り離されているので、各結合間での競合は発生せずI/O装置22とI/O装置23との並行動作が確保される。

【0043】図5は、BMA15の内部構成を示す説明図であり、31はコントロールストーレッジ、32はマイクロプロセッサ、33はRAM、34はIO-IFコントローラ、35はバッファコントローラ、36はSYSBUSコントローラ、37はデータバッファ、38はシステムバスをそれぞれ示している。なお、このような構成のBMA15は入出力側延長装置としても使用できる。

【0044】各コントローラの中、

- ・IO-IFコントローラ34は、マイクロプロセッサ32 の指示に従ってBMC12との間でインタフェース制御を 実行し、
- ・SYSBUSコントローラ36は、マイクロプロセッサ 32の指示に従ってラインセット16との間でインタフェー ス制御を実行し、
- ・バッファコントローラ35は、データバッファ37へのアクセス源であるマイクロプロセッサ32、IO-IFコントローラ34、SYSBUSコントローラ36の競合制御を実行している。

【0045】ここで、BMA15の制御は、コントロールストーレッジ31に格納されたマイクロプログラムに基づくシーケンス制御のかたちで行なわれ、各入出力装置の情報は個々にRAM33およびデータバッファ37上に格納されている。

【0046】なお、RAM33には各入出力装置のデータの保持状態などに関する制御情報が、またデータバッファ37にはコマンド、データ、ステータスについての情報がそれぞれ入出力装置ごとに保持されている。

【0047】図6は、入出力装置からBMCに通知されるステータスのフォーマットを示す説明図である。ステータスは1バイトで構成され、例えば、

・3ビット目は入出力装置が使用中であることを示すビ

ィジィ

・4ビット目はチャネルとの通信終了を示すチャネルエンド (CE)

・5ビット目は入出力装置の動作終了または使用状態解除を示すデバイスエンド(DE)

のためのビットとしてそれぞれ用いられている。

【0048】図7は、機番およびコマンドなどの起動要求に対する処理シーケンスを示す説明図であり、その内容は次のようになっている。なお、シーケンス中の「井1」および「‡2」は選択対象の入出力装置の機番を示している。

【0049】(1) BMC12は、入出力装置22(以下、IO#1と略記する)に対する選択指示の発行を行なう。 これにより、IO#1の機番がBMA15に通知される。 (2) BMA15は、IO#1が選択されたことの擬似応答をBMC12に対して行なう。

(3) BMC12は、IO#1に対するライトコマンドを指示する。

(4) BMA15は、IO#1が使用中(BUSY)である旨の 擬似応答をBMC12に対して行なう。なお、このときの データとしてはBUSY形式の「0001000」が用い られる(図6参照)。

【0050】このBUSYを示す擬似応答の結果、ホスト11 の内部ではIO#1に対する起動処理を待ち状態にして解除通知(デバイスエンド:DE#1)が通知されるのを待つことになる。

【0051】(5) BMA15は、BMC12から受けている 機番およびライトコマンドを、IO#1の制御主体であ るIOA20(以下、IOA#1と略記する)に転送す る。

(6) IOA#1は、IO#1に対する選択指示の発行を 行なう。

(7) I O # 1 は、自分が選択されたことを I O A # 1 に 報告する。

(8) IOA#1は、IO#1に対してコマンドを指示する。

(9) I O # 1 は、コマンド実行に関するステータスを I O A # 1 に報告する (図6参照)。

(10) I O A # 1 は、 I O # 1 から報告された内容(選択結果、コマンド実行に関する情報)を B M A 15に通知する。

【0052】(11) BMA15は、この報告内容をデータバッファ37に保持してBMC12に解除通知を行なう。なお、このときのデータとしてはDE形式の「00000100」が用いられる(図6参照)。

(12) B M C 12は、(4) で設定した待ち状態を解除することにより再起動処理を開始し、先ず I O # 1 の選択指示を発行する。

(13) B M A 15は、データバッファ37を参照し、I O # 1 が選択されていることをB M C 12に報告する。

(14) BMC 12は、I O # 1 に対する前記コマンドを指示する。

(15) BMA15は、データバッファ37を参照し、IO#1 でのコマンド実行に関する情報をBMC12に通知する。【0053】以上のシーケンスによりチャネルと入出力装置当該の間での起動処理はいったん終了するが、(15)の通知に基づいて、IO#1が「ライト可能」あることをBMC12が確認したときには図6に示すようなデータ転送処理に移行する。このときの確認は、IO#1から報告されたステータスが転送可能形式の「00000000」であることによって行われる。

【0054】なお、ステップ(4) やステップ(11)の擬似 応答および解除通知で用いるデータ形式はIO#1から 報告されたステータスの内容に依存しないものであり、 例えば解除通知の場合にはステータス中の実際のDEビットなどの値とは無関係に前記の「0000100」が、BMA15からBMC12に通知される。

【0055】前述のように、BMC12は、IO#1を対象とした起動処理が待ち状態となってその後のBMA15からの解除通知で再起動するまでの間、他の入出力装置に対する起動処理をIO#1のときと同様のシーケンスで行なうことができる。

【0056】図7では、BMC12の待ち状態の間にIO#2の起動処理が実行されており、IO#1のときと同じように、コマンド指示を受けたBMA15はIO#2が使用中(BUSY)である旨の擬似応答をBMC12に対して通知し、この通知に基づいてBMC12は待ち状態に設定される。なお、図中の網線部分はBMC12とBMA15との結合時間を示している。

【0057】このように、入出力装置の起動処理にともなうBMC12とBMA15との結合を、入出力装置からの本来の応答を待たずにBMA15からの擬似応答でいったん解き、続いて他の入出力装置に対する起動処理を行なえるようにしているので、複数の入出力装置に対する起動要求が競合しても効率的な処理を行なうことができる

【0058】図8は、図7のライトコマンドの起動処理に続いてライトデータをチャネルから入出力装置に転送するときの処理シーケンスを示す説明図であり、その内容は次のようになっている。なお、「・・S」はデータ転送の開始を、「・・E」はその終了をそれぞれ示している。

【 O O 5 9 】(21) B M A 15は、 I O # 1 へのライトデー タを B M C 12から先取りしてデータバッファ37に保持す る。

(22) BMA15は、この先取りが終了した段階でBMC12 に擬似応答を返す。この擬似応答によりBMC12とBMA15との結合がいったん切れてBMC12はDE待ちの状態になる。なお、このときのデータとしてはCE形式の「00001000」が用いられる(図6参照)。

- (23) B M A 15は、I O A # 1 にライトデータを転送する。
- (24) I O A # 1 は、I O # 1 に ライトデータを転送する。
- (25) I 〇 # 1 は、転送されたデータのライト処理が正常 終了したか、異常終了したかを示すステータスを I O A # 1 に通知する。なお、このステータスには図6に示す ようにCEとDEとが含まれている。
- (26) I OA#1は、このステータスをBMA15に通知する。
- (27) B M A 15は、このステータス中のD E を B M C 12に 通知する。

【0060】DE待ちの状態になっていたBMC12はこのDEに基づいてIO#1に対するライト処理を終了することになる。なお、ライト処理が異常終了の場合にはこれに対応した処理に移行する。

【0061】図8のシーケンスにおいても、図7のときと同じように、BMA15でのデータの先取りの終了にともないステップ(22)において発行される擬似応答によりBMC12はIO#1へのデータ転送処理からいったん解放され、その後はIO#2に対するデータ転送を開始することができる。なお、従来は、ステップ(27)の通知によってIO#1に対するライト処理が終了するまで、IO#2へのデータ転送を開始することはできない。

【0062】図9は、連鎖指定されたリードコマンドに対する処理手順を示している。なお、以下の実施例では入出力装置として、大量のデータを記憶でき、ホストとの間で入力方向あるいは出力方向の一括したデータ転送が行なわれるのが一般的である磁気テープ装置 (MT)を用いることにする。

【0063】また、連鎖指定されたコマンドは、リードコマンド(RD)およびライトコマンド(WT)ともにモードセットコマンド(MS)を先頭としてその後に複数のRDまたはWTが続く次のような形、

- (a) MS-RD-RD-RD-RD・・・・-RD (b) MS-WT-WT-WT・・・・-WT で連鎖している。
- 【0064】そして、この連鎖(CC)が指定されていることは、BMA15からBMC12に通知される各種ステータスを受理した旨の後者から前者への通知(AP)の内容によって、BMA15は認識することになる。

【0065】図9の処理手順の内容は次のようになっている。なお、以下の記載では、説明の便宜上、コマンドや終了ステータスなどの送信の際に経由するBMA15やIOA20などを適宜省略する。

【0066】(31) BMC12は、BMA15および I O A 20 を経由して起動 1 のMSコマンドをMT22に発行する。(32) MT22は、チャネルエンドCEとデバイスエンドDEの終了ステータスをBMC12に通知する。

(33) BMC12は、この終了ステータスに応答する形で、

「連鎖(CC)あり」のAPと1番目のRDコマンドをMT22に発行する。

(34) MT22は、このRDコマンドの実行結果であるリードデータと、チャネルエンドCEとデバイスエンドDE の終了ステータスとをBMC12に通知する。

【0067】この後はステップ(33)とステップ(34)とが 起動1の残りの各RDコマンドに対して個々に実行され、IOA20はステップ(33)の処理で受け取ったRDコマンドの数をカウントしていく。

【0068】そして、起動1の最後のRDコマンド、すなわち4番目のRDコマンドのときのMT22からの終了ステータスに応答する形で、

(35) BMC12は「連鎖(CC)なし」のAPを発行し、 これを受け取った IOA20はそのときのカウント値であ る連鎖総数「4」を予測値として記憶する。

【0069】その後、起動2の連鎖指定されたリードコマンドに対して先ず起動1のそれの場合と同じように次の処理が行なわれる。

- (41) BMC 12は、BMA15および I OA20を経由して起動2のMSコマンドをMT22に発行する。
- (42) MT22は、チャネルエンドCEとデバイスエンドDEの終了ステータスをBMC12に通知する。
- (43) B M C 12は、この終了ステータスに応答する形で、 「連鎖 (C C) あり」のA P と 1 番目のR D コマンドを M T 22に発行する。

(44) MT22は、このRDコマンドの実行結果であるリードデータと、チャネルエンドCEとデバイスエンドDEの終了ステータスとをBMC12に通知する。

【 O O 7 O 】 ここで、ステップ(43)における「連鎖(C C) あり」のA P を受け取った I O A 20は、ステップ(44)のチャネルエンド C E とデバイスエンド D E とを確認した後のステップ(45)で自発的にR D コマンドをMT22に発行する。

【0071】そして、ステップ(47)で、このRDコマンドに対する終了ステータスとリードデータがMT22からBMA15に送られる。BMA15は、当該リードデータを、すでにステップ(46)により「連鎖(CC)あり」のAPとともに取り込んでデータバッファ37に保持していた起動2の2番目のRDコマンドに対応付けてからBMC12に送る。このとき、BMA15は、MT22から受け取った終了ステータスも送っている。

【0072】また、起動2の3番目および4番目のRDコマンドに対してもこのような一連の処理が、

- ・起動1の3番目のRDコマンドの場合はステップ(48)~(50)
- ・起動1の4番目のRDコマンドの場合はステップ(51)~(53)

の形でそれぞれ実行される。

【0073】このように、起動2の連鎖指定されたリードコマンドの中の2番目から2番目(最後)までの各リ

ードコマンドについては、BMA15から長距離の回線を 経由しててIOA20に送るといった処理工程を省略で き、連鎖コマンド全体の処理時間が短縮化されてスルー プットを高めることになる。

【0074】なお、ステップ(46)、(49)、(52)における「連鎖(CC)あり」のAPとRDコマンドの発行はそれぞれステップ(44)、(47)、(50)によってBMC12が終了ステータスを受け取った後である。

【0075】BMC12は、起動2の4番目のRDコマンドに対応付けられたリードデータをBMA15から受け取ると、もはや起動2には連鎖指定がないので「連鎖(CC)なし」のAPをIOA20に通知する。

【0076】このとき、BMC12は起動2のRDコマンドの総数「4」についても送っており、これを受け取ったIOA20は起動1のときに求めた予測値と一致していること、すなわち予測値を変更する必要がないことを確認する。

【0077】図10は、図9において起動2のリードコマンドの総数が起動1のそれよりも少ない場合の処理手順を示す説明図であり、図9とは、このRDコマンドが少ない分だけのリードデータを廃棄することや、MT2を起動2のRDコマンドが個々に実行されたときの本来の状態に戻すことなどが相違している。

【0078】すなわち、起動2のRDコマンドの総数は「2」であるので、ステップ(45)のリードコマンド発行に対応したステップ(47)の処理によって起動2は終了となり、このステップ(47)に続くステップ(50)やステップ(53)でMT22からBMA15に送られるリードデータはともに不要であり、BMA15はこれらの廃棄処理を行なっている。

【0079】また、ステップ(54) だおける「連鎖(CC)なし」のAPを受け取ったIOA20はMT22との間で次の処理を実行する。

(61) I O A 20は、MT22に対してBS (Back Space)コマンドを発行する。

(62) M T 22は、I O A 20に対して終了ステータスのデバイスエンド D E を 通知する。

(63) I O A 20は、MT22に対してBSコマンドを発行する。

(64) MT22は、IOA20に対して終了ステータスのデバイスエンドDEを通知する。

【0080】BSコマンドは、MT22の読み取りヘッダの位置を1ブロック分だけ戻す指示であり、IOA20がこれを2回発行することにより、MT22のヘッド位置は3番目と4番目のデータブロックを読み込んでいない状態、つまり2番目のデータブロックを読んだ直後の状態になる。

【0081】なお、BMA15は「連鎖(CC)なし」の APを確認した上で、自装置で取り込んだRDコマンド の数「2」をIOA20に通知し、これを受け取ったIO A20は予測値をそれまでの「4」から「2」に変更している。

【0082】図11は、図9において起動2のリードコマンドの総数が起動1のそれよりも多い場合の処理手順を示す説明図であり、図9とは、BMC12はこの多い分だけのRDコマンドを従来の方法で発行してその実行結果であるリードデータをMT22から受け取るステップ(71)、(72)が付加されることが相違している。

【0083】また、起動2の終了にともなってBMC12が「連鎖(CC)なし」のAPをBMA15に通知し、このBMA15が自装置で取り込んだRDコマンドの数「4」をIOA20に通知して予測値をそれまでの「3」

から「4」に変更することは、図10の場合と同様である。

【0084】図12は、連鎖指定されたライトコマンドなどをBMC12から先取りして実行するときの処理手順を示す説明図であり、その内容は次のようになっている。

(81) BMC12は、BMA15およびIOA20を経由してMSコマンドをMT22に発行する。

(82) MT22は、チャネルエンドCEとデバイスエンドDEの終了ステータスをBMC12に通知する。

(83) BMA15は、1番目のWTコマンドおよびデータをBMC12から取り込んでMT22に転送し、擬似応答をBMC12に通知する。

(84) MT22は、デバイスエンドDEを発行する。

(85) BMA15は、2番目のWTコマンドおよびデータを BMC12から取り込んでMT22に転送し、擬似応答をB MC12に通知する。

(86) MT22は、デバイスエンドDEを発行する。

(87) BMA15は、3番目のWTコマンドおよびデータを BMC12から取り込んでMT22に転送し、擬似応答をB MC12に通知する。

(88) MT22は、デバイスエンドDEを発行する。

(89) BMA15は、4番目のWTコマンドおよびデータをBMC12から取り込んでMT22に転送し、擬似応答をBMC12に通知する。

(90) MT22は、デバイスエンドDEを発行する。

【0085】ここで、BMA15は、MT22からのデバイスエンドDEを受け取ることにより、それまで擬似応答を返していたWTコマンドのそれぞれが正常終了したであろうと判断する。

【0086】また、BMC12がステップ(90)のデバイスエンドDEを確認することにより一連の先取り処理を終了する。なお、BMC12およびBMA15はは他のステップにおけるMT22からのデバイスエンドDEも順次受け取っており、それがステータス(90)のデバイスエンドDEであることを確認する手法としては、

・連鎖指定されたWTコマンドの総数をあらかじめ確認 しておき、MT22から送られるデバイスエンドDEの数 を順にカウントしていくこと、

・最後(4番目)のWTコマンドおよびこれに対するデバイスエンドDE自体に最後であることを示す情報を含ませておくこと、

なとが用いられる。

【0087】図13は、図12のBMCとBMAとの間でのライトコマンドなどに対する処理手順を示す説明図であり、その内容は次のようになっている。

- (91) BMC12は、WTコマンドをBMA15に発行する。
- (92) BMA15は、WTコマンドを受け取った旨の初期ステータスをBMC12に擬似応答として通知する。
- (93) B M C 12は、初期ステータスの受理を示す A P を B M A 15に発行する。
- (94) BMC12は、データをBMA15に転送する。
- (95) B M A 15は、データを受け取った旨の終了ステータス1 (チャネルエンド)を B M C 12に擬似応答として通知する。
- (96) BMC12は、終了ステータス1の受理を示すAPを、連鎖(CC)の有無、すなわち次のWTコマンドが連鎖されているかどうか示す情報を含む形でBMA15に発行する。
- (97) BMA15は、再結合要求をBMC12に発行する。この発行は、ステップ(95)のチャネルエンドに基づいてBMA15とBMC12との間が切断されるからであり、また、ステップ(96)のAPが「連鎖(CC)なし」の場合にはMT22からのデバイスエンドDEを確認した上で実行される。
- (98) BMA15は、次のWTコマンドを取り込むための終了ステータス2 (デバイスエンド)をBMC12に通知する。この終了ステータスは、先のステップ(96)のAPが「連鎖(CC)あり」の場合には擬似応答、「連鎖(CC)なし」の場合にはMT22からのデバイスエンドDEとなる。
- (99) BMC12は、終了ステータス1の受理を示すAPをBMA15に発行する。この中には連鎖(CC)の有無を示す情報が含まれるのが通常である。
- (100) BMC12は、「連鎖(CC)あり」の場合は次の WTコマンドを先取りして発行する。

【0088】ステップ(98)における終了ステータス2の 使い分けは、前述のように、「連鎖(CC)なし」の場 合に擬似応答がCPUに通知され、CPUの方で、まだ 実際の実行結果が分かっていないジョブについて正常終 了したものとみなすことを防止するためである。なお、 「連鎖(CC)あり」の場合の終了ステータスはCPU に通知されない。

[0089]

【発明の効果】本発明は、このように、チャネルから発行される機番やコマンドなどの起動要求を受けたチャネル側延長装置が、この起動要求に関する選択対象入出力装置のステータスを待たずに擬似応答をチャネルに通知

してチャネルのそれまでの選択対象入出力装置に対する 起動処理をいったん待ち状態にすることにより、選択対 象入出力装置の起動処理を行なっているチャネルのいわ ばアイドル時間に他の入出力装置に対する新たな起動処 理を開始できるようにしている。

【0090】さらに、チャネルから選択対象入出力装置へのデータ転送の場合にも同じように、チャネル側延長装置がこのデータを受け取った段階、すなわちデータ転送の実行終了のステータスを受け取っていない段階でチャネルのそれまでのデータ転送処理を仮終了とする。

【0091】そのため、チャネルは、従来のように、最初に選択した入出力装置に対する起動処理やデータ転送の開始から終了までの間、継続して当該入出力装置に占有されるといったことはなく、チャネルの使用効率の向上を図ることができる。

【0092】ことにチャネルの占有時間が長距離にわたって設置された回線上の時間遅延や入出力装置の性能に依存しない構成になっているので、この占有時間をほぼローカル接続の場合と同じ程度にすることができ、チャネルの使用効率の向上も顕著なものとなる。

【0093】さらには、入出力側延長装置は入出力装置 に対して並行動作できるようにしているので、チャネル の配下の複数の入出力装置を同時動作させることも可能 である。

【0094】本発明は、また、同一ジョブ内では連鎖リードコマンドの個数が通常一定であることに着目して、 先ず起動1の連鎖リードコマンドの個数を記憶しておき、その後の起動2の連鎖リードコマンドの処理の際には、入出力側延長装置がこの個数に基づく所定数だけのリードコマンドを入出力装置に独自に発行するようにしており、これは、チャネル側延長装置から長距離の回線を経由して入出力側延長装置までリードコマンドを送信する時間を省略することになるのて、連鎖リードコマンド全体の処理時間の短縮化を図ることができ、回線使用効率が大幅に改善される。

【0095】本発明は、また、チャネル側延長装置が、自装置への第1のライトデータの転送処理についての擬似的な第1の終了ステータスをチャネルに通知した際に、ライトコマンド連鎖の指示がある場合には第1のライトデータの処理に関する入出力装置の応答を待たずに擬似的な第2の終了ステータスをチャネルに通知して次の第2のライトコマンドなどの先取り処理へと移行し、また前記指示がない場合には前記応答に基づく第2の終了ステータスをチャネルに通知するようにしているので、CPUの方で、まだ実際の実行結果が分かっていないジョブについて正常終了したものとみなすといったことは発生せず、システムの信頼性を確保した状態でのライトコマンドの先取り処理が可能となる。

【図面の簡単な説明】

【図1】本発明の、原理説明図(その1)である。

【図2】本発明の、原理説明図(その2)である。

【図3】本発明の、原理説明図(その3)である。

【図4】本発明の、入出力インタフェース延長制御システムの概要を示す説明図である。

【図5】本発明の、チャネル側延長装置(BMA)の内部構成を示す説明図である。

【図6】本発明の、入出力装置からBMCに通知される ステータスのフォーマットを示す説明図である。

【図7】本発明の、起動要求に対する処理シーケンスを 示す説明図である。

【図8】本発明の、起動処理に続いてデータ転送を行な うときの処理シーケンスを示す説明図である。

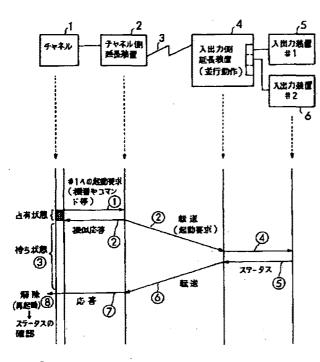
【図9】本発明の、連鎖指定されたリードコマンドに対する処理手順を示す説明図である。

【図10】図9において、起動2のリードコマンドの総数が起動1のそれよりも小さい場合の処理手順を示す説明図である。

【図11】図9において、起動2のリードコマンドの総数が起動1のそれよりも大きい場合の処理手順を示す説

【図1】

本発明の原理説明団(その1)



【 テヤネル1は、待ち状態の間、他の入出力接置6(#2)への ・起動要減信号を発行することができる

明図である。

【図12】本発明の、連鎖指定されたライトコマンドなどをBMCから先取りして実行するときの処理手順を示す説明図である。

【図13】図12の、BMCとBMAとの間でのライトコマンドなどに対する処理手順を示す説明図である。

【図14】一般的な、入出力インタフェース延長制御システムの概要を示す説明図である。

【図15】一般的な、起動要求に対する処理シーケンスを示す説明図である。

【符号の説明】

図1において、

1・・・チャネル

2・・・チャネル側延長装置

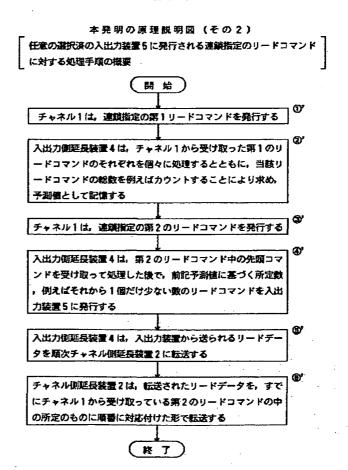
3・・・高速の回線

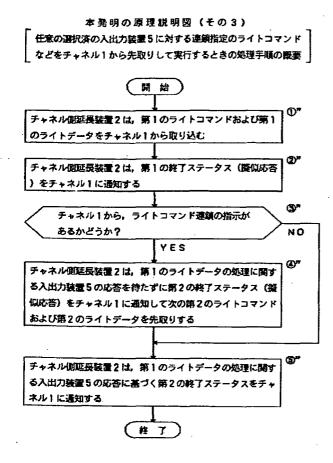
4 · · · 入出力側延長装置

5・・・入出力装置

6 · · · 入出力装置

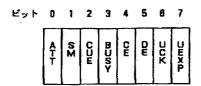
【図2】





【図6】

本発明の入出力装置から B M C に通知されるステータスの フォーマット



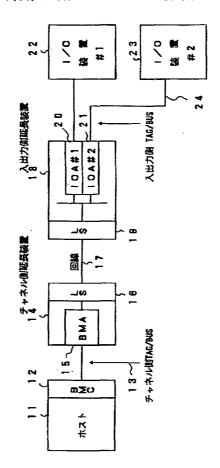
ATT :入出力装置側にホストに通知したい事象が発生したことを示す

SM :他のステータスピットの修飾に使用する CUE :コントロールユニットの使用中解除を示す BUSY:入出力装置が使用中であることを示す

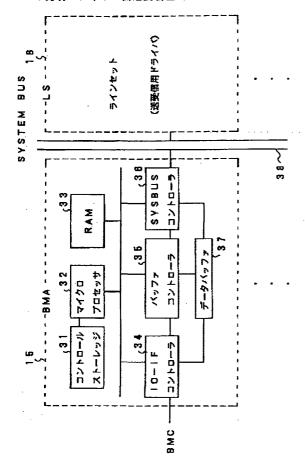
CE :チャネルとの通信終了を示す

DE: 入出力装置の動作終了あるいは使用中解除を示す UCK: 入出力装置が異常を検出したことを示す UEXP: 入出力装置が例外を検出したことを示す

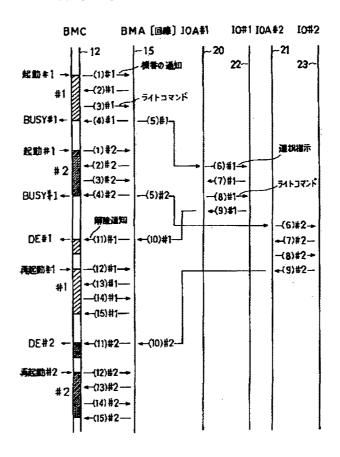
本発明の入出力インタフェース延長制御システムの概要



本発明のチャネル側延長装置(BMA)の内部構成

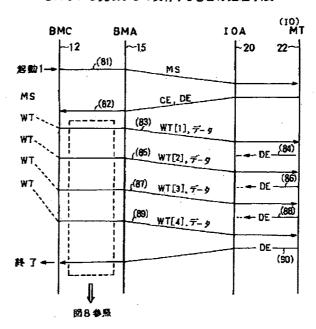


本発明の起動要求に対する処理シーケンス



【図12】

本発明の連鎖指定されたライトコマンドなどを BMCから先取りして実行するときの処理手順



本発明の起動処理に続いてデータ転送を 行なうときの処理シーケンス

BMC

#1

#2

日本うとその処理シーケン人

BMA [回縁] IOA# IO#2

-(21)#15 → -(21)#15 → -(22)#1 - (23)#15

-(21)#25 → -(22)#2 - (23)#16

(24)#15-

(24)#1E

·(25)#1_丁

CE, DE

-(24)#2S-

U(24)#2E-

-(25)#2-

(23)#25

(26)#1

(26)#2

(23) # 2E

本発明の連續指定されたリードコマンドに対する処理手順

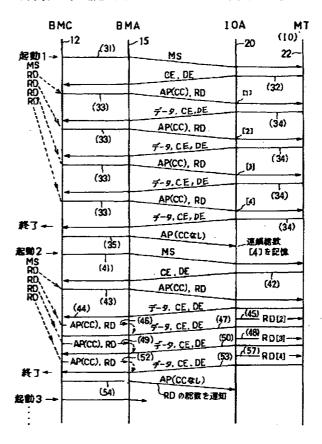


図9において起動2のリードコマンドの総数が 起動1のされよりも小さい場合の処理手順

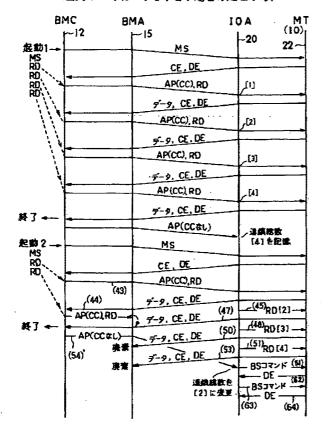


図9において起動2のリードコマンドの総数が 起動1のそれよりも大きい場合の処理手順

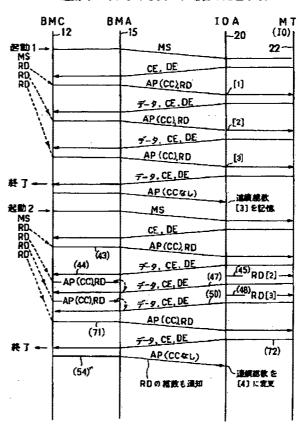
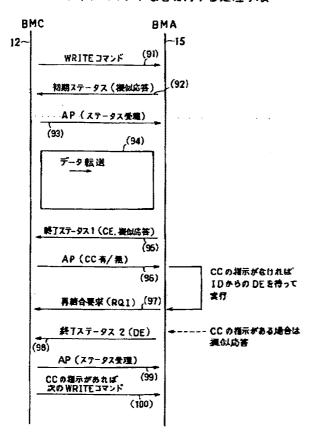
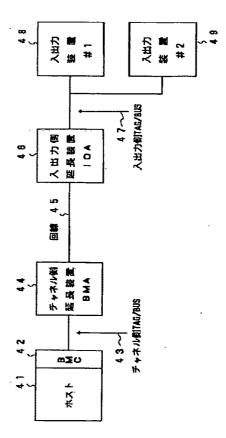


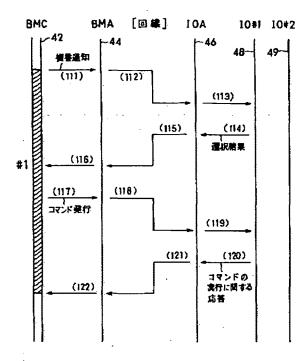
図12のBMCとBMAとの間での ライトコマンドなどに対する処理手順

一般的な入出力インタフェース延長制御システムの概要





一般的な起動要求に対する処理シーケンス



[網部分の間は IO # 2 への起動処理は不可]

フロントページの続き

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ARTIFACT SHEET

	tifact number below. Artifact number is application number + type code (see list below) + sequential letter (A, B, C). The first
artifact	folder for an artifact type receives the letter A, the second B, etc
Exampl	les: 59123456PA, 59123456PB, 59123456ZA, 59123456ZB
	e quantity of a single type of artifact received but not scanned. Create that artifact folder/box and artifact number for each Artifact Type.
	CD(s) containing: computer program listing Doc Code: Computer Artifact Type Code: P pages of specification and/or sequence listing and/or table Doc Code: Artifact Artifact Type Code: S content unspecified or combined Doc Code: Artifact Artifact Type Code: U
	Stapled Set(s) Color Documents or B/W Photographs Doc Code: Artifact Type Code: C
	Microfilm(s) Doc Code: Artifact
	Video tape(s) Doc Code: Artifact
	Model(s) Doc Code: Artifact
	Bound Document(s) Doc Code: Artifact Type Code: B
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3	Other, description: Doc Code: Artifact Type Code: Z
	\cdot

		CLAIMS A	S FILED	- PART	J	•	s	MALL E	NTITY	<u> </u>	OTHE	R THAN
_			(Colum	n 1)	(Colu	ımn 2)	T	YPE [OR	SMALL	ENTITY
1	OTAL CLAIMS		16					RATE	FEE]	RATE	FEE
F	OR		NUMBE	R FILED	NUME	ER EXTRA	E	BASIC FEE	150.00	OR	BASIC FEE	300.00
T	OTAL CHARGE	ABLE CLAIMS	18 m	inus 20=	<u></u>	9		X\$ 25=		OR	X\$50=	
INDEPENDENT CLAIMS		3 "	ninus 3 =	•	ゔ		X100=		OR	X200=		
M	JLTIPLE DEPE	NDENT CLAIM P	RESENT		<u> </u>			+180=	189	OR	+360=	
ı	the difference	e in column 1 is	less than a	tero, enter	"0" in c	olumn 2	L	TOTAL	730	OR	TOTAL	
	C	LAIMS AS A	MENDE	D - PAR'	TII)) (10	OTHER	THAN
_		(Column 1)		(Colun		(Column 3)		SMALL	NTITY	OR	SMALL	ENTITY
EN A	3/28/04	CLAIMS REMAINING AFTER AMENDMENT		HIGH NUME PREVIO PAID	BER	PRESENT EXTRA	L	RATE	ADDI- TIONAL FEE		RATE	ADDI- TIONAL FEE
	Total	.76	Minus	-3	0	= 56		X\$ 25=	1400	OR	X\$50=	
j E	Independent	. 3	Minus		3	*		X100=	-1/4	OR	X200=	
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				•			Ŀ	TOTAL	1,400		TOTAL	
		(Column 1)		(Colun	າກ 2)	(Column 3)	AĐ	DIT. FEE	1411-] ,	ADDIT. FEE	
2		CLAIMS REMAINING AFTER AMENDMENT		HIGHE NUME PREVIO PAID F	IER USLY	PRESENT EXTRA		RATE	ADDI- TIONAL FEE		RATE	ADDI- TIONAL FEE
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	Independent	* NTATION OF ML	Minus	***			,	<100=		OR		
	Independent	•	Minus	***			,	(100= -180= TOTAL	,	OR OR	+360= TOTAL	
	Independent	NTATION OF MU	Minus	PENDENT	CLAIM		,	<100= -180=		OR OR	+360=	
	Independent	•	Minus	***	CLAIM n 2) st en		Aoi	(100= 180= TOTAL DIT. FEE	ADDI- TIONAL FEE	OR OR	+360= TOTAL	ADDI- TIONAL FEE
	Independent	(Column 1) CLAIMS REMAINING AFTER AMENDMENT	Minus	(Column HIGHE NUMB PREVIOU	CLAIM n 2) st en	(Column 3)	ADI	(100= 180= TOTAL DIT. FEE	TONAL FEE	OR OR OR	+360= TOTAL DOIT. FEE	TIONAL
	Independent FIRST PRESE Total Independent	(Column 1) CLAIMS REMAINING AFTER AMENDMENT	Minus JLTIPLE DE Minus Minus	(Colum HIGHE NUMB PREVIOU PAID F	CLAIM n 2) ST ER USLY OR	(Column 3) PRESENT EXTRA	ADI	(100= 180= TOTAL DIT. FEE RATE 1	TIONAL FEE	OR OR	+360= TOTAL DOIT. FEE RATE X\$50=	TIONAL
	Independent FIRST PRESE Total Independent	(Column 1) CLAIMS REMAINING AFTER AMENDMENT	Minus JLTIPLE DE Minus Minus	(Colum HIGHE NUMB PREVIOU PAID F	CLAIM n 2) ST ER USLY OR	(Column 3) PRESENT EXTRA	ADI X	(100= 180= TOTAL DIT. FEE 3 3 25= (100=	TIONAL FEE	OR OR	+360= TOTAL DOIT. FEE RATE X\$50= X200=	TIONAL
	Independent FIRST PRESE Total Independent FIRST PRESE	(Column 1) CLAIMS REMAINING AFTER AMENDMENT	Minus JETIPLE DE	(Column Higher NUMB PREVIOUS PAID F	CLAIM n 2) ST ER JSLY OR	(Column 3) PRESENT EXTRA	ADD F	(100= 180= TOTAL DIT. FEE RATE 1	TIONAL FEE	OR OR	+360= TOTAL DOIT. FEE RATE X\$50=	TIONAL

ZTE (USA) 1003, Page 122



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APPLICATION NUMBER	FILING OR 371 (c) DATE	FIRST NAMED APPLICANT	ATTY. DOCKET NO./TITLE

11/078,778

03/11/2005

Michael Tasler

SCHO0102D-C

22862 GLENN PATENT GROUP 3475 EDISON WAY, SUITE L MENLO PARK, CA 94025



Date Mailed: 04/05/2006

NOTICE REGARDING CHANGE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 03/15/2006.

• The Power of Attorney to you in this application has been revoked by the assignee who has intervened as provided by 37 CFR 3.71. Future correspondence will be mailed to the new address of record(37 CFR 1.33).

BERHANU GIRUM PTOSS (703) 305-0677

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APPLICATION NUMBER FILING OR 371 (c) DATE FIRST NAMED APPLICANT

ATTY. DOCKET NO./TITLE 9576/96910

11/078,778

03/11/2005

Michael Tasler

CONFIRMATION NO. 8978



OC000000018463568

Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago, IL 60606

Date Mailed: 04/05/2006

NOTICE OF ACCEPTANCE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 03/15/2006.

The Power of Attorney in this application is accepted. Correspondence in this application will be mailed to the above address as provided by 37 CFR 1.33.

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FACSIMILE COVER SHEET

From:

Jeffrey W. Salmon, Esq.

Date: June 7, 2006

To:

Commissioner for Patents

UNITED STATES PATENT & TRADEMARK OFFICE

Washington, D.C. 20231

Attn: Examiner, Mr. Harold J. Kim

Fax:

(571) 273-8300

Number of pages including this cover letter: 4

COMMENTS:

File No. 0757/96910

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE

FOR USE WITH A PERSONAL COMPUTER (As Amended)

1.6(d) to the U.S. Patent and Trademark Office, (703) 872-9306, on the date indicated below.

une 7, 2006

I hereby certify under 37 C.F.R. 1.8 that this paper is being transmitted via facsimile pursuant to 37 C.F.R.

Attorney

Docket No.: 0757/96910

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. The listed document is U.S. Patent No. 5,844,961. No copy of it is being submitted herewith.

It is submitted that no fees are due in connection with this Information Disclosure Statement because it is being submitted prior to the issuance of the first Office Action.

No inferences should be drawn that the attached list represents a comprehensive investigation, or that any material disclosed is equivalent to the subject invention. In addition, none of the documents that have publication dates prior to the priority date of the above application anticipate the invention in this application.

The cited document(s) disclose numerous specific features. There has been no attempt to list each and every feature disclosed by each document. The Examiner is requested to review the document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular

date which may be relevant to the instant patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosures of the art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the instant patent application. The recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence.

Respectfully submitted,

sy W. Salmon, Esq. stration No. 37,435

Dated: June 7, 2006

Enclosures: Form PTO-1449

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Chicago, Illinois 60606

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JUN, 8, 2006 4:12PM

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		DISCLOSURE STATEMENT eral sheets if necessary)				hael Tasler		
	(USE SEVERALE	sneets it necessar	у)	Filing Date 03/	11/05	Grou	p No. 2181	_
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*Examiner:	Initial if citation citation if not in applicant.	considered, whet conformance and	her or not citation is in I not considered. Inc	n conformance w lude copy of this	ith MPEP form with	609; Draw lir next commur	e through	1

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(analognear3signal)and(externalnea r5hous\$5)andsensorand(analognear 3digitalnear3converter)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:10
L2	233402	(analog near3 signal)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:10
L3	1447	(analog near3 signal) and (external near5 hous\$5) and sensor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:11
L4	989	(analog near3 signal) and (external near5 hous\$5) and sensor and converter	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:11
L5	738	(analog near3 signal) and (external near5 hous\$5) and sensor and (digital near3 converter)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:12
L6	72	(analog near3 signal) and (external near5 hous\$5) and sensor and (digital near3 converter) and poll\$4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:13
L7	21	l6 and @ad<"19970304"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:14
L8	30	l6 and @rlad<"19970304"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:14
L9	6	l6 and @prad<"19970304"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:14
L10	40	17 or 18 or 19	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:16
L11	126	710/220.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:17
L12	387	710/16.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:18
L13	216	710/23.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:18
L14	488	(703/23).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:21

			IIISCO:)			
L15	5870	(707/10).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:21
L16	2462	(358/296).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:21
L17	725	(358/442).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:22
L18	1	("5508821").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:22
L19	8	("5508821").URPN.	USPAT	OR	ON	2006/06/08 23:45
L20	813	(710/15).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:45
S1	1	("6470399").PN.	USPAT; USOCR; EPO; JPO	OR	OFF	2004/05/17 01:03
S2	814	710/8.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S3	225	710/16.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S4	0	710/321.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S5	0	709/321.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S6	730	709/220.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S7	328	709/222.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S8	237	710/11.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S9	74	710/12.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S10	15	710/115.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S11	598	710/62.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S12	196	710/63.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05

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S13	180	710/64.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S14	324	703/23.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S15	225	703/24.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S16	220	703/25.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S17	50	710/8.ccls. and 710/16.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S18	36	710/8.ccls. and 709/220.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S19	14	710/8.ccls. and 709/222.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S20	50	710/8.ccls. and 710/11.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S21	20	710/8.ccls. and 710/12.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S22	122	710/8.ccls. and 710/62.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S23	35	710/8.ccls. and 710/63.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S24	28	710/8.ccls. and 710/64.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S25	3	710/8.ccls. and 703/23.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S26	5	710/8.ccls. and 703/24.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S27	21	710/8.ccls. and 703/25.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06
S28	29	(driver with storage) same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:08
S29	3	(driver with storage) same (virtual near2 system) same directory	USPAT; EPO; JPO	OR	ON	2004/05/17 01:09
S30	0	(driver with storage with regardless) same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:09
S31	0	(driver with storage with inquiry) same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:10
S32	1	(driver with storage) same inquiry same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:10
S33	1	(driver with storage) same inquir\$3 same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:10

				y		
S34	0	(driver with storage) same detect\$4 same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:10
S35	3	(driver with storage) same monitor\$4 same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:11
S36	576	(driver with storage) same monitor\$4	USPAT; EPO; JPO	OR	ON	2004/05/17 01:11
S37	1	("6012113").PN.	USPAT; USOCR; EPO; JPO	OR	OFF	2004/05/17 01:14
S38	6	"6012113".URPN.	USPAT	OR	ON	2004/05/17 01:12
S39	. 0	"6470399".URPN.	USPAT	OR	ON	2004/05/17 01:12
S40	15	("4040014" "4045774" "4425625" "4503288" "4797878" "4989203" "5065427" "5155847" "5355365" "5369700" "5408527" "5452329" "5524047" "5596628" "5628030").PN.	USPAT	OR	ON	2004/05/17 01:13
S41	15	("4040014" "4045774" "4425625" "4503288" "4797878" "4989203" "5065427" "5155847" "5355365" "5369700" "5408527" "5452329" "5524047" "5596628" "5628030").PN.	USPAT	OR	ON	2004/05/17 01:14
S42	1	("5548783").PN.	USPAT; USOCR; EPO; JPO	OR	OFF	2004/05/17 01:14
S43	26	"5548783".URPN.	USPAT	OR	ON	2004/05/17 01:15
S44	1	("6895449").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 20:30
S45	388	(710/16).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 21:09
S46	6	(("5915106") or ("5508821") or ("5131089") or ("4642759") or ("5724574") or ("5532825")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 21:22
S47	1	("6895449").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 22:59

S48	0	("(analognear3signal)and(externaln ear5hous\$5)andsensorand(analogn ear3digitalnear3converter)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:07
S49	0	(analognear3signal)and(externalnea r5hous\$5)andsensorand(analognear 3digitalnear3converter)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:10

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Rox 1450 Alexaddria, Virginia 22313-1450

NOTICE OF ALLOWANCE AND FEE(S) DUE

7590

06/20/2006

Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago, IL 60606 EXAMINER

KIM, HAROLD J

ART UNIT

PAPER NUMBER

2181

DATE MAILED: 06/20/2006

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/078.778	03/11/2005	Michael Tasler	9576/96910	8978

TITLE OF INVENTION: FLEXIBLE INTERFACE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$1000	09/20/2006

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. <u>PROSECUTION ON THE MERITS IS CLOSED</u>. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE REFLECTS A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE APPLIED IN THIS APPLICATION. THE PTOL-85B (OR AN EQUIVALENT) MUST BE RETURNED WITHIN THIS PERIOD EVEN IF NO FEE IS DUE OR THE APPLICATION WILL BE REGARDED AS ABANDONED.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL should be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). Even if the fee(s) have already been paid, Part B - Fee(s) Transmittal should be completed and returned. If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or <u>Fax</u> (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks I through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block I, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications. Note: A certificate of mailing can only be used for domestic mailings of the Fec(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission. CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) 7590 06/20/2006 Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below. Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza (Depositor's name Chicago, IL 60606 (Signature (Date) APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 11/078 778 03/11/2005 Michael Tasler 9576/96910 8978 TITLE OF INVENTION: FLEXIBLE INTERFACE APPLN, TYPE SMALL ENTITY **ISSUE FEE PUBLICATION FEE** TOTAL FEE(S) DUE DATE DUE nonprovisional YES \$700 \$300 \$1000 09/20/2006 **EXAMINER** CLASS-SUBCLASS ART UNIT KIM, HAROLD J 710-015000 1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) Please check the appropriate assignce category or categories (will not be printed on the patent): \Box Individual \Box Corporation or other private group entity \Box Government 4a. The following fee(s) are enclosed: 4b. Payment of Fee(s): Issue Fce A check in the amount of the fee(s) is enclosed. Publication Fee (No small entity discount permitted) Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized by charge the required fec(s), or credit any overpayment, to Deposit Account Number ______ (enclose an extra copy of this form). Advance Order - # of Copies 5. Change in Entity Status (from status indicated above) a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2). The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office. Authorized Signature Date Typed or printed name Registration No. _

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
11/078,778	03/11/2005	Michael Tasler	9576/96910	8978	
75	590 06/20/2006		EXAMINER		
Jeffrey W. Salmo	on. Esa.		KIM, HA	ROLD J	
Welsh & Katz, Ltd			ART UNIT PAPER NUMBER		
22nd Floor 120 S. Riverside Pl Chicago, IL 60606			2181 DATE MAILED: 06/20/2000	5	

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 40 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 40 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	P	Y
	Application No.	Applicant(s)
Nation of Allamability	11/078,778	TASLER, MICHAEL
Notice of Allowability	Examiner	Art Unit
	Harold Kim	2181
The MAILING DATE of this communication apperatus All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication IGHTS. This application is subject to	plication. If not included will be mailed in due course. THIS
1. This communication is responsive to the Preliminary American	ndment filed on 3/28/06.	
2. The allowed claim(s) is/are 17-93. [now 1-77].		
 3. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Application No. <u>09</u>	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) ☐ including changes required by the Notice of Draftspers		948) attached
1) hereto or 2) to Paper No./Mail Date	•	,
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date		ffice action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the		
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s)		
1. Notice of References Cited (PTO-892)	5. Notice of Informal P	atent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	 Interview Summary Paper No./Mail Daf 	(PTO-413),
3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 20060328		nent/Comment
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	9 🗆 Other	ent of Reasons for Allowance
	Sypervisory	FRITZ FLEMING PRIMARY EXAMINER 6/11/2006 GROUP 2100 PLY 219

Reasons for Allowance

1. The following is an examiner's statement of reasons for allowance:

The prior art cited on the attached form PTO-892 is the most relevant prior art known. However, Applicant's claimed invention distinguishes over the prior art for the following reasons. The claims are allowable over the prior art of record because none of the references, either alone or in combination, discloses or renders obvious that a analog data generating and processing device comprising: a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signal that are generated by a source that is external to the housing, the sensor to generate sets of analog data from the analog wave signals that is receives, generates a set of digitized analog data from each set of analog data, a first set of instructions being stored in a memory that are utilized by a processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by a personal computer, and a response signal being automatically and without user intervention sent from an input/output port to a multi-purpose interface after an inquiry signal has been received by the input/output port, the receipt and processing of the receipt signal by the personal computer causing it to automatically and without user intervention recognized the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom. The cited references at most, contain only one or the

Application/Control Number: 11/078,778 Page 3

Art Unit: 2181

other, but not all the limitations claimed. There would also be no reason to make any of the possible combinations

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any response to this action should be mailed to:

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The centralized fax number is 571-273-8300.

The centralized hand carry paper drop off location is:

U.S. Patent and Trademark Office

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Randolph Building

401 Dulany Street

Alexandria, VA 22314

Any inquiry of a general nature or relating to the status of this application should be directed to the central telephone number (571) 272-2100.

Application/Control Number: 11/078,778

Art Unit: 2181

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harold Kim whose telephone number is 571-272-4148. The examiner can normally be reached on Monday-Friday 9AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Fritz Fleming, can be reached on 571-272-4145. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Harold J. Kim
Patent Examiner

June 9, 2006/HK

ZTE (USA) 1003, Page 140

FRITZ FLEMING

Spervisory PRIMARY EXAMINER 6/1/2006

GROUP 2100

LIVE STATES

Page 4

Form PTO-1449 (Rev. 8-88) U.S. Department of Commerce Patent and Trademark Office Attorney Docket No. 0757/96910 Serial No. 11/078,778

INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)

applicant.

AFR OF	•	U.S	. PATENT DOCUMENTS	•		
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Examiner	/Harold Ki	m/ (06/09/20	Date Considered /H	arold Kim/	(06/09	/2006)

citation if not in conformance and not considered. Include copy of this form with next communication to

Form PTO-1- (Rev. 8-88)	449	U.S. Department of Commerce Patent and Trademark Office FORMATION DISCLOSURE STATEMENT				Serial No. 11/078,778	
						chael Tasler	ael Tasier
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Examiner	/Harold Kim/	(06/09/2006	Date Considered	/Harold	Kim/	(06/09/2	2006)
*Examiner:	Initial if citation	considered, whether conformance and no	r or not citation is in				

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		NFORMATION DISCLOSURE STATEMENT					Applicant Michael Tasler				
		eets if ned	eets if necessary)		Filing Date			Group No. 2181			
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7772	8.	IEEE Standard	for a Hi	ah Perf	ormance Serial I	Rus 1006 30	2 nn (IFI	FF Std 139	4_1995		
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HK		introduced 10/9	93) 11/29	9/05, 2	pp.				()		
Examiner	/Ha	arold Kim/	(06/09/	/2006)	Date Considered	/Harol	d Kim/	(06/09/	2006)		
Examiner:					or not citation is in ot considered. Incl						

Form PTO- (Rev. 8-88)		U.S. Department of Commerce Patent and Trademark Office	Attorney Docket No. 0757/96910	Serial No. 11/078,778						
		FORMATION DISCLOSURE STATEMENT	Applicant Michael Tasler							
		(Use several sheets if necessary)	Filing Date 03/11/05	Group No. 2181						
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Examiner	/H	arold Kim/ (06/09/2006) Date Considered	/Harold Kim/ (0	5/09/2006)						
*Examiner:		Initial if citation considered, whether or not citation is in citation if not in conformance and not considered. Incluapplicant.								

Form PT (Rev. 8-		9 U.S. Department of Commerce Patent and Trademark Office	Attorney Docket No. 0757/96910	Serial No. 11/078,778				
	IN	FORMATION DISCLOSURE STATEMENT (Use-several sheets if necessary)	Applicant Michael Tasler					
		(Use several sheets if flecessary)	Filing Date 03/11/05	Group No. 2181				
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Notice of References Cited Application/Control No. Applicant(s)/Patent Under Reexamination TASLER, MICHAEL Examiner Harold Kim Art Unit Page 1 of 1

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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.



Application/Control No. 11/078,778	Applicant(s)/Patent under Reexamination TASLER, MICHAEL
Examiner	Art Unit
Harold Kim	2181

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Search Notes



Application/Control No.

Applicant(s)/Patent under Reexamination

TASLER, MICHAEL

Art Unit

Harold Kim

11/078,778 Examiner

2181

	SEARCHED							
Class	Subclass	Date	Examiner					
710	15, 23, 220	6/8/2006	нк					
703	23	6/8/2006	нк					
707	10	6/8/2006	нк					
358	296, 442	6/8/2006	нк					
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Part of Paper No. 20060608



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Bib Data Sheet

CONFIRMATION NO. 8978

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Michael Tasler, Wurzburg, GERMANY; *** CONTINUING DATA **********************************									
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ADDRESS Jeffrey W. Salmon, E Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Pla Chicago, IL60606									
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/06

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

Please enter this supplemental preliminary amendment prior to examination of the abovecaptioned application.

Filed: 03/11/05 Date: June 26, 2006

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IN THE CLAIMS:

Please cancel claims 18, 45 and 70 without prejudice, and please amend claims 17, 44, and 69 as follows:

17. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and,

an the analog data generating and processing device comprising: having an input/output port that is operatively connected to the multi-purpose interface of the personal computer, the analog data generating and processing device including

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives.

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,;

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an input/output port that is adapted to be operatively connected to the multi-

purpose interface of the personal computer, wherein a response signal being is automatically and

without user intervention sent from the input/output port to the multi-purpose interface after they

have been operatively connected together and after an inquiry signal has been received by the

input/output port, the receipt and processing of the receipt signal by the personal computer

causing it to automatically and without user intervention recognize the analog data generating

and processing device as being a device having digital data that is stored therein and selectively

retrievable therefrom,; and

wherein, after the analog data generating and processing device has been

automatically recognized by the personal computer, and while the input/output port is

operatively connected to the multi-purpose interface, user selected ones of the digitized sets of

analog data can be transferred from the memory, through the input/output port, through the

multi-purpose interface, and to the personal computer by means of a driver that is associated with

the personal computer.

18. (cancelled).

19. (currently amended) The combination analog data generating and processing

device-of claim 17, wherein the analog wave signals comprise electromagnetic radiation.

20. (currently amended) The <u>combination analog data generating and processing</u>

device of claim 19, wherein the electromagnetic radiation received by the sensor is representative

of an object that is physically separated from and can be located not in substantial proximity to

the housing.

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- 21. (currently amended) The <u>combination analog data generating and processing</u> device-claim 20, wherein the electromagnetic radiation is generated by a medical device.
- 22. (currently amended) The <u>combination analog data generating and processing</u> device of claim 21, wherein the medical device comprises a diagnostic radiological system.
- 23. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the sensor comprises an electronic measuring device.
- 24. (currently amended) The <u>combination analog data generating and processing</u> device of claim 23, wherein the electronic measuring device comprises a multi-meter.
- 25. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the driver is adapted for use with a mass storage device.
- 26. (currently amended) The <u>combination analog data generating and processing</u> device of claim 25, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 27. (currently amended) The <u>combination analog data generating and processing</u> device of claim 26, wherein the driver is adapted for use with a hard disk drive.
- 28. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the driver is located in a memory of the personal computer.
- 29. (currently amended) The <u>combination analog data generating and processing</u> device of claim 28, wherein the personal computer memory comprises a BIOS of the personal computer.
- 30. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein receipt and processing of the response signal by the personal

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computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.

- 31. (currently amended) The <u>combination analog data generating and processing</u> device of claim 30, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 32. (currently amended) The <u>combination analog data generating and processing</u> device of claim 31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 33. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 20, wherein the processor comprises a digital signal processor.
- 36. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.

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- 37. (currently amended) The <u>combination analog data generating and processing</u> device of claim 36, wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 38. (currently amended) The <u>combination analog data generating and processing</u> device of claim 37, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 39. (currently amended) The <u>combination analog data generating and processing</u> device of claim 38, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 40. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 39, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein a wire based connection is used to operatively connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein a second set of instructions are stored in the memory which are adapted to cause the response signals to be generated.
- 43. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 20, wherein a third set of instructions are stored in the memory that allow user selected ones of the digitized sets of analog data to be transferred to a memory of the personal computer.

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44. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and, the

an analog data generating and processing device comprising: that is operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device including

means for receiving analog wave signals that are generated by a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data; means for causing the digitized sets of analog data to be individually

stored in a memory irrespective of whether or not the analog data generating and processing

device has been recognized by the personal computer,;

means for receiving from the multi-purpose interface of the personal computer the periodic inquiry signals, and for automatically and without user intervention responding thereto the receipt of a periodic inquiry signal from the personal computer by sending a signal to the multi-purpose interface that causes the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored, and

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means for transferring user selected ones of the digitized sets of analog data to the personal computer by means of a driver that is associated with the personal computer.

- 45. (cancelled).
- 46. (currently amended) The <u>combination analog data generating and processing</u> device of claim 44, wherein the analog wave signals comprise electromagnetic radiation.
- 47. (currently amended) The <u>combination analog data generating and processing</u> device of claim 46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.
- 48. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (currently amended) The <u>combination analog data generating and processing</u> device of claim 48, wherein the medical device comprises a diagnostic radiological system.
- 50. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (currently amended) The <u>combination analog data generating and processing</u> device of claim 50, wherein the electronic measuring device comprises a multi-meter.
- 52. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the driver is adapted for use with a mass storage device.

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- 53. (currently amended) The <u>combination analog data generating and processing</u> device of claim 52, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 54. (currently amended) The <u>combination analog data generating and processing</u> device of claim 53, wherein the driver is adapted for use with a hard disk drive.
- 55. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the driver is located in a memory of the personal computer.
- 56. (currently amended) The <u>combination analog data generating and processing</u> device of claim 55, wherein the personal computer memory comprises a BIOS of the personal computer.
- 57. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 47, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 58. (currently amended) The <u>combination analog data generating and processing</u> device of claim 57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 59. (currently amended) The <u>combination analog data generating and processing</u> device of claim 58, wherein receipt and processing of the response signal by the personal

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computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.

- 60. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 61. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the means for receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.
- 62. (currently amended) The <u>combination analog data generating and processing</u> device of claim 61, wherein the means for transferring comprises at least a portion of a digital signal processor.
- 63. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (currently amended) The <u>combination analog data generating and processing</u>
 device-of claim 63 wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 65. (currently amended) The <u>combination analog data generating and processing</u> device of claim 64, wherein at least one of the virtual files comprises a configuration file stored in the memory.

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- 66. (currently amended) The <u>combination analog data generating and processing</u> device of claim 64, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 67. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 68. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein a wire based connection is used to operatively connect the multipurpose interface of the personal computer with the means for receiving from the multipurpose interface.
 - 69. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and, the

an analog data generating and processing device comprising: having a connecting device that is operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals, the analog data generating and processing device including

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals originate from a source that is external to the analog data generating and processing device and that is not located in substantial

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proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer.

a connecting device operatively connected to the processor and the memory, the connecting device being adapted to be operatively connected to the multi-purpose interface of the personal computer and to receive therefrom the periodic inquiry signals;

wherein a response signal is automatically and without user intervention sent to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one <u>periodic</u> inquiry signal therefrom, receipt and processing of the response signal by the personal computer causing the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom.; and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and when the processor and memory are operatively connected to the circuit, user selected ones of the digitized sets of analog data can be transferred to the personal computer by means of a driver that is associated with the personal computer.

70. (cancelled).

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- 71. (currently amended) The <u>combination analog data generating and processing</u> device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.
- 72. (currently amended) The <u>combination analog data generating and processing</u> device of claim 71, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.
- 73. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the electromagnetic radiation is generated by a medical device.
- 74. (currently amended) The <u>combination analog data generating and processing</u> device of claim 73, wherein the medical device comprises a diagnostic radiological system.
- 75. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the sensor comprises an electronic measuring device.
- 76. (currently amended) The <u>combination analog data generating and processing</u> device of claim 75, wherein the electronic measuring device comprises a multi-meter.
- 77. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the driver is adapted for use with a mass storage device.
- 78. (currently amended) The <u>combination analog data generating and processing</u> device of claim 77, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 79. (currently amended) The <u>combination analog data generating and processing</u> device of claim 78, wherein the driver is adapted for use with a hard disk drive.

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- 80. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 72, wherein the driver is located in a memory of the personal computer.
- 81. (currently amended) The <u>combination analog data generating and processing</u> device of claim 80, wherein the personal computer memory comprises a BIOS of the personal computer.
- 82. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 72, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 83. (currently amended) The <u>combination analog data generating and processing</u> device of claim 82, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 84. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 85. (currently amended) of claim 72, wherein the memory of the analog data generating and processing device comprises a buffer memory.

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- 86. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the connecting device is adapted to be operatively connected to a SCSI interface of the personal computer.
- 87. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the processor comprises a digital signal processor.
- 88. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.
- 90. (currently amended) The <u>combination analog data generating and processing</u> device of claim 89, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 91. (currently amended) The <u>combination analog data generating and processing</u> device of claim 90, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 92. (currently amended) The <u>combination analog data generating and processing</u> device of claim 91, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.

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93. (currently amended) The <u>combination analog data generating and processing</u>

device of claim 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

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REMARKS

The purpose of this supplemental preliminary amendment is to amend the claims so that only combination claims are presented for the Examiner's consideration – a combination of a personal computer with an analog data generating and processing device.

It is respectfully submitted that no prior art reference of record, either taken alone or in a purported combination, teaches or suggests the combinations claimed in the currently amended claims, for example, for the reasons stated in the previously submitted preliminary amendment. It also is respectfully submitted that the currently amended claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited. In this regard, the Examiner is respectfully requested to contact the undersigned attorney upon entry of this amendment.

Respectfylly submitted

Attorney for Applicant Registration No. 37,435

June 26, 2006 Welsh & Katz, Ltd. 120 South Riverside Plaza 22nd Floor Chicago, IL 60606 Telephone (312) 655-1500 Facsimile (312) 655-1501

Electronic Acknowledgement Receipt						
EFS ID:	1092231					
Application Number:	11078778					
Confirmation Number:	8978					
Title of Invention:	FLEXIBLE INTERFACE					
First Named Inventor:	Michael Tasler					
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com					
Filer:	Jeffrey W. Salmon/Maura Halvey					
Filer Authorized By:	Jeffrey W. Salmon					
Attorney Docket Number:	9576/96910					
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Time Stamp:	16:35:32					
Application Type:	Utility					
International Application Number:						
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	supp-prel-amendment.pdf	768988	no	17
Warnings:					
Information:					
Total Files Size (in bytes)		7	68988		

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From:

Jeffrey W. Salmon, Esq.

Date: June 30, 2006

To:

Commissioner for Patents

UNITED STATES PATENT & TRADEMARK OFFICE

Washington, D.C. 20231

Attn: Examiner, Mr. Harold J. Kim

Fax:

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PTO/SB/30 (04-05)
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Request	Application Number	11/078,778				
for	Filing Date	03/11/2005 RECEIVED				
Continued Examination (RCE) Transmittal	First Named Invento	Michael Tasler CENTRAL FAX GENTER				
Address to:	Art Unit	2181 JUN 3 () 2006				
Mail Stop RCE Commissioner for Patents	Examiner Name	Harold J. Kim				
P.O. Box 1450 Alexandria, VA 22313-1450	Attorney Docket Nu	mber 9676/96910				
This is a Request for Continued Examination (RCE)						
Request for Continued Examination (RCE) practice under 37 Cl 1995, or to any design application. See Instruction Sheet for RC	FR 1.114 does not apply to	any utility or plant application filed prior to June 8,				
Submission required under 37 CFR 1.114 Not amendments enclosed with the RCE will be entered in the applicant does not wish to have any previously filed unenamendment(s). a. Previously submitted. If a final Office action is considered as a submission even if this box is	e order in which they were stered amendment(s) enter outstanding, any amendme	filed unless applicant instructs otherwise. If ed, applicant must request non-entry of such				
i. Consider the arguments in the Appeal B	nief or Reply Brief previous	ly filed on				
ii. 🔀 Other Supp. Prel. Amendment (fil	ii. Other Supp. Prel. Amendment (filed 6/26/06) a copy attached as Exhibit A					
b. Enclosed						
i. Amendment/Reply	iii. 🔲 Info	rmation Disclosure Statement (IDS)				
ii. Affidavit(s)/ Declaration(s)	iv othe	er				
2. Miscellaneous		•				
Suspension of action on the above-identified application is a. period of months. (Period of suspension b. Other IDS (filed 6/7/06), a copy attach	shall not exceed 3 months; Fe	``				
57	e following fees any under	ne RCE is filed. payment of fees or credit any overpayments to a duplicate copy of this sheet.				
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SIGNATURE OF APPLICAL						
Signature Name (Print/Type) Jeffce W Salmon		Date 30 Ju ← 2006 Registration No. 37,435				
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I hereby certify that this correspondence is being deposited with the United State addressed to: Mail Stop RCE, Commissioner for Patents, P. O. Box 1450, Alexa Office on the date shown betaty.	es Postal Service with sufficient p	ostage as first class mail in an envelope				
Signature Man Aftl. 9						
Name (Print/Type) Maura Halvey		Date 6/30/06				

This collection of information is required by 37 CFR 1.114. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the Individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

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JUN 3 0 2006

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/06

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING

AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

Please enter this supplemental preliminary amendment prior to examination of the abovecaptioned application.

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Applicant: Michael Tasler

Application No.: 11/078,778

Filed: 03/11/05 Date: June 26, 2006

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IN THE CLAIMS:

Please cancel claims 18, 45 and 70 without prejudice, and please amend claims 17, 44, and 69 as follows:

17. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and;

an the analog data generating and processing device comprising: having an input/output port that is operatively connected to the multi-purpose interface of the personal computer, the analog data generating and processing device including

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives.

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer.

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PAGE 5/22 * RCVD AT 6/30/2006 3:56:46 PM [Eastern Daylight Time] * 5VR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID: * DURATION (mm-ss):05-38

Applicant: Michael Tasler Application No.: 11/078,778

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an input/output port that is adapted to be operatively connected to the multi-

purpose interface of the personal computer, wherein a response signal being is automatically and without user intervention sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the input/output port, the receipt and processing of the receipt signal by the personal computer causing it to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom,; and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, user selected ones of the digitized sets of analog data can be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer by means of a driver that is associated with the personal computer.

- 18. (cancelled).
- 19. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (currently amended) The combination analog data generating and processing device of claim 19, wherein the electromagnetic radiation received by the sensor is representative of an object that is physically separated from and can be located not in substantial proximity to the housing.

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Ex. A. 3 of 12 Md99:2 9002 108 NNN PAGE 6/22 * RCVD AT 6/30/2006 3:56:46 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID: * DURATION (mm-ss):05-38

Applicant: Michael Tasler Application No.: 11/078,778

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21. (currently amended) The <u>combination analog data generating and processing</u> device claim 20, wherein the electromagnetic radiation is generated by a medical device.

- 22. (currently amended) The <u>combination analog data generating and-processing</u> device-of claim 21, wherein the medical device comprises a diagnostic radiological system.
- 23. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the sensor comprises an electronic measuring device.
- 24. (currently amended) The <u>combination analog data generating and processing</u> device of claim 23, wherein the electronic measuring device comprises a multi-meter.
- 25. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 20, wherein the driver is adapted for use with a mass storage device.
- 26. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 25, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 27. (currently amended) The <u>combination analog data generating and processing</u> device of claim 26, wherein the driver is adapted for use with a hard disk drive.
- 28. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 20, wherein the driver is located in a memory of the personal computer.
- 29. (currently amended) The <u>combination analog data-generating and processing</u>

 device-of claim 28, wherein the personal computer memory comprises a BIOS of the personal computer.
- 30. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 20, wherein receipt and processing of the response signal by the personal

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Applicant: Michael Tasler Application No.: 11/078,778

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computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.

31. (currently amended) The <u>combination analog data generating and processing</u> device of claim 30, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

- 32. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 33. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the processor comprises a digital signal processor.
- 36. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.

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> 37. (currently amended) The combination analog data generating and processing

device of claim 36, wherein a root directory and virtual files are created in the memory which

can be accessed by the personal computer.

38. (currently amended) The combination analog data generating and processing

device-of claim 37, wherein at least one of the virtual files comprises a configuration file stored

in the memory.

39. (currently amended) The combination analog data-generating and processing

device of claim 38, wherein the configuration file allows a user to configure the analog data

generating and processing device as being a specific mass storage device.

40. (currently amended) The combination analog data generating and processing

device of claim 39, wherein the configuration file allows a user to configure the analog data

generating and processing device as being a specific hard disk drive.

41. (currently amended) The combination analog data generating and processing

device of claim 20, wherein a wire based connection is used to operatively connect the

input/output port to the multi-purpose interface of the personal computer.

42. (currently amended) The combination analog data generating and processing

device of claim 20, wherein a second set of instructions are stored in the memory which are

adapted to cause the response signals to be generated.

43. (currently amended) The combination analog data generating and processing

device of claim 20, wherein a third set of instructions are stored in the memory that allow user

selected ones of the digitized sets of analog data to be transferred to a memory of the personal

computer.

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44. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and, the

an analog data generating and processing device comprising: that is operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device including

means for receiving analog wave signals that are generated by a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data;

means for causing the digitized sets of analog data to be individually stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer.

means for receiving from the multi-purpose interface of the personal computer the periodic inquiry signals, and for automatically and without user intervention responding thereto the receipt of a periodic inquiry signal from the personal computer by sending a signal to the multi-purpose interface that causes the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored; and

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means for transferring user selected ones of the digitized sets of analog data to the personal computer by means of a driver that is associated with the personal computer.

45. (cancelled).

46. (currently amended) The <u>combination enalog data generating and processing</u> device of claim 44, wherein the analog wave signals comprise electromagnetic radiation.

47. (currently amended) The <u>combination analog data generating and processing</u> device of claim 46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.

48. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the means for receiving analog wave signals forms a part of a medical device.

- 49. (currently amended) The <u>combination enalog data generating and processing</u> device of claim 48, wherein the medical device comprises a diagnostic radiological system.
- 50. (currently amended) The <u>combination analog data generating and processing</u>

 device-of claim 47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (currently amended) The <u>combination analog data generating and processing</u> device of claim 50, wherein the electronic measuring device comprises a multi-meter.
- 52. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 47, wherein the driver is adapted for use with a mass storage device.

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53. (currently amended) The <u>combination analog data generating and processing</u>
device-of claim 52, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.

- 54. (currently amended) The <u>combination enalog data-generating and processing</u> device-of claim 53, wherein the driver is adapted for use with a hard disk drive.
- 55. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the driver is located in a memory of the personal computer.
- 56. (currently amended) The <u>combination analog-data generating and processing</u> device of claim 55, wherein the personal computer memory comprises a BIOS of the personal computer.
- 57. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 47, wherein receipt and processing of the response signal by the personal
 computer allows it to communicate with the analog data generating and processing device as if it
 were a mass storage device even though it is not a mass storage device.
- 58. (currently amended) The <u>combination analog data generating and processing</u> device of claim 57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 59. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 58, wherein receipt and processing of the response signal by the personal

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computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.

- 60. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 61. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 47, wherein the means for receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.
- 62. (currently amended) The <u>combination analog data-generating and processing</u> device-of claim 61, wherein the means for transferring comprises at least a portion of a digital signal processor.
- 63. (currently amended) The <u>combination enalog data generating and processing</u> device of claim 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (currently amended) The <u>combination enalog data generating and processing</u> device of claim 63 wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 65. (currently amended) The <u>combination analog data-generating and processing</u>
 device-of claim 64, wherein at least one of the virtual files comprises a configuration file stored in the memory.

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EX. A 13 1F 14 Na89:7 9007 08 NN1 ZTE (USA) 1003, Page 180 PAGE 13/22 * RCVD AT 6/30/2006 3:56:46 PM (Eastern Daylight Time] * SVR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID: * DURATION (mm-ss):05-38

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66. (currently amended) The <u>combination analog data generating and processing</u> device of claim 64, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

67. (currently amended) The <u>combination analog data generating and processing</u> device of claim 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.

68. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein a wire based connection is used to operatively connect the multi-purpose interface of the personal computer with the means for receiving from the multi-purpose interface.

69. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and, the

an analog data generating and processing device comprising: having a connecting device that is operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals, the analog data generating and processing device including

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals originate from a source that is external to the analog data generating and processing device and that is not located in substantial

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X. A 11 0F 17 Md89:7 9007 08 NN1 ZTE (USA) 1003, Page 18 PAGE 14/22 * RCVD AT 6/30/2006 3:56:46 PM [Eastern Daylight Time] * SVR: USPTO-EFXRF-1/15 * DNIS: 2738300 * CSID: * DURATION (mm-ss):05-38

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proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,

a connecting device operatively connected to the processor and the memory; the connecting device being adapted to be operatively connected to the multi-purpose interface of the personal computer and to receive therefrom the periodic inquiry signals;

wherein a response signal is automatically and without user intervention sent to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one <u>periodic</u> inquiry signal therefrom, receipt and processing of the response signal by the personal computer causing the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and when the processor and memory are operatively connected to the circuit, user selected ones of the digitized sets of analog data can be transferred to the personal computer by means of a driver that is associated with the personal computer.

70. (cancelled).

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PAGE 15122 * RCVD AT 6/30/2006 3:56:46 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID: * DURATION (mm-ss):05-38

Applicant: Michael Tasler Application No.: 11/078,778

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71. (currently amended) The <u>combination analog data generating and processing</u>
device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.

72. (currently amended) The <u>combination analog data-generating and processing</u> device of claim 71, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.

73. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the electromagnetic radiation is generated by a medical device.

74. (currently amended) The <u>combination analog data generating and processing</u>
device-of claim 73, wherein the medical device comprises a diagnostic radiological system.

75. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the sensor comprises an electronic measuring device.

76. (currently amended) The <u>combination analog data-generating and processing</u> device of claim 75, wherein the electronic measuring device comprises a multi-meter.

77. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the driver is adapted for use with a mass storage device.

78. (currently amended) The <u>combination analog data generating and processing</u>

device of claim 77, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.

79. (currently amended) The combination analog data generating and processing device of claim 78, wherein the driver is adapted for use with a hard disk drive.

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PAGE 16/22 * RCVD AT 6/30/2006 3:56:46 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID: * DURATION (mm-55):05-38

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80. (currently amended) The <u>combination analog-data-generating and processing</u>
device of claim 72, wherein the driver is located in a memory of the personal computer.

- 81. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 80, wherein the personal computer memory comprises a BIOS of the personal computer.
- 82. (currently amended) The combination analog data generating and processing device of claim 72, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 83. (currently amended) The combination analog data generating and processing device of claim 82, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 84. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 85. (currently amended) of claim 72, wherein the memory of the analog data generating and processing device comprises a buffer memory.

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W469:7 9007 08 NMf ZTE (USA) 1003, Page 184

PAGE 17/22 * RCVD AT 6/30/2006 3:56:46 PM [Eastern Daylight Time] * SVR: USPTO-EFXRF-1/15 * DNIS:2738300 * CSID: * DURATION (mm-ss):05-38

Applicant: Michael Tasler Application No.: 11/078,778

Filed: 03/11/05 Date: June 26, 2006

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86. (currently amended) The <u>combination analog data generating and processing</u>

device of claim 72, wherein the connecting device is adapted to be operatively connected to a

SCSI interface of the personal computer.

- 87. (currently amended) The <u>combination analog data-generating and processing</u> device of claim 72, wherein the processor comprises a digital signal processor.
- 88. (currently amended) The combination analog data generating and processing device-of claim 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (currently amended) The <u>combination analog data generating and processing</u>
 device-of claim 88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.
- 90. (currently amended) The <u>combination analog data generating and processing</u>
 device-of claim 89, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 91. (currently amended) The <u>combination analog data generating and processing</u> device of claim 90, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 92. (currently amended) The <u>combination enalog data generating and processing</u> device-of claim 91, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.

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M469:7 9007 08 NNF ZTE (USA) 1003, Page 185

PAGE 18/22 * RCVD AT 6/30/2006 3:56:46 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID: * DURATION (mm-ss):05-38

Applicant: Michael Tasler Application No.: 11/078,778

Filed: 03/11/05 Date: June 26, 2006

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93. (currently amended) The <u>combination analog data generating and processing</u>

device of claim 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

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Ex A 16 OF 17

M469:7 9007 08 NAG ZTE (USA) 1003, Page 186

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Applicant: Michael Tasler Application No.: 11/078,778

Filed: 03/11/05 Date: June 26, 2006

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REMARKS

The purpose of this supplemental preliminary amendment is to amend the claims so that only combination claims are presented for the Examiner's consideration – a combination of a personal computer with an analog data generating and processing device.

It is respectfully submitted that no prior art reference of record, either taken alone or in a purported combination, teaches or suggests the combinations claimed in the currently amended claims, for example, for the reasons stated in the previously submitted preliminary amendment. It also is respectfully submitted that the currently amended claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited. In this regard, the Examiner is respectfully requested to contact the undersigned attorney upon entry of this amendment.

Respectfully submitted

/Salmon Attorney for Applicant

Registration No. 37,435

June 26, 2006 Welsh & Katz, Ltd. 120 South Riverside Plaza 22nd Floor Chicago, IL 60606 Telephone (312) 655-1500 Facsimile (312) 655-1501

ZTE (USA) 1003, Page 187

PATENT APPLICATION FEE DETERMINATION RECORD Effective December 8, 2004 11/078778 11/078778													
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler Group No.: 2181

Serial No.: 11/078,778 Conf. No.: 8978

Filed: 3/11/06 Examiner: Harold J. Kim

For: ANALOG DATA GENERATING

AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

A Notice of Allowance for this case was mailed from the USPTO on June 20, 2006. On June 30, 2006, an RCE request was filed by facsimile, the purpose of which was for the USPTO to consider and act on the supplemental preliminary amendment that was filed by facsimile on June 26, 2006, as well as the IDS that was filed by facsimile on June 7, 2006. Please enter this further supplemental preliminary amendment prior to examination of the above-captioned application in connection with the above-noted RCE request.

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IN THE CLAIMS:

Prior to the entry of this supplemental preliminary amendment, claims 1-16, 18, 45 and 70 were cancelled, which leaves only claims 17, 19-44, 46-69, and 71-93 pending. In addition to such claims, please add claims 94-113 as follows:

- 1-16. (cancelled).
- 17. (previously presented) A combination, comprising:

a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and

an analog data generating and processing device having an input/output port that is operatively connected to the multi-purpose interface of the personal computer, the analog data generating and processing device including

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives,

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data,

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,

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wherein a response signal is automatically and without user intervention

sent from the input/output port to the multi-purpose interface after they have been operatively

connected together and after an inquiry signal has been received by the input/output port, the

receipt and processing of the receipt signal by the personal computer causing it to automatically

and without user intervention recognize the analog data generating and processing device as

being a device having digital data that is stored therein and selectively retrievable therefrom, and

wherein, after the analog data generating and processing device has been

automatically recognized by the personal computer, and while the input/output port is

operatively connected to the multi-purpose interface, user selected ones of the digitized sets of

analog data can be transferred from the memory, through the input/output port, through the

multi-purpose interface, and to the personal computer by means of a driver that is associated with

the personal computer.

- 18. (cancelled).
- 19. (previously presented) The combination of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (previously presented) The combination of claim 19, wherein the electromagnetic radiation received by the sensor is representative of an object that is physically separated from and can be located not in substantial proximity to the housing.
- 21. (previously presented) The combination claim 20, wherein the electromagnetic radiation is generated by a medical device.
- 22. (previously presented) The combination of claim 21, wherein the medical device comprises a diagnostic radiological system.

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- 23. (previously presented) The combination of claim 20, wherein the sensor comprises an electronic measuring device.
- 24. (previously presented) The combination of claim 23, wherein the electronic measuring device comprises a multi-meter.
- 25. (previously presented) The combination of claim 20, wherein the driver is adapted for use with a mass storage device.
- 26. (previously presented) The combination of claim 25, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 27. (previously presented) The combination of claim 26, wherein the driver is adapted for use with a hard disk drive.
- 28. (previously presented) The combination of claim 20, wherein the driver is located in a memory of the personal computer.
- 29. (previously presented) The combination of claim 28, wherein the personal computer memory comprises a BIOS of the personal computer.
- 30. (previously presented) The combination of claim 20, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 31. (previously presented) The combination of claim 30, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

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- 32. (previously presented) The combination of claim 31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 33. (previously presented) The combination of claim 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (previously presented) The combination of claim 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (previously presented) The combination of claim 20, wherein the processor comprises a digital signal processor.
- 36. (previously presented) The combination of claim 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 37. (previously presented) The combination of claim 36, wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 38. (previously presented) The combination of claim 37, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 39. (previously presented) The combination of claim 38, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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- 40. (previously presented) The combination of claim 39, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (previously presented) The combination of claim 20, wherein a wire based connection is used to operatively connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (previously presented) The combination of claim 20, wherein a second set of instructions are stored in the memory which are adapted to cause the response signals to be generated.
- 43. (previously presented) The combination of claim 20, wherein a third set of instructions are stored in the memory that allow user selected ones of the digitized sets of analog data to be transferred to a memory of the personal computer.
 - 44. (previously presented) A combination, comprising:

a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and

an analog data generating and processing device that is operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device including

means for receiving analog wave signals that are generated by a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data,

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means for causing the digitized sets of analog data to be individually stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,

means for automatically and without user intervention responding the receipt of a periodic inquiry signal from the personal computer by sending a signal to the multi-purpose interface that causes the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored, and

means for transferring user selected ones of the digitized sets of analog data to the personal computer by means of a driver that is associated with the personal computer.

- 45. (cancelled).
- 46. (previously presented) The combination of claim 44, wherein the analog wave signals comprise electromagnetic radiation.
- 47. (previously presented) The combination of claim 46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.
- 48. (previously presented) The combination of claim 47, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (previously presented) The combination of claim 48, wherein the medical device comprises a diagnostic radiological system.

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- 50. (previously presented) The combination of claim 47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (previously presented) The combination of claim 50, wherein the electronic measuring device comprises a multi-meter.
- 52. (previously presented) The combination of claim 47, wherein the driver is adapted for use with a mass storage device.
- 53. (previously presented) The combination of claim 52, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 54. (previously presented) The combination of claim 53, wherein the driver is adapted for use with a hard disk drive.
- 55. (previously presented) The combination of claim 47, wherein the driver is located in a memory of the personal computer.
- 56. (previously presented) The combination of claim 55, wherein the personal computer memory comprises a BIOS of the personal computer.
- 57. (previously presented) The combination of claim 47, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 58. (previously presented) The combination of claim 57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

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- 59. (previously presented) The combination of claim 58, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 60. (previously presented) The combination of claim 47, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 61. (previously presented) The combination of claim 47, wherein the means for receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.
- 62. (previously presented) The combination of claim 61, wherein the means for transferring comprises at least a portion of a digital signal processor.
- 63. (previously presented) The combination of claim 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (previously presented) The combination of claim 63 wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 65. (previously presented) The combination of claim 64, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 66. (previously presented) The combination of claim 64, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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- 67. (previously presented) The combination of claim 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 68. (previously presented) The combination of claim 47, wherein a wire based connection is used to operatively connect the multi-purpose interface of the personal computer with the means for receiving from the multi-purpose interface.
 - 69. (previously presented) A combination, comprising:

a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and

an analog data generating and processing device having a connecting device that is operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals, the analog data generating and processing device including

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals originate from a source that is external to the analog data generating and processing device and that is not located in substantial proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data,

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,

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wherein a response signal is automatically and without user intervention

sent to the multi-purpose interface of the personal computer after the connecting device is

operatively connected to the multi-purpose interface and after the connecting device receives at

least one periodic inquiry signal therefrom, receipt and processing of the response signal by the

personal computer causing the personal computer to automatically and without user intervention

recognize the analog data generating and processing device as being a device having digital data

that is stored therein and selectively retrievable therefrom, and

wherein, after the analog data generating and processing device has been

automatically recognized by the personal computer, user selected ones of the digitized sets of

analog data can be transferred to the personal computer by means of a driver that is associated

with the personal computer.

70. (cancelled).

71. (previously presented) The combination of claim 69, wherein the analog wave

signals comprise electromagnetic radiation.

72. (previously presented) The combination of claim 71, wherein the electromagnetic

radiation is representative of an object that is physically separated from and can be located not in

substantial proximity to the analog data generating and processing device.

73. (previously presented) The combination of claim 72, wherein the electromagnetic

radiation is generated by a medical device.

74. (previously presented) The combination of claim 73, wherein the medical device

comprises a diagnostic radiological system.

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- 75. (previously presented) The combination of claim 72, wherein the sensor comprises an electronic measuring device.
- 76. (previously presented) The combination of claim 75, wherein the electronic measuring device comprises a multi-meter.
- 77. (previously presented) The combination of claim 72, wherein the driver is adapted for use with a mass storage device.
- 78. (previously presented) The combination of claim 77, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 79. (previously presented) The combination of claim 78, wherein the driver is adapted for use with a hard disk drive.
- 80. (previously presented) The combination of claim 72, wherein the driver is located in a memory of the personal computer.
- 81. (previously presented) The combination of claim 80, wherein the personal computer memory comprises a BIOS of the personal computer.
- 82. (previously presented) The combination of claim 72, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 83. (previously presented) The combination of claim 82, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

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- 84. (previously presented) The combination of claim 83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 85. (previously presented) The combination of claim 72, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 86. (previously presented) The combination of claim 72, wherein the connecting device is adapted to be operatively connected to a SCSI interface of the personal computer.
- 87. (previously presented) The combination of claim 72, wherein the processor comprises a digital signal processor.
- 88. (previously presented) The combination of claim 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (previously presented) The combination of claim 88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.
- 90. (previously presented) The combination of claim 89, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 91. (previously presented) The combination of claim 90, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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92. (previously presented) The combination of claim 91, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.

93. (previously presented) The combination of claim 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

94. (new) A combination, comprising:

a personal computer (PC) having an operating system, a display, and one or more multi-purpose user interfaces (MPUI), the operating system causing a device identification signal to be periodically sent to each MPUI so that the PC can identify the time when and what type of a device is operatively connected to a particular MPUI, the operating system also being adapted to send a data identification signal to a particular MPUI to which a data storage device is operatively coupled so that a visual representation of the contents of the data storage device can be displayed on the display;

an analog data generating and processing device (ADGPD) having a processor, an I/O port that is operatively connected to an MPUI of the PC, an analog to digital converter, and a memory in which one or more digitized data sets are stored, each one of the digitized data sets being representative of one or more analog wave signals that are generated or reflected by a source that is external to and not in substantial proximity to the ADGPD;

wherein, after a device identification signal has been received and processed by the ADGPD, the ADGPD automatically and without user intervention sends a response signal to

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the MPUI to which its I/O port is operatively coupled to cause the PC to automatically and without user intervention recognize the ADGPD as being a data storage device;

wherein, after a data identification signal has been received and processed by the ADGPD, the ADGPD automatically and without user intervention sends to the MPUI to which the I/O port is operatively coupled information that allows the operating system to create, on the display, a visual representation of the contents of the portion of the memory in which the digitized data sets are or can be stored; and

wherein user selected ones of the digitized data sets can be transferred from the memory and to the MPUI to which the I/O port is operatively coupled by means of a driver that is a part of the operating system when it is installed in the PC for the first time.

- 95. (new) The combination of claim 94, wherein each analog wave signal comprises electromagnetic radiation.
- 96. (new) The combination of claim 95, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the ADGPD.
- 97. (new) The combination claim 96, wherein the electromagnetic radiation is generated by a medical device.
- 98. (new) The combination of claim 97, wherein the medical device comprises a diagnostic radiological system.
- 99. (new) The combination of claim 94, wherein the data storage device comprises a mass storage device.

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- 100. (new) The combination of claim 99, wherein the mass storage device comprises a hard disk drive.
- 101. (new) The combination of claim 94, wherein receipt and processing of the response signal by the PC allows it to communicate with the ADGPD as if it were a mass storage device even though it is not a mass storage device.
- 102. (new) The combination of claim 101, wherein receipt and processing of the response signal by the PC allows it to communicate with the ADGPD device as if it were a hard disk drive even though it is not a hard disk drive.
- 103. (new) The combination of claim 94, the MPUI to which the I/O port is connected comprises a SCSI interface.
- 104. (new) The combination of claim 94, wherein the processor comprises a digital signal processor.
- 105. (new) The combination of claim 94, wherein the information that the ADGPD sends to the PC comprises at least the number of different digitized data sets that are stored in the memory of the ADGPD.
- 106. (new) The combination of claim 105, wherein the information that the ADGPD sends to the PC further comprises a root directory that can be accessed by the PC.
- 107. (new) The combination of claim 106, wherein the information that the ADGPD sends to the PC further comprises a configuration file.
- 108. (new) The combination of claim 94, wherein the I/O port is operatively coupled to a MPUI by a wire-based connection.

Filed: 03/11/05 Date: July 13, 2006

Page - 17 -

- 109. (new) The combination of claim 94, wherein the digitized data sets are stored as individual files in the memory of the ADGPD.
- 110. (new) The combination of claim 109, wherein the individual files form a part of a file system.
- 111. (new) The combination of claim 110, wherein the file system comprises a virtual file system.
- 112. (new) The combination of claim 94, wherein the digitized data sets are generated and stored in the memory independent of then when the I/O port is operatively coupled to an MPUI.
- 113. (new) The combination of claim 112, wherein the digitized data sets are generated and stored in the memory before the time when the I/O port is operatively coupled to an MPUI.

Filed: 03/11/05

Date: July 13, 2006

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REMARKS

After entry of this supplemental preliminary amendment, claims 17, 19-44, 46-69, and 71-113 will be pending. Claims 94-113 are new claims that are being added in connection with this supplemental preliminary amendment. Such claims are similar in scope in material respects to the claims that the Examiner found to be patentable and, therefore, also should be found to be patentable by the Examiner for at least the same reasons as the previously presented claims were deemed to be allowable.

It also is respectfully submitted that all of the currently pending claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited. If the Examiner is of the opinion that a telephone interview would help to expedite the prosecution of the above-captioned application, he is respectfully requested to contact the undersigned attorney at his convenience.

Respectfully submitted,

Jeffrey W. Salmon

Attorney for Applicant

Registration No. 37,435

July 13, 2006

Welsh & Katz, Ltd.

120 South Riverside Plaza, 22nd Floor

Chicago, IL 60606

Telephone (312) 655-1500

Facsimile (312) 655-1501

Electronic Patent Application Fee Transmittal							
Application Number:	11078778						
Filing Date:	11-Mar-2005						
Title of Invention:	FLEXIBLE INTERFACE						
First Named Inventor: Michael Tasler							
Filer: Jeffrey W. Salmon							
Attorney Docket Number: 9576/96910							
Filed as Large Entity							
Utility Filing Fees							
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:							
Pages:							
Claims:							
Claims in excess of 20	1202	1	50	50			
Independent claims in excess of 3	1201	1	200	200			
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:	Patent-Appeals-and-Interference:						
Post-Allowance-and-Post-Issuance: ZTE (USA) 1003, Page 208							

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
	Tota	al in USE	O (\$)	250

Electronic Acknowledgement Receipt						
EFS ID:	1111407					
Application Number:	11078778					
Confirmation Number:	8978					
Title of Invention:	FLEXIBLE INTERFACE					
First Named Inventor:	Michael Tasler					
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com					
Filer:	Jeffrey W. Salmon					
Filer Authorized By:						
Attorney Docket Number:	9576/96910					
Receipt Date:	13-JUL-2006					
Filing Date:	11-MAR-2005					
Time Stamp:	11:47:25					
Application Type:	Utility					
International Application Number:						
Daymant information.	1					

Payment information:

Submitted with Payment	yes	
Payment was successfully received in RAM	\$250	ZTE (USA) 1003, Page 210

RAM confirmation Number	659
Deposit Account	230920

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages			
1	Preliminary Amendment	supprlmamdmnt96910.pdf 873081		no	18			
Warnings:								
Information:								
2	Fee Worksheet (PTO-875)	fee-info.pdf	8239	no	2			
Warnings:								
Information:								
	Total Files Size (in bytes): 881320							

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

Electronic Ac	knowledgement Receipt
EFS ID:	1113272
Application Number:	11078778
Confirmation Number:	8978
Title of Invention:	FLEXIBLE INTERFACE
First Named Inventor:	Michael Tasler
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com
Filer:	Jeffrey W. Salmon/Maura Halvey
Filer Authorized By:	Jeffrey W. Salmon
Attorney Docket Number:	9576/96910
Receipt Date:	14-JUL-2006
Filing Date:	11-MAR-2005
Time Stamp:	12:06:34
Application Type:	Utility
International Application Number:	
Dovment information:	•

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages			
1	Preliminary Amendment	96910secsupprelmamdmt.pd f	899955	no	18			
Warnings:	Warnings:							
Information	Information:							
	Total Files Size (in bytes): 899955							

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("5844961").PN.	USPAT; USOCR; EPO; JPO	OR	OFF	2006/07/20 18:09
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L3	604	(710/15).CCLS.	USPAT; USOCR; EPO; JPO	OR	OFF	2006/07/20 18:09
L4	122	(710/220).CCLS.	USPAT; USOCR; EPO; JPO	OR	OFF	2006/07/20 18:09
S1	1	("6470399").PN.	USPAT; USOCR; EPO; JPO	OR .	OFF	2006/07/20 17:29
S2	814	710/8.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S3	225	710/16.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S4	0	710/321.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S5	0	709/321.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:04
S6	730	709/220.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S 7	328	709/222.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S8	237	710/11.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S9	74	710/12.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S10	15	710/115.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S11	598	710/62.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S12	196	710/63.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S13	180	710/64.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
S14	324	703/23.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05
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S16	220	703/25.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:05

EAST Search History

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; S26	. 5	710/8.ccls. and 703/24.ccls.	USPAT; EPO; JPO	OR	ON	2004/05/17 01:06		
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S30	O	(driver with storage with regardless) same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:09		
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S33	1	(driver with storage) same inquir\$3 same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:10		
S34	0	(driver with storage) same detect\$4 same (virtual near2 system)	USPAT; EPO; JPO	OR	ON	2004/05/17 01:10		
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7/20/06 6:49:21 PM

EAST Search History

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S41	15	("4040014" "4045774" "4425625" "4503288" "4797878" "4989203" "5065427" "5155847" "5355365" "5369700" "5408527" "5452329" "5524047" "5596628" "5628030").PN.	USPAT	OR	ON	2004/05/17 01:14
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S43	26	"5548783".URPN.	USPAT	OR	ON	2004/05/17 01:15
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S46	6	(("5915106") or ("5508821") or ("5131089") or ("4642759") or ("5724574") or ("5532825")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 21:22
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S48	0	("(analognear3signal)and(externaln ear5hous\$5)andsensorand(analogn ear3digitalnear3converter)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:07
S49	0	(analognear3signal)and(externalnea r5hous\$5)andsensorand(analognear 3digitalnear3converter)	US-PGPUB; USPAT; EPO; JPO	OR	ON .	2006/06/08 23:10
S50	0	(analognear3signal)and(externalnea r5hous\$5)andsensorand(analognear 3digitalnear3converter)	US-PGPUB; USPAT; EPO; JPO	OR .	ON	2006/06/08 23:10

EAST Search History

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S51	233402	(analog near3 signal)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:10
S52	1447	(analog near3 signal) and (external near5 hous\$5) and sensor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:11
S53	989	(analog near3 signal) and (external near5 hous\$5) and sensor and converter	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:11
S54	738	(analog near3 signal) and (external near5 hous\$5) and sensor and (digital near3 converter)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:12
S55	72	(analog near3 signal) and (external near5 hous\$5) and sensor and (digital near3 converter) and poll\$4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:13
S56	. 21	S55 and @ad<"19970304"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:14
S57	30	S55 and @rlad<"19970304"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:14
S58	6	S55 and @prad<"19970304"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:14
S59	40	S56 or S57 or S58	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:16
S60	126	710/220.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:17
S61	387	710/16.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:18
S62	216	710/23.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/08 23:18
S63	488	(703/23).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:21
S64	5870	(707/10).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:21
S65	2462	(358/296).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:21

7/20/06 6:49:21 PM

EAST Search History

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S68	8	("5508821").URPN.	USPAT	OR	ON	2006/06/08 23:45
S69	813	(710/15) CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/06/08 23:45
S70	3462	(digital adj camera) with (download\$4 or transfer\$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/09 01:39
S71	164	(digital adj camera) with flash with (download\$4 or transfer\$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON Z	2006/06/09 01:39



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 8978

SERIAL NUMBI 11/078,778			CLASS GRO		GRO	PUP ART UNIT 2181		ATTORNEY DOCKET NO. 9576/96910	
APPLICANTS Michael Tas	sler, V	Vurzburg, GERMANY;	;						
** CONTINUING I	DATA	*******	*						
** FOREIGN APP	LICA	TIONS ************	***						
IF REQUIRED, FO ** 04/08/2005	OREI	GN FILING LICENSE	GRANT	ED** SMALL E	NTITY	**			
							INDEPENDENT CLAIMS 3		
Welsh & Katz, Ltd 22nd Floor	ADDRESS Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza								
TITLE									
ANALOG DATA G	SENE	RATING AND PROCE	SSING	DEVICE FOR	USE W	ITH A F	PERSON	IAL C	OMPUTER
RECEIVED N	No	: Authority has been gi to charge/cro for following	edit DEP	aper 'OSIT ACCOU	NT	1.1 time)	6 Fees (7 Fees (8 Fees (Proce	essing Ext. of

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NOTICE OF ALLOWANCE AND FEE(S) DUE

7590

07/28/2006

Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago, IL 60606 EXAMINER

KIM, HAROLD J

ART UNIT PAPER NUMBER

2101

DATE MAILED: 07/28/2006

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/078,778	03/11/2005	Michael Tasler	9576/96910	8978

TITLE OF INVENTION: ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER

APPLN, TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$0	\$1000	10/30/2006

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

- A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.
- B. If the status above is to be removed, check box 5b on Part B-Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

- A. Pay TOTAL FEE(S) DUE shown above, or
- B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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	ENCE ADDRESS (Note: Use Bl	lock 1 for any change of address)	Fee par	e(s) Transmittal. The ers. Each additiona	is certif I paper	icate cannot be used f	or domestic mailings of the or any other accompanying nt or formal drawing, must
Jeffrey W. Salr Welsh & Katz, I 22nd Floor	mon, Esq. .td.	//2006	I h Sta add trar	Cer ereby certify that th tes Postal Service v dressed to the Mail asmitted to the USP	tificate is Fec(s vith suf Stop TO (57	of Mailing or Trans s) Transmittal is being ficient postage for firs ISSUE FEE address 1) 273-2885, on the d	mission g deposited with the United st class mail in an envelope above, or being facsimile ate indicated below.
120 S. Riverside Chicago, IL 606							(Depositor's name)
8.,							(Signature)
							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	ł	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
11/078,778	03/11/2005		Michael Tasler	<u> </u>		9576/96910	8978
TITLE OF INVENTION	: ANALOG DATA GEN	NERATING AND PROC	ESSING DEVICE FOR U	SE WITH A PERS	ONAL	COMPUTER	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSU	E FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$0 \$1000			10/30/2006
EXAM	INER	ART UNIT	CLASS-SUBCLASS]			
KIM, HA	ROLD J	2181	710-015000	-			
CFR 1.363). Change of corresp Address form PTO/SE "Fee Address" indi	ication (or "Fee Address" 2 or more recent) attach	ange of Correspondence	2. For printing on the (1) the names of up to or agents OR, alternation (2) the name of a sing registered attorney or 2 registered patent attolisted, no name will be	o 3 registered patentively, le firm (having as a agent) and the name orneys or agents. If	it attorn	er a 2	
PLEASE NOTE: Unl	ess an assignee is ident h in 37 CFR 3.11. Comp	ified below, no assignee	THE PATENT (print or ty data will appear on the pT a substitute for filing an (B) RESIDENCE: (CIT)	patent. If an assign assignment.			ocument has been filed for
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This collection of informin application. Confident	ation is required by 37 C iality is governed by 35	CFR 1.311. The information U.S.C. 122 and 37 CFR	on is required to obtain or 1.14. This collection is es	retain a benefit by t	he publ	ic which is to file (and to complete, includin	by the USPTO to process) g gathering, preparing, and

submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
11/078,778	03/11/2005	Michael Tasler	9576/96910	8978
75	590 07/28/2006		EXAM	INER
Jeffrey W. Salmo	on, Esa.		KIM, HA	ROLD J
Welsh & Katz, Ltd			ART UNIT	PAPER NUMBER
22nd Floor		•	2181	
120 S. Riverside P.	laza		DATE MAILED: 07/28/200	6

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 40 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 40 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)
	11/078,778	TASLER, MICHAEL
Notice of Allowability	Examiner	Art Unit
	Harold Kim	2181
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet with to (OR REMAINS) CLOSED in this or other appropriate communic IGHTS. This application is subjusted and MPEP 1308.	is application. If not included cation will be mailed in due course. THIS lect to withdrawal from issue at the initiative
1. This communication is responsive to the Preliminary Amer	ndment filed on7/13/2006, and t	he RCE filed on 6/30/2006.
2. X The allowed claim(s) is/are 17, 19-44, 46-69, 71-93. [now	<u>1-74]</u> .	
 3. Acknowledgment is made of a claim for foreign priority unal All b) Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have Topies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	e been received. e been received in Application N	lo. <u>09/331,002</u> .
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		eply complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner' Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the second of the sheet.	son's Patent Drawing Review (f s Amendment / Comment or in .84(c)) should be written on the c	the Office action of frawings in the front (not the back) of
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
 Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date 20060607; 20060630 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summer Paper No./Ma 7. ☑ Examiner's Am 8. ☑ Examiner's Sta 9. ☐ Other	il Date nendment/Comment stement of Reasons for Allowance FRITZ FLEMING PERVISORY PATENT EXAMINER
		rechnology center,2100 1/24/206

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

- 2. Authorization for this examiner's amendment was given in telephone interviews with Jeffrey W. Salmon, Reg. No. 37,435 on 7/20/2006.
- 3. Cancel claims 94-113.

Reasons for Allowance

4. The following is an examiner's statement of reasons for allowance:

The prior art cited on the attached form PTO-892 is the most relevant prior art known. However, Applicant's claimed invention distinguishes over the prior art for the following reasons. The claims are allowable over the prior art of record because none of the references, either alone or in combination, discloses or renders obvious that a personal computer having at lease one multi-purpose interface to which inquiry signals are periodically sent, and an analog data generating and processing device having an input/output port that is operatively connected to the multi-purpose interface of a personal computer including a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signal that are generated by a source that is external to

Application/Control Number: 11/078,778

Art Unit: 2181

the housing, the sensor to generate sets of analog data from the analog wave signals that is receives, generates a set of digitized analog data from each set of analog data, a first set of instructions being stored in a memory that are utilized by a processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by a personal computer, and a response signal being automatically and without user intervention sent from an input/output port to a multi-purpose interface after an inquiry signal has been received by the input/output port, the receipt and processing of the receipt signal by the personal computer causing it to automatically and without user intervention recognized the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom. The cited references at most, contain only one or the other, but not all the limitations claimed. There would also be no reason to make any of the possible combinations.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harold Kim whose telephone number is 703-305-1948. The examiner can normally be reached on Monday-Friday 9 AM - 5 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Fritz Fleming, can be reached on 571-272-4145. The fax phone numbers for the organization where this application or proceeding is assigned is 571-273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-746-7240.

Harold J. Kim

Patent Examiner

July 20, 2006/HK

FRITZ FLEMING SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100

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Page 4

PAGE 4/4 * RCVD AT 6/7/2006 5:12:24 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-266 * DNIS:2738300 * CSID: * DURATION (mm-ss):01-14

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Issue	Class	ification

Application/Control	No.
11/078,778	

TASLER, MICHAEL
Art Unit
2181

Applicant(s)/Patent under

Reexamination

Examiner Harold Kim

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Application/Control	No.
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Applicant(s)/Patent under Reexamination

TASLER, MICHAEL

11/078,778

Examiner

2181

Art Unit

Harold Kim

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710	15, 23, 220	7/20/2006	нк							
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INTERFERENCE SEARCHED											
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Bib Data Sheet

CONFIRMATION NO. 8978

SERIAL NUMBER 11/078,778	FILING OR 371(c)	C	CLASS 710	GROUP ART UNIT 2181			ATTORNEY DOCKET NO. 9576/96910	
** CONTINUING DATA This application is ** FOREIGN APPLICAT GERMANY 1970 EUROPEAN PAT	CON -f 10/210 105 09/1	15/2002 PA ES MK, M EP98/0118	1/20/2006	ITITY **				
Foreign Priority claimed yes no 35 USC 119 (a-d) conditions met yes yes no Werified and Acknowledged Examiner's Signature Initials STATE OR COUNTRY DRAWING GERMANY SHEETS DRAWING CLAIMS CLAIMS 3								
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IN THE CLAIMS:

Prior to the entry of this supplemental preliminary amendment, claims 1-16, 18, 45 and 70 were cancelled, which leaves only claims 17, 19-44, 46-69, and 71-93 pending. In addition to such claims, please add claims 94-113 as follows:

- 1-16. (cancelled).
- 17. (previously presented) A combination, comprising:

a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and

an analog data generating and processing device having an input/output port that is operatively connected to the multi-purpose interface of the personal computer, the analog data generating and processing device including

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives,

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data,

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,

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wherein a response signal is automatically and without user intervention sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the input/output port, the receipt and processing of the receipt signal by the personal computer causing it to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom, and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, user selected ones of the digitized sets of analog data can be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer by means of a driver that is associated with the personal computer.

- 18. (cancelled).
- 19. (previously presented) The combination of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (previously presented) The combination of claim 19, wherein the electromagnetic radiation received by the sensor is representative of an object that is physically separated from and can be located not in substantial proximity to the housing.
- 21. (previously presented) The combination claim 20, wherein the electromagnetic radiation is generated by a medical device.
- 22. (previously presented) The combination of claim 21, wherein the medical device comprises a diagnostic radiological system.

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- 23. (previously presented) The combination of claim 20, wherein the sensor comprises an electronic measuring device.
- 24. (previously presented) The combination of claim 23, wherein the electronic measuring device comprises a multi-meter.
- 25. (previously presented) The combination of claim 20, wherein the driver is adapted for use with a mass storage device.
- 26. (previously presented) The combination of claim 25, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 27. (previously presented) The combination of claim 26, wherein the driver is adapted for use with a hard disk drive.
- 28. (previously presented) The combination of claim 20, wherein the driver is located in a memory of the personal computer.
- 29. (previously presented) The combination of claim 28, wherein the personal computer memory comprises a BIOS of the personal computer.
- 30. (previously presented) The combination of claim 20, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 31. (previously presented) The combination of claim 30, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

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- 32. (previously presented) The combination of claim 31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 33. (previously presented) The combination of claim 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (previously presented) The combination of claim 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (previously presented) The combination of claim 20, wherein the processor comprises a digital signal processor.
- 36. (previously presented) The combination of claim 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 37. (previously presented) The combination of claim 36, wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 38. (previously presented) The combination of claim 37, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 39. (previously presented) The combination of claim 38, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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- 40. (previously presented) The combination of claim 39, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (previously presented) The combination of claim 20, wherein a wire based connection is used to operatively connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (previously presented) The combination of claim 20, wherein a second set of instructions are stored in the memory which are adapted to cause the response signals to be generated.
- 43. (previously presented) The combination of claim 20, wherein a third set of instructions are stored in the memory that allow user selected ones of the digitized sets of analog data to be transferred to a memory of the personal computer.
 - 44. (previously presented) A combination, comprising:

a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and

an analog data generating and processing device that is operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device including

means for receiving analog wave signals that are generated by a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data,

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means for causing the digitized sets of analog data to be individually stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,

means for automatically and without user intervention responding the receipt of a periodic inquiry signal from the personal computer by sending a signal to the multi-purpose interface that causes the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored, and

means for transferring user selected ones of the digitized sets of analog data to the personal computer by means of a driver that is associated with the personal computer.

- 45. (cancelled).
- 46. (previously presented) The combination of claim 44, wherein the analog wave signals comprise electromagnetic radiation.
- 47. (previously presented) The combination of claim 46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.
- 48. (previously presented) The combination of claim 47, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (previously presented) The combination of claim 48, wherein the medical device comprises a diagnostic radiological system.

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- 50. (previously presented) The combination of claim 47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (previously presented) The combination of claim 50, wherein the electronic measuring device comprises a multi-meter.
- 52. (previously presented) The combination of claim 47, wherein the driver is adapted for use with a mass storage device.
- 53. (previously presented) The combination of claim 52, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 54. (previously presented) The combination of claim 53, wherein the driver is adapted for use with a hard disk drive.
- 55. (previously presented) The combination of claim 47, wherein the driver is located in a memory of the personal computer.
- 56. (previously presented) The combination of claim 55, wherein the personal computer memory comprises a BIOS of the personal computer.
- 57. (previously presented) The combination of claim 47, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 58. (previously presented) The combination of claim 57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

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- 59. (previously presented) The combination of claim 58, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 60. (previously presented) The combination of claim 47, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 61. (previously presented) The combination of claim 47, wherein the means for receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.
- 62. (previously presented) The combination of claim 61, wherein the means for transferring comprises at least a portion of a digital signal processor.
- 63. (previously presented) The combination of claim 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (previously presented) The combination of claim 63 wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 65. (previously presented) The combination of claim 64, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 66. (previously presented) The combination of claim 64, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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- 67. (previously presented) The combination of claim 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 68. (previously presented) The combination of claim 47, wherein a wire based connection is used to operatively connect the multi-purpose interface of the personal computer with the means for receiving from the multi-purpose interface.
 - 69. (previously presented) A combination, comprising:

a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and

an analog data generating and processing device having a connecting device that is operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals, the analog data generating and processing device including

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals originate from a source that is external to the analog data generating and processing device and that is not located in substantial proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data,

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,

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wherein a response signal is automatically and without user intervention

sent to the multi-purpose interface of the personal computer after the connecting device is

operatively connected to the multi-purpose interface and after the connecting device receives at

least one periodic inquiry signal therefrom, receipt and processing of the response signal by the

personal computer causing the personal computer to automatically and without user intervention

recognize the analog data generating and processing device as being a device having digital data

that is stored therein and selectively retrievable therefrom, and

wherein, after the analog data generating and processing device has been

automatically recognized by the personal computer, user selected ones of the digitized sets of

analog data can be transferred to the personal computer by means of a driver that is associated

with the personal computer.

70. (cancelled).

71. (previously presented) The combination of claim 69, wherein the analog wave

signals comprise electromagnetic radiation.

72. (previously presented) The combination of claim 71, wherein the electromagnetic

radiation is representative of an object that is physically separated from and can be located not in

substantial proximity to the analog data generating and processing device.

73. (previously presented) The combination of claim 72, wherein the electromagnetic

radiation is generated by a medical device.

74. (previously presented) The combination of claim 73, wherein the medical device

comprises a diagnostic radiological system.

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- 75. (previously presented) The combination of claim 72, wherein the sensor comprises an electronic measuring device.
- 76. (previously presented) The combination of claim 75, wherein the electronic measuring device comprises a multi-meter.
- 77. (previously presented) The combination of claim 72, wherein the driver is adapted for use with a mass storage device.
- 78. (previously presented) The combination of claim 77, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 79. (previously presented) The combination of claim 78, wherein the driver is adapted for use with a hard disk drive.
- 80. (previously presented) The combination of claim 72, wherein the driver is located in a memory of the personal computer.
- 81. (previously presented) The combination of claim 80, wherein the personal computer memory comprises a BIOS of the personal computer.
- 82. (previously presented) The combination of claim 72, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 83. (previously presented) The combination of claim 82, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.

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- 84. (previously presented) The combination of claim 83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 85. (previously presented) The combination of claim 72, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 86. (previously presented) The combination of claim 72, wherein the connecting device is adapted to be operatively connected to a SCSI interface of the personal computer.
- 87. (previously presented) The combination of claim 72, wherein the processor comprises a digital signal processor.
- 88. (previously presented) The combination of claim 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (previously presented) The combination of claim 88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.
- 90. (previously presented) The combination of claim 89, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 91. (previously presented) The combination of claim 90, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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92. (previously presented) The combination of claim 91, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.

93. (previously presented) The combination of claim 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

94. (new) A combination, comprising:

a personal computer (PC) having an operating system, a display, and one or more multi-purpose user interfaces (MPUI), the operating system causing a device identification signal to be periodically sent to each MPUI so that the PC can identify the time when and what type of a device is operatively connected to a particular MPUI, the operating system also being adapted to send a data identification signal to a particular MPUI to which a data storage device is operatively coupled so that a visual representation of the contents of the data storage device can be displayed on the display;

an analog data generating and processing device (ADGPD) having a processor, an I/O port that is operatively connected to an MPUI of the PC, an analog to digital converter, and a memory in which one or more digitized data sets are stored, each one of the digitized data sets being representative of one or more analog wave signals that are generated or reflected by a source that is external to and not in substantial proximity to the ADGPD;

wherein, after a device identification signal has been received and processed by the ADGPD, the ADGPD automatically and without user intervention sends a response signal to

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the MPUI to which its I/O port is operatively coupled to cause the PC to automatically and without user intervention recognize the ADGPD as being a data storage device;

wherein, after a data identification signal has been received and processed by the ADGPD, the ADGPD automatically and without user intervention sends to the MPUI to which the I/O port is operatively coupled information that allows the operating system to create, on the display, a visual representation of the contents of the portion of the memory in which the digitized data sets are or can be stored; and

wherein user selected ones of the digitized data sets can be transferred from the memory and to the MPUI to which the I/O port is operatively coupled by means of a driver that is a part of the operating system when it is installed in the PC for the first time.

- 95. (new) The combination of claim 94, wherein each analog wave signal comprises electromagnetic radiation.
- 96. (new) The combination of claim 95, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the ADGPD.
- 97. (new) The combination claim 96, wherein the electromagnetic radiation is generated by a medical device.
- 98. (new) The combination of claim 97, wherein the medical device comprises a diagnostic radiological system.
- 99. (new) The combination of claim 94, wherein the data storage device comprises a mass storage device.

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- 100. (new) The combination of claim 99, wherein the mass storage device comprises a hard disk drive.
- 101. (new) The combination of claim 94, wherein receipt and processing of the response signal by the PC allows it to communicate with the ADGPD as if it were a mass storage device even though it is not a mass storage device.
- 102. (new) The combination of claim 101, wherein receipt and processing of the response signal by the PC allows it to communicate with the ADGPD device as if it were a hard disk drive even though it is not a hard disk drive.
- 103. (new) The combination of claim 94, the MPUI to which the I/O port is connected comprises a SCSI interface.
- 104. (new) The combination of claim 94, wherein the processor comprises a digital signal processor.
- 105. (new) The combination of claim 94, wherein the information that the ADGPD sends to the PC comprises at least the number of different digitized data sets that are stored in the memory of the ADGPD.
- 106. (new) The combination of claim 105, wherein the information that the ADGPD sends to the PC further comprises a root directory that can be accessed by the PC.
- 107. (new) The combination of claim 106, wherein the information that the ADGPD sends to the PC further comprises a configuration file.
- 108. (new) The combination of claim 94, wherein the I/O port is operatively coupled to a MPUI by a wire-based connection.

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- 109. (new) The combination of claim 94, wherein the digitized data sets are stored as individual files in the memory of the ADGPD.
- 110. (new) The combination of claim 109, wherein the individual files form a part of a file system.
- 111. (new) The combination of claim 110, wherein the file system comprises a virtual file system.
- 112. (new) The combination of claim 94, wherein the digitized data sets are generated and stored in the memory independent of then when the I/O port is operatively coupled to an MPUI.
- 113. (new) The combination of claim 112, wherein the digitized data sets are generated and stored in the memory before the time when the I/O port is operatively coupled to an MPUI.

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IN THE CLAIMS:

Please cancel claims 18, 45 and 70 without prejudice, and please amend claims 17, 44, and 69 as follows:

17. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and,

an the analog data generating and processing device comprising: having an input/output port that is operatively connected to the multi-purpose interface of the personal computer, the analog data generating and processing device including

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives.

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer.

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an input/output port that is adapted to be operatively connected to the multipurpose interface of the personal computer, wherein a response signal being is automatically and without user intervention sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the input/output port, the receipt and processing of the receipt signal by the personal computer causing it to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom.; and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, user selected ones of the digitized sets of analog data can be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer by means of a driver that is associated with the personal computer.

- 18. (cancelled).
- 19. (currently amended) The <u>combination analog data generating and processing</u> device of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (currently amended) The <u>combination analog data generating and processing</u>

 device-of claim 19, wherein the electromagnetic radiation received by the sensor is representative of an object that is physically separated from and can be located not in substantial proximity to the housing.

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- 21. (currently amended) The <u>combination analog data generating and processing</u> device claim 20, wherein the electromagnetic radiation is generated by a medical device.
- 22. (currently amended) The <u>combination analog data generating and processing</u> device of claim 21, wherein the medical device comprises a diagnostic radiological system.
- 23. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the sensor comprises an electronic measuring device.
- 24. (currently amended) The <u>combination analog data generating and processing</u> device of claim 23, wherein the electronic measuring device comprises a multi-meter.
- 25. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the driver is adapted for use with a mass storage device.
- 26. (currently amended) The <u>combination analog data generating and processing</u>

 device-of claim 25, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 27. (currently amended) The <u>combination analog data generating and processing</u> device of claim 26, wherein the driver is adapted for use with a hard disk drive.
- 28. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the driver is located in a memory of the personal computer.
- 29. (currently amended) The <u>combination analog data generating and processing</u> device of claim 28, wherein the personal computer memory comprises a BIOS of the personal computer.
- 30. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein receipt and processing of the response signal by the personal

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computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.

- 31. (currently amended) The <u>combination analog data generating and processing</u> device of claim 30, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 32. (currently amended) The <u>combination analog data generating and processing</u> device of claim 31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 33. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the processor comprises a digital signal processor.
- 36. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.

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- 37. (currently amended) The <u>combination analog data generating and processing</u> device of claim 36, wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 38. (currently amended) The <u>combination analog data generating and processing</u> device of claim 37, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 39. (currently amended) The <u>combination analog data generating and processing</u> device of claim 38, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 40. (currently amended) The <u>combination analog data generating and processing</u> device of claim 39, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein a wire based connection is used to operatively connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (currently amended) The <u>combination analog data generating and processing</u> device-of claim 20, wherein a second set of instructions are stored in the memory which are adapted to cause the response signals to be generated.
- 43. (currently amended) The <u>combination analog data generating and processing</u> device of claim 20, wherein a third set of instructions are stored in the memory that allow user selected ones of the digitized sets of analog data to be transferred to a memory of the personal computer.

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44. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and, the

an analog data generating and processing device comprising: that is operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device including

means for receiving analog wave signals that are generated by a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data;

means for causing the digitized sets of analog data to be individually stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,;

means for receiving from the multi-purpose interface of the personal computer the periodic inquiry signals, and for automatically and without user intervention responding thereto the receipt of a periodic inquiry signal from the personal computer by sending a signal to the multi-purpose interface that causes the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored, and

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means for transferring user selected ones of the digitized sets of analog data to the personal computer by means of a driver that is associated with the personal computer.

- 45. (cancelled).
- 46. (currently amended) The <u>combination analog data generating and processing</u> device of claim 44, wherein the analog wave signals comprise electromagnetic radiation.
- 47. (currently amended) The <u>combination analog data generating and processing</u> device of claim 46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.
- 48. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (currently amended) The <u>combination analog data generating and processing</u> device of claim 48, wherein the medical device comprises a diagnostic radiological system.
- 50. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 50, wherein the electronic measuring device comprises a multi-meter.
- 52. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the driver is adapted for use with a mass storage device.

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- 53. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 52, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 54. (currently amended) The <u>combination analog data generating and processing</u> device of claim 53, wherein the driver is adapted for use with a hard disk drive.
- 55. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the driver is located in a memory of the personal computer.
- 56. (currently amended) The <u>combination analog data generating and processing</u> device of claim 55, wherein the personal computer memory comprises a BIOS of the personal computer.
- 57. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 47, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 58. (currently amended) The <u>combination analog data generating and processing</u> device of claim 57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 59. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 58, wherein receipt and processing of the response signal by the personal

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computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.

- 60. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 61. (currently amended) The <u>combination analog data generating and processing</u>

 device-of claim 47, wherein the means for receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.
- 62. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 61, wherein the means for transferring comprises at least a portion of a digital signal processor.
- 63. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 63 wherein a root directory and virtual files are created in the memory which can be accessed by the personal computer.
- 65. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 64, wherein at least one of the virtual files comprises a configuration file stored in the memory.

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- 66. (currently amended) The <u>combination analog data generating and processing</u> device of claim 64, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 67. (currently amended) The <u>combination analog data generating and processing</u> device of claim 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 68. (currently amended) The <u>combination analog data generating and processing</u> device of claim 47, wherein a wire based connection is used to operatively connect the multipurpose interface of the personal computer with the means for receiving from the multi-purpose interface.
 - 69. (currently amended) A combination, comprising:

An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which the personal computer sends periodic inquiry signals are periodically sent as to what type of device is operatively connected thereto; and, the

an analog data generating and processing device comprising: having a connecting device that is operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals, the analog data generating and processing device including

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals originate from a source that is external to the analog data generating and processing device and that is not located in substantial

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proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer,;

a connecting device operatively connected to the processor and the memory, the connecting device being adapted to be operatively connected to the multi-purpose interface of the personal computer and to receive therefrom the periodic inquiry signals;

wherein a response signal is automatically and without user intervention sent to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one <u>periodic</u> inquiry signal therefrom, receipt and processing of the response signal by the personal computer causing the personal computer to automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom.; and

wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and when the processor and memory are operatively connected to the circuit, user selected ones of the digitized sets of analog data can be transferred to the personal computer by means of a driver that is associated with the personal computer.

70. (cancelled).

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- 71. (currently amended) The <u>combination analog data generating and processing</u> device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.
- 72. (currently amended) The <u>combination analog data generating and processing</u> device of claim 71, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device.
- 73. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the electromagnetic radiation is generated by a medical device.
- 74. (currently amended) The <u>combination analog data generating and processing</u> device of claim 73, wherein the medical device comprises a diagnostic radiological system.
- 75. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the sensor comprises an electronic measuring device.
- 76. (currently amended) The <u>combination analog data generating and processing</u> device of claim 75, wherein the electronic measuring device comprises a multi-meter.
- 77. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the driver is adapted for use with a mass storage device.
- 78. (currently amended) The <u>combination analog data generating and processing</u> device of claim 77, wherein the driver is adapted for use with a mass storage device that includes a rotatable storage medium.
- 79. (currently amended) The <u>combination analog data generating and processing</u> device of claim 78, wherein the driver is adapted for use with a hard disk drive.

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- 80. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the driver is located in a memory of the personal computer.
- 81. (currently amended) The <u>combination analog data generating and processing</u> device of claim 80, wherein the personal computer memory comprises a BIOS of the personal computer.
- 82. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 72, wherein receipt and processing of the response signal by the personal
 computer allows it to communicate with the analog data generating and processing device as if it
 were a mass storage device even though it is not a mass storage device.
- 83. (currently amended) The <u>combination analog data generating and processing</u>
 device of claim 82, wherein receipt and processing of the response signal by the personal
 computer allows it to communicate with the analog data generating and processing device as if it
 were a mass storage device having a rotatable storage disk even though it is not a mass storage
 device having a rotatable storage disk.
- 84. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive even though it is not a hard disk drive.
- 85. (currently amended) of claim 72, wherein the memory of the analog data generating and processing device comprises a buffer memory.

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- 86. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the connecting device is adapted to be operatively connected to a SCSI interface of the personal computer.
- 87. (currently amended) The <u>combination analog data generating and processing</u> device of claim 72, wherein the processor comprises a digital signal processor.
- 88. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (currently amended) The <u>combination analog data generating and processing</u>

 device of claim 88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.
- 90. (currently amended) The <u>combination analog data generating and processing</u> device of claim 89, wherein at least one of the virtual files comprises a configuration file stored in the memory.
- 91. (currently amended) The <u>combination analog data generating and processing</u> device of claim 90, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 92. (currently amended) The <u>combination analog data generating and processing</u> device of claim 91, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.

Filed: 03/11/05 Date: June 26, 2006

Page - 16 -

93. (currently amended) The <u>combination analog data-generating and processing</u>

device of claim 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

PATENT APPLICATION FEE DETERMINATION RECORD 11/0_78.778													
Effective December 8, 2004									j				
CLAIMS AS FILED - PART I (Column 1) (Column 2)						1	SMALL E	NTITY	OR		THAN ENTITY		
T	DTAL CLAIMS		16	i				RATE	FEE]	RATE	FEE	
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		(Column 1)		(Colum	n 2)	(Column 3)	1 .	SMALL	ENTITY	OR	SMALL		
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on the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "20." ADDIT. FEE OR ADDIT. FEE ADDIT. FEE													
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Electronic Patent Application Fee Transmittal							
Application Number:	11078778						
Filing Date:	11-Mar-2005						
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER						
First Named Inventor: Michael Tasler							
Filer:	Jeffrey W. Salmon						
Attorney Docket Number:	95	76/96910					
Filed as Large Entity							
Utility Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Utility Appl issue fee		1501	1	1400	1400		
Publ. Fee- early, voluntary, or normal		1504	1	7TF (USA)	1003, Page 264		

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Extension-of-Time:							
Miscellaneous:							
Total in USD (\$)							

Electronic Acknowledgement Receipt						
EFS ID:	1133397					
Application Number:	11078778					
Confirmation Number:	8978					
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER					
First Named Inventor:	Michael Tasler					
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com					
Filer:	Jeffrey W. Salmon					
Filer Authorized By:						
Attorney Docket Number:	9576/96910					
Receipt Date:	31-JUL-2006					
Filing Date:	11-MAR-2005					
Time Stamp:	12:01:22					
Application Type:	Utility					
International Application Number:						
Decimand information.						

Payment information:

Submitted with Payment	yes	
Payment was successfully received in RAM	\$1700	ZTE (USA) 1003, Page 266

RAM confirmation Number	772
Deposit Account	230920

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages				
1	Fee Worksheet (PTO-875)	fee-info.pdf	8305	no	2				
Warnings:	Warnings:								
Information	Information:								
Total Files Size (in bytes): 8305									

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Parent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

Note: A certificate of mailing can only be used for domestic mailings of the Foc(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission. CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) 7590 07/28/2006 Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimila transmitted to the USFTO (571) 273-2885, on the date indicated below. Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Maura Halvey Chicago, IL 60606 (Slensture 2006 (Date 27 October APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 03/11/2005 9576/96910 8978 11/078,778 Michael Tasler TITLE OF INVENTION: ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER APPLN. TYPE SMALL ENTITY ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE DATE DUE YES \$700 \$0 \$300 \$1000 10/30/2006 nonprovisional EXAMINER ART UNIT CLASS-SUBCLASS 710-015000 KIM, HAROLD J 2121 Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list WELSH & KATZ, LTD. (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. (2) the name of a single firm (having as a member a Tree Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. Number is required. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignce is identified below, no assignce data will appear on the patent. If an assignce is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) St. Georgen, GERMANY Papst Licensing GmbH +Co. KG Please check the appropriate assignee calegory or categories (will not be printed on the patent): 🔲 Individual 😡 Corporation or other private group entity 🖵 Government 4a. The following fee(s) are submitted: 4b. Payment of Fcc(s): (Please first reapply any previously paid issue fcc shown above) Issue Fee A check is enclosed. Publication Fee (No small entity discount permitted)
Advance Order - # of Copies Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number 230920 (chelose an extra copy of this form). 5. Change in Entity Status (from status indicated above) Exa. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2). NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant, a registered attorney or agent; or the assignce or other party in interest as shown by the records of the United States Patent and Trademark Office. de mon Authorized Signature Date 37,435 Typed or printed name Jeffrey W Salmon Registration No. This collection of information is required by 37 CFR 1.311. The information is required to obtain or remin a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandra, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandra, Virginia 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

9676/96910

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Request 11/078.778 **Application Number** for 03/11/2005 Filing Date Continued Examination (RCE) Michael Tasler First Named Inventor **Transmittal** Address to: 2181 Art Unit Mail Stop RCE Commissioner for Patents Harold J. Kim **Examiner Name** P.O. Box 1450

Attorney Docket Number

This is a Request for Continued Examination (RCE) under 37 CFR 1 .114 of the above-identified application. Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8, 1995, or to any design application. See Instruction Sheet for RCEs (not to be submitted to the USPTO) on page 2.

Alexandria, VA 22313-1450

amendments	n required under 37 CFR 1.114 Note: If the Feed on the enclosed with the RCE will be entered in the order in some some some some some some some some	n which they	y were filed unless applicant instructs otherwise. If				
***************************************	Previously submitted. If a final Office action is outstanding, any amendments filed after the final Office action may be considered as a submission even if this box is not checked.						
i.	Consider the arguments in the Appeal Brief or Re	eply Brief pre	reviously filed on				
ìi.	Other						
b. End	closed						
i	Amendment/Reply	iii. 🛛	Information Disclosure Statement (IDS)				
ii.	Affidavit(s)/ Declaration(s)	iv. 🛚	other Preliminary Amendment				
2. Miscellaneo	ous						
Suspension of	action on the above-identified application is requeste	d under 37 (CFR 1.103(c) for				
a. pe	eriod of months. (Period of suspension shall not	exceed 3 mon	nths; Fee under 37 CFR 1.17(i) required)				
bOt	her						
Th	•	ng fees any	underpayment of fees or credit any overpayments to				
	7	I have encl	closed a duplicate copy of this sheet.				
i. 🔼	RCE fee required under 37 CFR 1.17(e)						
ii L	Extension of time fee (37 CFR 1.136 and 1.17) Other						
b. Cr	neck in the amount of \$						
c. Pa	syment by credit card (Form PTO-2038 enclosed)						
	tion on this form may become public. Credit carc and authorization on PTO-2038.	l informatio	on should not be included on this form. Provide credit				
	A SIGNATURE OF APPLICANT, ATT	ORNEY, OR					
Signature	Jeff W. Heller		Date 19 Dec 2006				
Name (Print/Type)	Jeffrey W. Salmon		Registration No. 37,435				
	CERTIFICATE OF MAILIN						
	orrespondence is being deposited with the United States Postal S RCE, Confinissioner for Patents, P. O.,Box 1450, Alexandria, VA In below.						
Signature	1/ Carfely		1				
Name (Print/Type)	Maura Halvey		Date 6/30/06 / 17 / 17				

This collection of information is required by 37 CFR 1.114. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

NOTICE TO THE EXAMINER UNDER MPEP §1442.04 OF PENDING LITIGATION

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

This is a Notice To The Examiner Under MPEP §1442.04 to disclose the existence of litigation with respect to the grandparent and parent patents of the above-captioned application, US Patent Nos. 6,470,399 and 6,895,449 (the "399 and '449 patents").

On October 16, 2006, Casio, Inc. filed a declaratory judgment action against Papst Licensing in the U.S. District Court for the District of Columbia that was assigned case number 1:06CV01751. Papst Licensing has not yet filed a response to the complaint.

In the complaint, Casio alleges that the '399 and '449 patents are not infringed by the sale of any Casio products. Casio also alleges in the complaint that all claims of the '399 and '449 patents which Papst Licensing asserts are infringed by Casio products are invalid for failure to

comply with the patent laws of the United States, including the requirements of 35 U.S.C. §§ 102, 103 and/or 112.

Papst Licensing disagrees with Casio's assertion about the infringement and/or validity issues with regard to the '399 and '449 patents.

December 19, 2006 Welsh & Katz, Ltd. 120 South Riverside Plaza, 22nd Floor Chicago, IL 60606 Telephone (312) 655-1500 Facsimile (312) 655-1501 Respectfully submitted,

WELSH & KATZ, LTD.

Jeffrey W. Salmon

Reg. No. 37,435

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

PETITION UNDER 37 CFR 1.313(C)(2) FOR WITHDRAWAL FROM ISSUE FOR CONSIDERATION OF AN RCE REQUEST UNDER 37 CFR 1.114

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

This is a petition under 37 CFR 1.313(c)(2) to withdraw the instant application from issue so that the USPTO can consider the RCE request under 37 CFR 1.114 concurrently filed herewith. The petition fee set forth in 37 CFR 1.17(h) is paid in connection with the electronic filing of this petition. A showing of good and sufficient reasons why this petition should be granted follows.

The issue fee for the above-noted application was paid on October 27, 2006. Subsequent to this date, the undersigned attorney became aware of new prior art relating to the patentability of the claims that are to be issued in connection with this application.

After this application issues as a patent, the patent will be asserted against third parties.

Filed: 03/11/05

Date: December 19, 2006

Page - 2 -

Even thought the newly cited art is believed to be cumulative of at least one reference already considered by the Examiner, the newly cited prior art should be given to the Examiner for his consideration to eliminate the possibility of third parties raising inequitable conduct issues with regard to the newly cited art in connection with the enforcement of any patent granted on the instant application. For this reason, it is respectfully requested that this petition be granted so that the Examiner can consider the prior art listed in the IDS filed herewith, and allow the instant application to issue over such prior art.

Respectfully submitted.

Jeffrey W. Salmon Attorney for Applicant Registration No. 37,435

December 19, 2006 Welsh & Katz, Ltd. 120 South Riverside Plaza, 22nd Floor Chicago, IL 60606 Telephone (312) 655-1500 Facsimile (312) 655-1501

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler Group No.: 2181

Serial No.: 11/078,778 Conf. No.: 8978

Filed: 3/11/05 Examiner: Harold J. Kim

For: ANALOG DATA GENERATING

AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. It is submitted that no fees are due in connection with this Information Disclosure Statement because it is being submitted in connection with a Request for Continued Examination.

No inferences should be drawn that the attached list represents a comprehensive investigation, or that any material disclosed is equivalent to the subject invention. In addition, none of the documents that have publication dates prior to the priority date of the above application anticipate the invention in this application.

The cited document(s) disclose numerous specific features. There has been no attempt to list each and every feature disclosed by each document. The Examiner is requested to review the

document(s) and determine the extent of the materiality of the document disclosures with respect

to the present invention.

The discussion of any art and the citation of any document(s) herein or in the papers filed

contemporaneously herewith is not to be construed as an admission that the art or document

disclosure is necessarily within the invention field of endeavor, that the art or document

disclosure is necessarily prior in time to a particular date which may be relevant to the instant

patent application, and/or that the art or document disclosure is otherwise necessarily prior art as

defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is

distinguished over the disclosure of any document or other art, including the disclosures of the

art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the

instant patent application. The recitation herein of the art and document(s) is not to be construed

as an assertion that more pertinent art could not possibly be in existence.

Respectfully submitted,

Dated: December 19, 2006

Enclosures: Form PTO-1449

WELSH & KATZ, LTD.

120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

Telephone: Facsimile:

(312) 655-1500

(312) 655-1501

Form PTO-144 (Rev. 8-88)	49		S. Department tent and Trade	t of Commerce emark Office	Attorney Doc 0757	ket No. 7/96910		Seria	l No. 11/078,7	78
,	INFORMATION DISC	LOSL	JRE STATEN	MENT	Applicant Michael Tasler					
	(Use several sh	(Use several sheets if necessary)			Filing Date	Gr			roup No.	
			U.S.	. PATENT DOCUME	1	11/05			2181	,
		T = 7.2				1	1			
	5,493,335		20/96	Parulski, et al.						
	5,614,948	<u> </u>	25/97	Hannah						
	5,639,606	6/1	7/97	Willey						
	5,712,682	1/2	7/98	Hannah						
	5,784,581	7/2	:1/98	Hannah						400 to 100 to
	5,841,471	11/	24/98	Endsley, et al.						
	5,914,748	6/2	2/99	Parulski, et al.						
	5,923,193	7/1	3/99	Bloch, et al.						
	5,926,208	7/2	0/99	Noonen, et al.					***************************************	***************************************
	5,929,903	7/2	7/99	Kiesow						-
	5,995,080	11/	30/99	Biro, et al.						
	6,005,613	12/	21/99	Endsley, et al.						
	6,026,217	2/1	5/00	Adiletta						
	6,088,532	7/1	1/00	Yamamoto, et a	ıl.					
	6,101,276	8/8	/00	Adeiletta, et al.						
	6,292,589	9/1	8/01	Chow, et al.						
			FOREIG	GN PATENT DOCUM	MENTS				***************************************	
	Document Numbe	<u> </u>	Date	Countr		Class	Subo	alone		slation
	Document Number	-	Date	Codita	у	Class	Subc	1055	Yes	No
		\dashv								
	OTHER [OCU	IMENTS (Incl	luding Author, Title, D	Date, Pertinent	Pages, E	tc.)			
	Universal Serial	Bus S	Specification	," 1.0 Final Draft R	evision, Nove	ember 13,	1995			
	Universal Serial	Bus S	Specification	," Revision 1.0, Jan	nuary 15, 1996	ó			***************************************	***************************************
Examiner	_ !			Date Considered						AMARAS SA SA SA SA SA SA SA SA SA SA SA SA S
*Examiner:				or not citation is in control of considered. Include						

Electronic Patent Application Fee Transmittal								
Application Number:	11078778							
Filing Date:	11	-Mar-2005						
Title of Invention:		ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER						
First Named Inventor/Applicant Name:	Mi	chael Tasler						
Filer:	Jeffrey W. Salmon/Maura Halvey							
Attorney Docket Number:	9576/96910							
Filed as Large Entity								
Utility Filing Fees								
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:								
Pages:								
Claims:								
Miscellaneous-Filing:								
Petition:								
Patent-Appeals-and-Interference:								
Post-Allowance-and-Post-Issuance:								
Extension-of-Time:								

Description	Fee Code	Fee Code Quantity		Sub-Total in USD(\$)	
Miscellaneous:					
Request for continued examination	1801	1	790	790	
) (\$)	790			

Electronic Acknowledgement Receipt						
EFS ID:	1387049					
Application Number:	11078778					
International Application Number:						
Confirmation Number:	8978					
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER					
First Named Inventor/Applicant Name:	Michael Tasler					
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com					
Filer:	Jeffrey W. Salmon/Maura Halvey					
Filer Authorized By:	Jeffrey W. Salmon					
Attorney Docket Number:	9576/96910					
Receipt Date:	19-DEC-2006					
Filing Date:	11-MAR-2005					
Time Stamp:	20:49:46					
Application Type:	Utility					
avment information:						

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$790
RAM confirmation Number	1146 ZTE (USA) 1003, Page 279

Deposit Account 230920

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)	
1	Preliminary Amendment	96910premamendment.pdf	1399908	no	23	
Warnings:						
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler Group No.: 2181

Serial No.: 11/078,778 Conf. No.: 8978

Filed: 3/11/05 Examiner: Harold J. Kim

For: ANALOG DATA GENERATING

AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

The issue fee for the instant application case was paid on October 27, 2006, but it has not yet issued as a patent. A petition for withdrawal from issue under 37 CFR 1.313(c)(2), a Request for Continued Examination under 37 CFR 1.114, and an Information Disclosure Statement are being concurrently filed herewith. It is respectfully submitted that this application be withdrawn from issue, and that this preliminary amendment be entered before consideration of the RCE request.

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IN THE CLAIMS:

Please amend claims 17, 19-44, 46-69 and 71-93 as noted hereinafter:

1-16. (cancelled).

17. (currently amended) An analog data generating and processing device for use with a combination, comprising: a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto, the ; and an analog data generating and processing device having comprising:

interface of the personal computer; the analog data generating and processing device including a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals from that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of

an input/output port that is to be operatively connected to the multi-purpose

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

analog data from the analog wave signals that it receives:

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

a second set of instructions being stored in the memory that are utilized by the processor to cause wherein a response signal to beis automatically and without user intervention

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sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the input/output port, the receipt and processing of the receiptresponse signal informing by the personal computer causing it to that the personal computer can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

a third set of instructions being stored in the memory that are utilized by the processor, wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, to cause user selected ones of the digitized sets of analog data ean-to be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer. by means of a driver that is associated with the personal computer.

- 18. (previously presented) A combination comprising the analog data generating and processing device of claim 17 and a personal computer.
- 19. (currently amended) The combination analog data generating and processing device of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (currently amended) The combination analog data generating and processing device of claim 17,19, wherein the electromagnetic radiation received by the sensor is representative of an object that is physically separated from and can be located not in substantial proximity to the housing, the sensor is adapted to have two-way communication with the personal computer.

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- 21. (currently amended) The eombination analog data generating and processing device claim 17,20, wherein the analog wave signals are electromagnetic radiation is generated by a medical device.
- 22. (currently amended) The eombination analog data generating and processing device of claim 17,21, wherein the processor, memory and circuit form a flexible interface.

 medical device comprises a diagnostic radiological system.
- 23. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the sensor comprises an electronic measuring device.
- 24. (currently amended) The <u>combination analog data generating and processing</u>
 <u>device of claim 17, 23,</u> wherein the <u>electronic measuring device comprises a multi-meter.sensor</u>
 is electrically connected to the processor and the memory by a two-way communication line.
- 25. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the driver is adapted for use with a mass storage device having a rotatable storage medium. digitized sets of analog data are stored in the memory only after the analog data generating and transmitting device is operatively connected to the personal computer.
- 26. (currently amended) The eombination analog data generating and processing device of claim 17,25, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is driver is adapted for use with a mass storage device. that includes a rotatable storage medium.
- 27. (currently amended) The combination analog data generating and processing device of claim 17,26, wherein the driver is adapted for use with response signal is adapted to

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inform the personal computer that the analog data generating and processing device is a hard disk drive.

- 28. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the driver is located in a memory of the personal computer-response signal is adapted to lie to the personal computer about the true nature of the analog data generating and processing device.
- 29. (currently amended) The combination analog data generating and processing device of claim 17,28, wherein the personal computer memory comprises driver is located in a BIOS of the personal computer.
- 30. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device. the analog to digital converter receives power when the digitized sets of analog data are being transferred to the personal computer.
- 31. (currently amended) The eombination analog data generating and processing device of claim 17,30, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk. the sensor receives power when the digitized sets of analog data are being transferred to the personal computer.

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- 32. (currently amended) The eombination analog data generating and processing device of claim 17,31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drivemass storage device even though it is not a hard disk drivemass storage device.
- 33. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein the processor comprises a digital signal processor.
- 36. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 37. (currently amended) The eombination analog data generating and processing device of claim 17,36, wherein a root directory and virtual files are is created in the memory which can be accessed by the personal computer.
- 38. (currently amended) The combination analog data generating and processing device of claim 17,37, wherein at least one of the virtual files comprises a configuration file is stored in the memory.

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39. (currently amended) The eombination analog data generating and processing device of claim 17,38, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

- 40. (currently amended) The combination analog data generating and processing device of claim 17,39, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein a wire based connection is used to operatively connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein a second set of instructions are stored in the memory which are adapted to cause the response signals to be generated, the sensor is detachably coupled to the analog to digital converter.
- 43. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein a third set of instructions are stored in the memory that allow user selected ones of the digitized sets of analog data to be transferred to a memory of the personal computer. wherein the sensor is adapted to provide data to the personal computer.
- 44. (currently amended) An analog data generating and processing device for use with combination, comprising: a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and an analog data generating and processing device that is and capable of being

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operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device including comprising:

means for receiving analog wave signals <u>from that are generated by</u> a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data;

means for causing the digitized sets of analog data to be individually-stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

means for automatically and without user intervention responding to the receipt of a periodic inquiry signal from the personal computer by sending a <u>response</u> signal to the multipurpose interface, the response signal informing a personal computer that eauses the personal computer <u>can to</u> automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored; and

means for <u>utilizing a driver associated with the personal computer to allow</u>

transferring user selected ones of the digitized sets of analog data to <u>be transferred through a</u>

<u>cable that is attached to the multipurpose interface and to the personal computer by means of a driver that is associated with the personal computer.</u>

- 45. (previously presented) A combination comprising the analog data generating and processing device of claim 44 and a personal computer.
- 46. (currently amended) The eombination analog data generating and processing device of claim 44, wherein the analog wave signals comprise electromagnetic radiation.

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- 47. (currently amended) The combination analog data generating and processing device of claim 44,46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device means for receiving is adapted to have two-way communication with a personal computer.
- 48. (currently amended) The combination analog data generating and processing device of claim 44,47, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (currently amended) The eombination analog data generating and processing device of claim 44,48, wherein the digitizing means, causing means and responding means comprise a flexible interface. medical device comprises a diagnostic radiological system.
- 50. (currently amended) The eombination analog data generating and processing device of claim 44,47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (currently amended) The eombination analog data generating and processing device of claim 4450, wherein the electronic measuring device comprises a multimeter-receiving means is electrically connected to the causing means by a two-way communication line.
- 52. (currently amended) The combination analog data generating and processing device of claim 44,52, wherein the driver is adapted for use with a mass storage device causing means is adapted to cause the sets of digitized analog data to be individually stored in the

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memory only after the analog data generating and processing device has been connected to the multi-purpose user interface of the personal computer.

- 53. (currently amended) The eombination analog data generating and processing device of claim 44,52, wherein the driver is adapted for use with response signal is adapted to inform the personal computer that the analog data generating and processing device is a mass storage device that includes a rotatable storage medium.
- 54. (currently amended) The combination analog data generating and processing device of claim 44,53, wherein the driver is adapted for use with response signal is adapted to inform the personal computer that the analog data generating and processing device is a hard disk drive.
- 55. (currently amended) The eombination analog data generating and processing device of claim 44, 47, wherein the driver is located in a memory of the personal computer-response signal is adapted to lie to the personal computer about the true nature of the analog data generating and processing device.
- 56. (currently amended) The combination analog data generating and processing device of claim 44,55, wherein the personal computer memory comprises the driver is located in a BIOS of the personal computer.
- 57. (currently amended) The eombination analog data generating and processing device of claim 44, 47, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device. the means for receiving

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receives power when the digitized sets of analog data are being transferred to a personal computer.

- 58. (currently amended) The combination analog data generating and processing device of claim 44,57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk. the means for digitizing receives power when the digitized sets of analog data are being transferred to a personal computer.
- 59. (currently amended) The combination analog data generating and processing device of claim 44,58, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage devicehard disk drive even though it is not a hard disk drivemass storage device.
- 60. (currently amended) The eombination analog data generating and processing device of claim 44, 47, wherein the memory of the analog data generating and processing device comprises a buffer memory-means for receiving is adapted to provide data to the personal computer.
- 61. (currently amended) The eombination analog data generating and processing device of claim 44, 47, wherein the means for transferring receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.

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- 62. (currently amended) The combination analog data generating and processing device of claim 44,61, wherein the means for transferring comprises at least a portion of a digital signal processor.receiving is detachably coupled to the means for causing.
- 63. (currently amended) The combination analog data generating and processing device of claim 44, 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (currently amended) The combination analog data generating and processing device of claim 44,63 wherein a root directory and virtual files are is created in the memory which can be accessed by the personal computer.
- 65. (currently amended) The combination analog data generating and processing device of claim 44,64, wherein at least one of the virtual files comprises a configuration file is stored in the memory.
- 66. (currently amended) The combination analog data generating and processing device of claim 44, 64, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- device of claim 44, 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive. the means for receiving is adapted to provide analog data to a personal computer.
- 68. (currently amended) The combination analog data generating and processing device of claim 44,47, wherein a wire based connection is used to operatively connect the multi-

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purpose interface of the personal computer with the means for receiving from the multi-purpose interface.

69. (currently amended) An analog data generating and processing device for use with combination, comprising: a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto,; and an analog data generating and processing device having comprising:

a connecting device that is to be operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals; the analog data generating and processing device including

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals that originate from a source that is external to the analog data generating and processing device and that is not located in substantial proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

the processor and memory being adapted to wherein a response signal is automatically and without user intervention send a response signalsent to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one periodic inquiry

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signal therefrom, receipt and processing of the response signal informing by the personal computer eausing the personal computer to that it can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

wherein, after the analog data generating and processing device being adapted to, after it has been automatically recognized by the personal computer, and after the connecting device has been coupled to the multi-purpose interface of the personal computer, utilize a driver associated with the personal computer to allow user selected ones of the digitized sets of analog data toean be transferred to the personal computer. by means of a driver that is associated with the personal computer.

- 70. (previously presented) A combination comprising the analog data generating and processing device of claim 69 and a personal computer.
- 71. (currently amended) The eombination analog data generating and processing device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.
- 72. (currently amended) The combination analog data generating and processing device of claim 69,71, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device the sensor is adapted to have two-way communication with a personal computer.
- 73. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the electromagnetic radiation is analog wave signals are generated by a medical device.

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- 74. (currently amended) The combination analog data generating and processing device of claim 69,73, wherein the connecting device, circuit, processor and memory form a flexible interface. medical device comprises a diagnostic radiological system.
- 75. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the sensor comprises an electronic measuring device.
- 76. (currently amended) The combination analog data generating and processing device of claim 69,75, wherein the electronic measuring device comprises a multi-meter. sensor is electrically connected to the processor and memory by a two-way communication line.
- 77. (currently amended) The eombination analog data generating and processing device of claim 69,77, wherein the sets of digitized analog data are to be individually stored in the memory only after the connecting device has been connected to the multi-purpose interface of the personal computer.
- 78. (currently amended) The eombination analog data generating and processing device of claim 69,77, wherein the driver is adapted for use with response signal is adapted to inform a personal computer that the analog data generating and processing device is a mass storage device. that includes a rotatable storage medium.
- 79. (currently amended) The eombination analog data generating and processing device of claim 69,78, wherein the driver is adapted for use with a hard disk drive. the response signal is adapted to inform a personal computer that the analog data generating and processing device is a hard disk drive.
- 80. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the driver is located in a memory of the personal computer.the

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response signal is adapted to lie to a personal computer about the true nature of the analog data generating and processing device.

- 81. (currently amended) The eombination analog data generating and processing device of claim 69,80, wherein the personal computer memory comprises driver is located in a BIOS of the personal computer.
- 82. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the circuit receives power when the digitized sets of analog data are being transferred to a personal computer receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 83. (currently amended) The eombination analog data generating and processing device of claim 69,82, wherein the sensor receives power when the digitized sets of analog data are being transferred to a personal computer. receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 84. (currently amended) The combination analog data generating and processing device of claim 69,83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage devicehard disk drive even though it is not a mass storage device.hard disk drive.

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- 85. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the memory of the analog data generating and processing device comprises a buffer memory the sensor is detachably coupled to the analog to digital converter.
- 86. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the connecting device is adapted to be operatively connected to a SCSI interface of the personal computer.
- 87. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the processor comprises a digital signal processor sensor is adapted to provide data to the personal computer.
- 88. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (currently amended) The combination analog data generating and processing device of claim 69,88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.
- 90. (currently amended) The eombination analog data generating and processing device of claim 69,89, wherein at least one of the virtual files comprises a configuration file is stored in the memory.
- 91. (currently amended) The eombination analog data generating and processing device of claim 69,90, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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- 92. (currently amended) The combination analog data generating and processing device of claim 69,91, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 93. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

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REMARKS

After the issue fee was paid for this application, but before the application issued as a patent, the undersigned attorney became aware of a number of arguably relevant prior art references that were not of record in this application. All such references are identified on the PTO 1449 form submitted herewith. A petition for withdrawal from issue pursuant to 37 CFR 1.313(c)(2) for consideration of an RCE request, an RCE request pursuant to 37 CFR 1.114, and an Information Disclosure Statement are being filed herewith.

This paragraph provides a further explanation of the "automatically and without user intervention recognize" language of the previously allowed claims. In particular, the use of this claim language indicates, for example, that a PC can automatically and without user intervention recognize that digital data can be selectively transferred from a device to the PC by means of a driver that is both associated with a host device and that is a standard component of practically all host devices. Such drivers are, for example, found in virtually all personal computers that are commercially available at any particular time, and can be, for example, located in a memory of a host device (e.g., BIOS of a PC). Thus, a user <u>does not</u> have to, for example, load an applications level program (e.g., Adobe Photoshop) onto a PC in order for a user to cause a PC to recognize the device. No prior art of record teaches, for example, this subject matter.

It is respectfully submitted that support for the above-noted aspect of the claimed invention is found in the specification of the instant application. See, for example, page 4, paragraph 4, lines 1-6 of the originally filed specification, which refer to "drivers for input/output devices customary in a host device which are found in practically all host devices are, for example, drivers for hard disks, graphics devices or for printer devices."

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All claims listed herein (except claims 18, 45 and 70) have been amended so that they would be directly infringed by the manufacture or sale of an analog data generating and processing device that falls within the scope of claims. Examples of such infringing devices include, for example, digital cameras, digital camcorders, camera phones, and digital devices with a voice recorder function. Such claims also would be directly infringed by, for example, the use of such products by an end user. Claims 18, 45 and 70 claim the combination of a personal computer and an analog data generating and processing device and, therefore, would be first directly infringed by the use of, for example, a digital camera in connection with a personal computer.

Some of the previously allowed dependent claims have been amended to depend directly from an independent claim, not a dependent claim, and refer to information taken from the originally filed specification. Although it is believed to be not necessary to do so, these amendments have been made in order to clarify the claim differentiation issues. In particular, such amendments have been made to make it clear that the scope of the independent claims *should not* be limited to the elements recited in the dependent claims.

It is respectfully submitted that no prior art reference of record, either taken alone or in a purported combination, teaches or suggests the subject matter claimed in the currently pending claims for a number of different reasons. An exemplary analysis is presented hereinafter with respect to the camera that is disclosed in the Polaroid camera manual that previously was considered by the Examiner, and over which the claims were allowed. Again, the Examiner is asked to assume, for the sake of argument, that this manual is prior art, and applicant reserves the rights to contest that the manual is prior art.

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Assuming, for argument's sake, that the camera manual is prior art, it should be noted that, for example, the camera disclosed therein cannot be automatically recognized by a PC as claimed in the previously allowed and currently pending claims. In particular, user intervention always is required to have the Polaroid camera recognized because, for example, a user needs to make sure that the camera's SCSI identification number does not conflict with the ID number of any other device in a daisy chain of which the camera forms a part. Moreover, the camera manual states at page 3 that a user must load an applications level program such as "Adobe Photoshop" or "PDC-2000 Direct" onto a PC in order to "view and manipulate pictures taken with the PDC-2000 camera." For this reason alone, for example, the currently pending claims should be found to be patentable over the Polaroid camera manual (assuming, for argument's sake, that it is prior art).

An Information Disclosure statement is being filed herewith for the Examiner's consideration so that all of the prior art listed therein will be listed on the cover page of any patent that is granted on the instant application. A number of references noted in the IDS (*e.g.*, U.S. Patent Nos. 5,493,335, 6,088,532 and 6,005,613¹) are believed to be cumulative of the Polaroid camera manual discussed above because, for example, a user must load an applications level program onto a PC in order to transfer pictures to the PC just as is the case the camera described in the Polaroid camera manual.

As a first example, Figure 2 of US Patent No. 5,493,335 is a flowchart showing the operation of the camera illustrated in Figure 1, and contains a block that states "initiate computer

A copy of the USB specification referred to in the '613 patent, together with a copy of the final version of the document dated January 13, 1996, is being submitted herewith.

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interface program on computer." Column 3, lines 52-58 of the patent state that, after "the images are captured . . . the interface is initiated through the computer 18 (by appropriate software, which is no part of the invention)." This means that the devices disclosed in the '335 patent require, for example, a user to utilize an applications level program in order for the camera to be recognized by the PC. This is, for example, in direct contrast to the "automatic and without user intervention" element of the previously allowed and currently pending claims. For this reason alone, the currently pending claims should be found to be patentable over the '335 patent.

As a second example, US Patent No. 6,088,532 requires that a user load an applications level program on a PC in order for the device shown in Figure 29 to be recognized by a PC. The patent teaches that a user can utilize a PC to cause a camera to perform various operations. See, for example, column 23, lines 4-9, which state that the camera shown in Figure 29 is "used as a scanner by the external computer, which controls the still video camera to read an image recorded in the electro-developing recording medium 30, and outputs the image into a monitor provided in the computer." The "scan" and "set window" commands discussed later in column 23 of the patent are example of software commands that a user inputs into the PC. The presence of such commands and PC control indicate, for example, that an applications level program must be loaded onto the PC in order to have the Figure 29 device recognized by the PC. This is in direct contrast to the "automatic and without user intervention" feature of the currently pending and previously allowed claims. For this reason, for example, the currently pending claims should be found to be patentable over the '532 patent.

As a third example, the following analysis demonstrates why US Patent No. 6,005,613 requires human intervention in order to have the camera disclosed therein recognized by a PC.

Filed: 03/11/05

Date: December 19, 2006

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Figures 3A and 3B of the '613 patent show illustrations of interfaces that are shown on a computer screen to which the camera disclosed in the patent is connected. Such screen interfaces are not a part of or customary to PCs. The presence of such screen interfaces in the disclosure indicates, for example, that a user must load an applications level software program on the PC to be able to display them. Accordingly, a user cannot transfer pictures from the camera to a PC without loading the applications level program, which is in direct contrast to the "automatic recognition and without user intervention" feature of the previously allowed claims. For this reason, for example, the currently pending claims should be found to be patentable over the '613 patent.

It is respectfully submitted that the currently pending claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited. In this regard, the Examiner is respectfully requested to contact the undersigned attorney upon entry of this supplemental preliminary amendment.

Respectfully submitted.

Teffrey W. Salmon

Attorney for Applicant Registration No. 37,435

December 19, 2006

Welsh & Katz, Ltd.

120 South Riverside Plaza, 22nd Floor

Chicago, IL 60606

Telephone (312) 655-1500

Facsimile (312) 655-1501

Electronic Patent Application Fee Transmittal					
Application Number:	11	11078778			
Filing Date:	11	-Mar-2005			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER				DEVICE FOR
First Named Inventor/Applicant Name:	Michael Tasler				
Filer:	Je	ffrey W. Salmon/N	laura Halvey		
Attorney Docket Number:	9576/96910				
Filed as Large Entity					
Utility Filing Fees					
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:					
Pages:					
Claims:					
Miscellaneous-Filing:					
Petition:					
Petition fee- 37 CFR 1.17(h) (Group III)		1464	1	130	130
Patent-Appeals-and-Interference:					
Post-Allowance-and-Post-Issuance:					
Extension-of-Time: ZTE (USA) 1003, Page 305					

Description	Fee Code Quantity		Amount	Sub-Total in USD(\$)	
Miscellaneous:					
	Tota	al in USE) (\$)	130	

Electronic Acknowledgement Receipt				
EFS ID:	1389319			
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com			
Filer:	Jeffrey W. Salmon/Maura Halvey			
Filer Authorized By:	Jeffrey W. Salmon			
Attorney Docket Number:	9576/96910			
Receipt Date:	20-DEC-2006			
Filing Date:	11-MAR-2005			
Time Stamp: 15:56:48				
Application Type:	Utility			
Pavment information:				

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$130
RAM confirmation Number	388 ZTE (USA) 1003, Page 307

Deposit Account 230920

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)		
1	Fee Worksheet (PTO-06)	fee-info.pdf	8206	no	2		
Warnings:	Warnings:						
Information:							
Total Files Size (in bytes): 8206							

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

FORM	PTO-302
(REV.	12-87)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

DATE	
20-Dec-	O

PATENT WITHDRAWAL NOTICE

15139

The following WITHDRAWAL and SUBSTITUTION, where appropriate, has been made in the issue of Tuesday, January 09, 2007

WITHDRAWAL	SUBSTITUTION			
SERIAL NO. 11/078,778	SERIAL NO.			
7,162,548	PATENT NUMBER			
DRAWINGS 002	DRAWINGS			
710/015	CLASS			
NAME AND ADDRESS MICHAEL TASLER WURZBURG, GERMANY	NAME AND ADDRESS			
ASSIGNEE	ASSIGNEE			
ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER	TITLE			
Mary Louise McAskill, Manager Statistical Analysis Division				



Commissioner for Patents United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

JEFFREY W. SALMON, ESQ. WELSH & KATZ, LTD. 120 S. RIVERSIDE PLAZA, 22ND FLOOR CHICAGO, IL 60606

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DEC 2 0 2006

OFFICE OF PETITIONS

In re Application of Michael Tasler Application No. 11/078,778 Filed: March 11, 2005 Attorney Docket No. 9576/96910

: DECISION GRANTING PETITION : UNDER 37 CFR 1.313(c)(2)

:

This is a decision on the petition, filed December 19, 2006, under 37 CFR 1.313(c)(2) to withdraw the above-identified application from issue after payment of the issue fee.

The petition is GRANTED.

The above-identified application is withdrawn from issue for consideration of a submission under 37 CFR 1.114 (request for continued examination). See 37 CFR 1.313(c)(2).

Petitioner is advised that the issue fee paid on July 31, 2006 in the above-identified application cannot be refunded. If, however, the above-identified application is again allowed, petitioner may request that it be applied towards the issue fee required by the new Notice of Allowance.¹

Telephone inquiries should be directed to the undersigned at (571) 272-3218.

This matter is being referred to Technology Center AU 2181 for processing of the request for continued examination under 37 CFR 1.114 and for consideration of the Information Disclosure Statement.

rrances Hicks Petitions Examiner

Office of Petitions

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¹ The request to apply the issue fee to the new Notice may be satisfied by completing and returning the new Part B – Fee(s) Transmittal Form (along with any balance due at the time of submission). <u>Petitioner is advised that the Issue Fee Transmittal Form must be completed and timely submitted to avoid abandonment.</u>

Commissioner for Patents United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450 www.uspto.gov

Date

December 20, 2006

TO

Director, Office of Patent Publication

FROM

: Office of Petitions

SUBJECT -

Withdrawal from Issue of Application No. 11/078,778

Applicant(s)

: Michael Tasler Application No. : 11/078,778

Filed

: March 11, 2005

The above-identified application has been assigned Patent No. 7,162,548 and an issue date of January 9, 2007.

It is hereby directed that this application be withdrawn from issue at the request of the applicant. Do not refund the issue fee.

The following erratum should be published in the Official Gazette if the above-identified application is published in the OG of January 9, 2007:

"All reference to Patent No. 7,162,548 to Michael Tasler of Germany for ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PESONAL COMPUTER appearing in the Official Gazette of January 9, 2007 should be deleted since no patent was granted."

Petitions Examiner Office of Petitions

cc:

Paul Harrison Deneise Boyd

Mary Louise McAskill

Niomi Farmer

Mary E. Johnson (Cookie)

Duane Davis (CDS)

~		
Ī	FORM (REV.	PTO-30: 12-87)
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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ı	DATE
l	20-Dec-06

PATENT WITHDRAWAL NOTICE

15139

The following WITHDRAWAL and SUBSTITUTION, where appropriate, has been made in the issue of Tuesday, January 09, 2007

WITHDRAWAL	SUBSTITUTION		
SERIAL NO.	SERIAL NO.		
11/078,778			
	·		
PATENT NUMBER	PATENT NUMBER		
7,162,548			
DRAWINGS	DRAWINGS .		
002	·		
·			
CLASS	CLASS		
710/015			
	·		
NAME AND ADDRESS	NAME AND ADDRESS		
MICHAEL TASLER	·		
WURZBURG, GERMANY			
ASSIGNEE	ASSIGNEE		
•			
·.			
•			
TITLE .	TITLE		
ANALOG DATA GENERATING AND			
PROCESSING DEVICE FOR USE WITH A			
PERSONAL COMPUTER			
,			
ADDOUGED			
APPROVED .			
Mary To	uiso McAskill Manager		
Mary Louise McAskill, Manager			
Statistic	cal Analysis Division		

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

Alexandra, VA 22313-1430

Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. It is submitted that no fees are due in connection with this Information Disclosure Statement because a Request for Continued Examination was filed on December 19, 2006, and no USPTO response has yet been received.

No inferences should be drawn that the attached list represents a comprehensive investigation, or that any material disclosed is equivalent to the subject invention. In addition, none of the documents that have publication dates prior to the priority date of the above application anticipate the invention in this application.

The cited document(s) disclose numerous specific features. There has been no attempt to list each and every feature disclosed by each document. The Examiner is requested to review the

document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein or in the papers filed contemporaneously herewith is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosures of the art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the instant patent application. The recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence.

Respectfully submitted,

W. Salmon, Esq.

Dated: December 21, 2006

Enclosures: Form PTO-1449

WELSH & KATZ, LTD.

120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

Telephone:

(312) 655-1500

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Form PTO-1- (Rev. 8-88)	449	U.S. Departmer Patent and Trac	nt of Commerce demark Office	Attorney Do	cket No. 7/96910	Se	ial No. 11/078,1	778
INFORMATION DISCLOSURE STATE				Applicant				
(Use several sheets if necessary			`		Michael Ta Filing Date		Group No.	
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Electronic Acknowledgement Receipt				
EFS ID:	1393246			
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com			
Filer:	Jeffrey W. Salmon			
Filer Authorized By:				
Attorney Docket Number:	9576/96910			
Receipt Date:	21-DEC-2006			
Filing Date:	11-MAR-2005			
Time Stamp:	15:48:08			
Application Type:	Utility			
Payment information:				
Submitted with Payment	no			

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)		
1	Information Disclosure Statement (IDS) Filed	96910IDS.pdf	170718	no	3		
Warnings:							
Information:							

This is not an USPTO supplied IDS fillable form

Total Files Size (in bytes): 170718

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

RECEIVED
DEC 2 1 2006

OFFICE OF PETITIONS

Attorney

Docket No.:

0757/96910

PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

The issue fee for the instant application case was paid on October 27, 2006, but it has not yet issued as a patent. A petition for withdrawal from issue under 37 CFR 1.313(c)(2), a Request for Continued Examination under 37 CFR 1.114, and an Information Disclosure Statement are being concurrently filed herewith. It is respectfully submitted that this application be withdrawn from issue, and that this preliminary amendment be entered before consideration of the RCE request.

Filed: 03/11/05

Date: December 19, 2006

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IN THE CLAIMS:

Please amend claims 17, 19-44, 46-69 and 71-93 as noted hereinafter:

1-16. (cancelled).

17. (currently amended) An analog data generating and processing device for use with a combination, comprising: a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto, the ; and an analog data generating and processing device having comprising:

an input/output port that is to be operatively connected to the multi-purpose interface of the personal computer; the analog data generating and processing device including

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals from that are generated by a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives;

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

a second set of instructions being stored in the memory that are utilized by the processor to cause wherein a response signal to beis automatically and without user intervention

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sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the input/output port, the receipt and processing of the receiptresponse signal informing by the personal computer causing it to that the personal computer can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

a third set of instructions being stored in the memory that are utilized by the processor, wherein, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, to cause user selected ones of the digitized sets of analog data ean to be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer. by means of a driver that is associated with the personal computer.

- 18. (previously presented) A combination comprising the analog data generating and processing device of claim 17 and a personal computer.
- 19. (currently amended) The eombination analog data generating and processing device of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (currently amended) The combination analog data generating and processing device of claim 17,19, wherein the electromagnetic radiation received by the sensor is representative of an object that is physically separated from and can be located not in substantial proximity to the housing, the sensor is adapted to have two-way communication with the personal computer.

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- 21. (currently amended) The combination analog data generating and processing device claim 17,20, wherein the analog wave signals are electromagnetic radiation is generated by a medical device.
- 22. (currently amended) The eombination analog data generating and processing device of claim 17,21, wherein the processor, memory and circuit form a flexible interface.

 medical device comprises a diagnostic radiological system.
- 23. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the sensor comprises an electronic measuring device.
- 24. (currently amended) The combination analog data generating and processing device of claim 17, 23, wherein the electronic measuring device comprises a multi-meter sensor is electrically connected to the processor and the memory by a two-way communication line.
- 25. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein the driver is adapted for use with a mass storage device having a rotatable storage medium. digitized sets of analog data are stored in the memory only after the analog data generating and transmitting device is operatively connected to the personal computer.
- 26. (currently amended) The eombination analog data generating and processing device of claim 17,25, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is driver is adapted for use with a mass storage device, that includes a rotatable storage medium.
- 27. (currently amended) The eombination analog data generating and processing device of claim 17,26, wherein the driver is adapted for use with response signal is adapted to

Filed: 03/11/05

Date: December 19, 2006

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inform the personal computer that the analog data generating and processing device is a hard disk drive.

- 28. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the driver is located in a memory of the personal computer response signal is adapted to lie to the personal computer about the true nature of the analog data generating and processing device.
- 29. (currently amended) The combination analog data generating and processing device of claim 17,28, wherein the personal computer memory comprises driver is located in a BIOS of the personal computer.
- 30. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device the analog to digital converter receives power when the digitized sets of analog data are being transferred to the personal computer.
- 31. (currently amended) The eombination analog data generating and processing device of claim 17,30; wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk. the sensor receives power when the digitized sets of analog data are being transferred to the personal computer.

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Date: December 19, 2006

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- 32. (currently amended) The combination analog data generating and processing device of claim 17,31, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a hard disk drive mass storage device even though it is not a hard disk drive mass storage device.
- 33. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the input/output port is adapted to be operatively connected to a SCSI interface of the personal computer.
- 35. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the processor comprises a digital signal processor.
- 36. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 37. (currently amended) The eombination analog data generating and processing device of claim 17,36, wherein a root directory and virtual files are is created in the memory which can be accessed by the personal computer.
- 38. (currently amended) The combination analog data generating and processing device of claim 17,37, wherein at least one of the virtual files comprises a configuration file is stored in the memory.

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- 39. (currently amended) The eombination analog data generating and processing device of claim 17,38, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 40. (currently amended) The combination analog data generating and processing device of claim 17,39, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein a wire based connection is used to operatively connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (currently amended) The eombination analog data generating and processing device of claim 17, 20, wherein a second set of instructions are stored in the memory which are adapted to cause the response signals to be generated, the sensor is detachably coupled to the analog to digital converter.
- 43. (currently amended) The combination analog data generating and processing device of claim 17, 20, wherein a third set of instructions are stored in the memory that allow user selected ones of the digitized sets of analog data to be transferred to a memory of the personal computer wherein the sensor is adapted to provide data to the personal computer.
- 44. (currently amended) An analog data generating and processing device for use with-combination, comprising: a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto; and an analog data generating and processing device that is and capable of being

Filed: 03/11/05

Date: December 19, 2006

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operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device including comprising:

means for receiving analog wave signals from that are generated by a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data;

means for causing the digitized sets of analog data to be individually stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

means for automatically and without user intervention responding to the receipt of a periodic inquiry signal from the personal computer by sending a response signal to the multi-purpose interface, the response signal informing a personal computer that causes the personal computer can to-automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored; and

means for <u>utilizing a driver associated with the personal computer to allow</u>

transferring user selected ones of the digitized sets of analog data to <u>be transferred through a</u>

<u>cable that is attached to the multipurpose interface and to</u> the personal computer by means of a driver that is associated with the personal computer.

- 45. (previously presented) A combination comprising the analog data generating and processing device of claim 44 and a personal computer.
- 46. (currently amended) The eombination analog data generating and processing device of claim 44, wherein the analog wave signals comprise electromagnetic radiation.

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- 47. (currently amended) The combination analog data generating and processing device of claim 44,46, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device means for receiving is adapted to have two-way communication with a personal computer.
- 48. (currently amended) The embination analog data generating and processing device of claim 44,47, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (currently amended) The eombination analog data generating and processing device of claim 44,48, wherein the digitizing means, causing means and responding means comprise a flexible interface. medical device comprises a diagnostic radiological system.
- 50. (currently amended) The eombination analog data generating and processing device of claim 44,47, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (currently amended) The combination analog data generating and processing device of claim 4450, wherein the electronic measuring device comprises a multimeter receiving means is electrically connected to the causing means by a two-way communication line.
- 52. (currently amended) The combination analog data generating and processing device of claim 44,52; wherein the driver is adapted for use with a mass storage device causing means is adapted to cause the sets of digitized analog data to be individually stored in the

Filed: 03/11/05

Date: December 19, 2006

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memory only after the analog data generating and processing device has been connected to the multi-purpose user interface of the personal computer.

- 53. (currently amended) The eombination analog data generating and processing device of claim 44,52, wherein the driver is adapted for use with response signal is adapted to inform the personal computer that the analog data generating and processing device is a mass storage device that includes a rotatable storage medium.
- 54. (currently amended) The eombination analog data generating and processing device of claim 44,53, wherein the driver is adapted for use with response signal is adapted to inform the personal computer that the analog data generating and processing device is a hard disk drive.
- 55. (currently amended) The eombination analog data generating and processing device of claim 44, 47, wherein the driver is located in a memory of the personal eomputer response signal is adapted to lie to the personal computer about the true nature of the analog data generating and processing device.
- 56. (currently amended) The combination analog data generating and processing device of claim 44,55, wherein the personal computer memory comprises the driver is located in a BIOS of the personal computer.
- 57. (currently amended) The eombination analog data generating and processing device of claim 44, 47, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device the means for receiving

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receives power when the digitized sets of analog data are being transferred to a personal computer.

- 58. (currently amended) The eombination analog data generating and processing device of claim 44,57, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk the means for digitizing receives power when the digitized sets of analog data are being transferred to a personal computer.
- 59. (currently amended) The combination analog data generating and processing device of claim 44,58, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage devicehard disk drive even though it is not a hard disk drivemass storage device.
- device of claim 44, 47, wherein the memory of the analog data generating and processing device comprises a buffer memory means for receiving is adapted to provide data to the personal computer.

- material description of the re-

61. (currently amended) The embination analog data generating and processing device of claim 44, 47, wherein the means for transferring receiving from the multi-purpose interface is adapted to be operatively connected to a SCSI interface of the personal computer.

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- 62. (currently amended) The combination analog data generating and processing device of claim 44,61, wherein the means for transferring comprises at least a portion of a digital signal processor receiving is detachably coupled to the means for causing.
- 63. (currently amended) The eombination analog data generating and processing device of claim 44, 47, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (currently amended) The combination analog data generating and processing device of claim 44,63 wherein a root directory and virtual files are is created in the memory which can be accessed by the personal computer.
- 65. (currently amended) The eombination analog data generating and processing device of claim 44,64, wherein at least one of the virtual files comprises a configuration file is stored in the memory.
- 66. (currently amended) The eombination analog data generating and processing device of claim 44, 64, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 67. (currently amended) The eombination analog data generating and processing device of claim 44, 65, wherein the configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive the means for receiving is adapted to provide analog data to a personal computer.
- 68. (currently amended) The eombination analog data generating and processing device of claim 44,47, wherein a wire based connection is used to operatively connect the multi-

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purpose interface of the personal computer with the means for receiving from the multi-purpose interface.

69. (currently amended) An analog data generating and processing device for use with combination, comprising: a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto,; and an analog data generating and processing device having comprising:

a connecting device that is <u>to be</u> operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals; the analog data generating and processing device including

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals that originate from a source that is external to the analog data generating and processing device and that is not located in substantial proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

automatically and without user intervention send a response signal sent to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one periodic inquiry

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11 3.

signal therefrom, receipt and processing of the response signal informing by the personal computer eausing the personal computer to that it can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

wherein, after the analog data generating and processing device being adapted to, after it has been automatically recognized by the personal computer, and after the connecting device has been coupled to the multi-purpose interface of the personal computer, utilize a driver associated with the personal computer to allow user selected ones of the digitized sets of analog data toean be transferred to the personal computer, by means of a driver that is associated with the personal computer.

- 70. (previously presented) A combination comprising the analog data generating and processing device of claim 69 and a personal computer.
- 71. (currently amended) The eombination analog data generating and processing device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.
- 72. (currently amended) The eombination analog data generating and processing device of claim 69.71, wherein the electromagnetic radiation is representative of an object that is physically separated from and can be located not in substantial proximity to the analog data generating and processing device the sensor is adapted to have two-way communication with a personal computer.
- 73. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the electromagnetic radiation is analog wave signals are generated by a medical device.

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- 74. (currently amended) The combination analog data generating and processing device of claim 69,73, wherein the connecting device, circuit, processor and memory form a flexible interface. medical device comprises a diagnostic radiological system.
- 75. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the sensor comprises an electronic measuring device.
- 76. (currently amended) The eombination analog data generating and processing device of claim 69,75, wherein the electronic measuring device comprises a multi-meter sensor is electrically connected to the processor and memory by a two-way communication line.
- 77. (currently amended) The eombination analog data generating and processing device of claim 69,77, wherein the sets of digitized analog data are to be individually stored in the memory only after the connecting device has been connected to the multi-purpose interface of the personal computer.
- 78. (currently amended) The eombination analog data generating and processing device of claim 69,77, wherein the driver is adapted for use with response signal is adapted to inform a personal computer that the analog data generating and processing device is a mass storage device, that includes a rotatable storage medium.
- 79. (currently amended) The eombination analog data generating and processing device of claim 69,78, wherein the driver is adapted for use with a hard disk drive the response signal is adapted to inform a personal computer that the analog data generating and processing device is a hard disk drive.
- 80. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the driver is located in a memory of the personal computer.the

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response signal is adapted to lie to a personal computer about the true nature of the analog data generating and processing device.

- 81. (currently amended) The combination analog data generating and processing device of claim 69,80, wherein the personal computer memory comprises driver is located in a BIOS of the personal computer.
- 82. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the circuit receives power when the digitized sets of analog data are being transferred to a personal computer receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- device of claim 69,82, wherein the sensor receives power when the digitized sets of analog data are being transferred to a personal computer. receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device having a rotatable storage disk even though it is not a mass storage device having a rotatable storage disk.
- 84. (currently amended) The eombination analog data generating and processing device of claim 69,83, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage devicehard disk drive even though it is not a mass storage device.hard disk drive.

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- 85. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the memory of the analog data generating and processing device comprises a buffer memory the sensor is detachably coupled to the analog to digital converter.
- 86. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the connecting device is adapted to be operatively connected to a SCSI interface of the personal computer.
- 87. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein the processor comprises a digital signal processor sensor is adapted to provide data to the personal computer.
- 88. (currently amended) The combination analog data generating and processing device of claim 69, 72, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (currently amended) The combination analog data generating and processing device of claim 69.88 wherein the processor is adapted to create a root directory and virtual files in the memory which can be accessed by the personal computer.
- 90. (currently amended) The combination analog data generating and processing device of claim 69,89, wherein at least one of the virtual files comprises a configuration file is stored in the memory.
- 91. (currently amended) The eombination analog data generating and processing device of claim 69.90, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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- 92. (currently amended) The combination analog data generating and processing device of claim 69.91, wherein the a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 93. (currently amended) The eombination analog data generating and processing device of claim 69, 72, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.

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REMARKS

After the issue fee was paid for this application, but before the application issued as a patent, the undersigned attorney became aware of a number of arguably relevant prior art references that were not of record in this application. All such references are identified on the PTO 1449 form submitted herewith. A petition for withdrawal from issue pursuant to 37 CFR 1.313(c)(2) for consideration of an RCE request, an RCE request pursuant to 37 CFR 1.114, and an Information Disclosure Statement are being filed herewith.

This paragraph provides a further explanation of the "automatically and without user intervention recognize" language of the previously allowed claims. In particular, the use of this claim language indicates, for example, that a PC can automatically and without user intervention recognize that digital data can be selectively transferred from a device to the PC by means of a driver that is both associated with a host device and that is a standard component of practically all host devices. Such drivers are, for example, found in virtually all personal computers that are commercially available at any particular time, and can be, for example, located in a memory of a host device (e.g., BIOS of a PC). Thus, a user does not have to, for example, load an applications level program (e.g., Adobe Photoshop) onto a PC in order for a user to cause a PC to recognize the device. No prior art of record teaches, for example, this subject matter.

It is respectfully submitted that support for the above-noted aspect of the claimed invention is found in the specification of the instant application. See, for example, page 4, paragraph 4, lines 1-6 of the originally filed specification, which refer to "drivers for input/output devices customary in a host device which are found in practically all host devices are, for example, drivers for hard disks, graphics devices or for printer devices."

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All claims listed herein (except claims 18, 45 and 70) have been amended so that they would be directly infringed by the manufacture or sale of an analog data generating and processing device that falls within the scope of claims. Examples of such infringing devices include, for example, digital cameras, digital camcorders, camera phones, and digital devices with a voice recorder function. Such claims also would be directly infringed by, for example, the use of such products by an end user. Claims 18, 45 and 70 claim the combination of a personal computer and an analog data generating and processing device and, therefore, would be first directly infringed by the use of, for example, a digital camera in connection with a personal computer.

Some of the previously allowed dependent claims have been amended to depend directly from an independent claim, not a dependent claim, and refer to information taken from the originally filed specification. Although it is believed to be not necessary to do so, these amendments have been made in order to clarify the claim differentiation issues. In particular, such amendments have been made to make it clear that the scope of the independent claims should not be limited to the elements recited in the dependent claims.

It is respectfully submitted that no prior art reference of record, either taken alone or in a purported combination, teaches or suggests the subject matter claimed in the currently pending claims for a number of different reasons. An exemplary analysis is presented hereinafter with respect to the camera that is disclosed in the Polaroid camera manual that previously was considered by the Examiner, and over which the claims were allowed. Again, the Examiner is asked to assume, for the sake of argument, that this manual is prior art, and applicant reserves the rights to contest that the manual is prior art.

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Assuming, for argument's sake, that the camera manual is prior art, it should be noted that, for example, the camera disclosed therein cannot be automatically recognized by a PC as claimed in the previously allowed and currently pending claims. In particular, user intervention always is required to have the Polaroid camera recognized because, for example, a user needs to make sure that the camera's SCSI identification number does not conflict with the ID number of any other device in a daisy chain of which the camera forms a part. Moreover, the camera manual states at page 3 that a user must load an applications level program such as "Adobe Photoshop" or "PDC-2000 Direct" onto a PC in order to "view and manipulate pictures taken with the PDC-2000 camera." For this reason alone, for example, the currently pending claims should be found to be patentable over the Polaroid camera manual (assuming, for argument's sake, that it is prior art).

An Information Disclosure statement is being filed herewith for the Examiner's consideration so that all of the prior art listed therein will be listed on the cover page of any patent that is granted on the instant application. A number of references noted in the IDS (e.g., U.S. Patent Nos. 5,493,335, 6,088,532 and 6,005,613¹) are believed to be cumulative of the Polaroid camera manual discussed above because, for example, a user must load an applications level program onto a PC in order to transfer pictures to the PC just as is the case the camera described in the Polaroid camera manual.

As a first example, Figure 2 of US Patent No. 5,493,335 is a flowchart showing the operation of the camera illustrated in Figure 1, and contains a block that states "initiate computer

A copy of the USB specification referred to in the '613 patent, together with a copy of the final version of the document dated January 13, 1996, is being submitted herewith.

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are captured . . . the interface is initiated through the computer 18 (by appropriate software, which is no part of the invention)." This means that the devices disclosed in the '335 patent require, for example, a user to utilize an applications level program in order for the camera to be recognized by the PC. This is, for example, in direct contrast to the "automatic and without user intervention" element of the previously allowed and currently pending claims. For this reason alone, the currently pending claims should be found to be patentable over the '335 patent.

As a second example, US Patent No. 6,088,532 requires that a user load an applications level program on a PC in order for the device shown in Figure 29 to be recognized by a PC. The patent teaches that a user can utilize a PC to cause a camera to perform various operations. See, for example, column 23, lines 4-9, which state that the camera shown in Figure 29 is "used as a scanner by the external computer, which controls the still video camera to read an image lang ang kalonggara 18 jayan propinsi ang Ka recorded in the electro-developing recording medium 30, and outputs the image into a monitor لحامد والمناب المنافرة فيرأ الموسود بتقطيع فيناه المحجود بالأساد بوازوا والموروف الماروب provided in the computer." The "scan" and "set window" commands discussed later in column The same of the same of the same of the 23 of the patent are example of software commands that a user inputs into the PC. The presence of such commands and PC control indicate, for example, that an applications level program must be loaded onto the PC in order to have the Figure 29 device recognized by the PC. This is in direct contrast to the "automatic and without user intervention" feature of the currently pending and previously allowed claims. For this reason, for example, the currently pending claims should be found to be patentable over the '532 patent.

As a third example, the following analysis demonstrates why US Patent No. 6,005,613 requires human intervention in order to have the camera disclosed therein recognized by a PC.

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Figures 3A and 3B of the '613 patent show illustrations of interfaces that are shown on a computer screen to which the camera disclosed in the patent is connected. Such screen interfaces are not a part of or customary to PCs. The presence of such screen interfaces in the disclosure indicates, for example, that a user must load an applications level software program on the PC to be able to display them. Accordingly, a user cannot transfer pictures from the camera to a PC without loading the applications level program, which is in direct contrast to the "automatic recognition and without user intervention" feature of the previously allowed claims. For this reason, for example, the currently pending claims should be found to be patentable over the '613 patent.

1. It is respectfully submitted that the currently pending claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited. In this regard, the Examiner is respectfully requested to contact the undersigned attorney upon entry of this ง ราย ครับ () เป็น โดย เป็น เป็น แบบอย่าง () เป็นเหมือง เป็น คร**ีย**ย ได้ () การครั้งกับ () การ โดย () การ supplemental preliminary amendment.

Respectfully submitted,

Teffrey W. Salmon

Attorney for Applicant

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December 19, 2006 Registration No. 37,435 Welsh & Katz, Ltd.

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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Request 11/078,778 Application Number for 03/11/2005 Filing Date Michael Tasler Continued Examination (RCE) **Transmittal** First Named Inventor Address to: 2181 Art Unit Mail Stop RCE Harold J. Commissioner for Patents **Examiner Name** P.O. Box 1450 9676/96910 Attorney Docket Number Alexandria, VA 22313-1450

This is a Request for Continued Examination (RCE) under 37 CFR 1.114 of the above-identified application.

Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8, 1995, or to any design application. See Instruction Sheet for RCEs (not to be submitted to the USPTO) on page 2.

root, or to dry assign application. See this assist cheer or Note (not to be submitted a	7 810 007 107 011 pago 2.
 Submission required under 37 CFR 1.114 Note: If the RCE is proper, an amendments enclosed with the RCE will be entered in the order in which they were applicant does not wish to have any previously filed unentered amendment(s) enter amendment(s). 	e filed unless applicant instructs otherwise. If
 Previously submitted. If a final Office action is outstanding, any amendment considered as a submission even if this box is not checked. 	ents filed after the final Office action may be
i. Consider the arguments in the Appeal Brief or Reply Brief previou	isty filed on
ii. Other	
b. Enclosed	
i. Amendment/Reply iii. Inf	formation Disclosure Statement (IDS)
ii. Affidavit(s)/ Declaration(s) iv. X otl	her Preliminary Amendment
2. Miscellaneous	
Suspension of action on the above-identified application is requested under 37 CFR	4.403(a) for
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a. period of months. (Period of suspension shall not exceed 3 months; F	ee under 37 CFR 1.17(1) required)
b Other	,m
3. Fees The RCE fee under 37 CFR 1.17(e) is required by 37 CFR 1.114 when The Director is hereby authorized to charge the following fees any under a. Deposit Account No. 23-0920 I have enclosed.	erpayment of fees or credit any overpayments to
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This collection of information is required by 37 CFR 1.114. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Application Number:	11078778				
International Application Number:	·				
Confirmation Number:	8978				
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER				
First Named Inventor/Applicant Name:	Michael Tasler				
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Preliminary Amendment	96910premamendment.pdf	1399908	no	23
Warnings:		· · · · · · · · · · · · · · · · · · ·			
Information:	·				
2	Request for Continued Examination (RCE)	96910rce.pdf	128277	no	1
Warnings:	1				
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3	Miscellaneous Incoming Letter	96910pendinglit.pdf	63112	no	2
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

The issue fee for the instant application case was paid on October 27, 2006, but it has not yet issued as a patent. A petition for withdrawal of this application from issue under 37 CFR 1.313(c)(2) recently was granted so that a Request for Continued Examination under 37 CFR 1.114, a preliminary amendment, and an Information Disclosure Statement could be considered. Please enter this supplemental preliminary amendment in connection with the previously filed RCE request.

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IN THE CLAIMS:

Please amend claims 19-20, 23, 26, 43, 56, 72, 80 and 85, and add new claims 94-109 as noted hereinafter:

1-16. (cancelled).

17. (previously presented) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto, the analog data generating and processing device comprising:

an input/output port that is to be operatively connected to the multi-purpose interface of the personal computer;

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals from a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives;

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

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a second set of instructions being stored in the memory that are utilized by the processor to cause a response signal to be automatically and without user intervention sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the input/output port, the response signal informing the personal computer that the personal computer can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

a third set of instructions being stored in the memory that are utilized by the processor, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, to cause user selected ones of the digitized sets of analog data to be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer.

- 18. (previously presented) A combination comprising the analog data generating and processing device of claim 17 and a personal computer.
- 19. (previously presented) The analog data generating and processing device of claim 17, wherein the analog wave signals comprise electromagnetic radiation.
- 20. (currently amended) The analog data generating and processing device of claim 17, wherein the the sensor is adapted to have two-way communication with the personal computer.
- 21. (previously presented) The analog data generating and processing device claim 17, wherein the analog wave signals are generated by a medical device.

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22. (previously presented) The analog data generating and processing device of claim 17,21, wherein the processor, memory and circuit form a flexible interface.

- 23. (previously presented) The analog data generating and processing device of claim 17, 20, wherein the sensor comprises an electronic measuring device.
- 24. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor is electrically connected to the processor and the memory by a two-way communication line.
- 25. (previously presented) The analog data generating and processing device of claim 17, wherein the digitized sets of analog data are stored in the memory only after the analog data generating and transmitting device is operatively connected to the personal computer.
- 26. (currently amended) The analog data generating and processing device of claim 17, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is is a mass storage device.
- 27. (previously presented) The analog data generating and processing device of claim 17, wherein the response signal is adapted to inform the personal computer that the analog data generating and processing device is a hard disk drive.
- 28. (previously presented) The analog data generating and processing device of claim 17, wherein the response signal is adapted to lie to the personal computer about the true nature of the analog data generating and processing device.
- 29. (previously presented) The analog data generating and processing device of claim 17, wherein the driver is located in a BIOS of the personal computer.

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30. (previously presented) The analog data generating and processing device of claim 17, wherein the analog to digital converter receives power when the digitized sets of analog data are being transferred to the personal computer.

- 31. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor receives power when the digitized sets of analog data are being transferred to the personal computer.
- 32. (previously presented) The analog data generating and processing device of claim 17, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 33. (previously presented) The analog data generating and processing device of claim 17, wherein the memory of the analog data generating and processing device comprises a buffer memory.
- 34. (previously presented) The analog data generating and processing device of claim 17, wherein the input/output port is adapted to be connected to a SCSI interface of the personal computer.
- 35. (previously presented) The analog data generating and processing device of claim 17, wherein the processor comprises a digital signal processor.
- 36. (previously presented) The analog data generating and processing device of claim 17, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.

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37. (previously presented) The analog data generating and processing device of claim 17, wherein a root directory is created in the memory which can be accessed by the personal computer.

- 38. (previously presented) The analog data generating and processing device of claim 17, wherein a configuration file is stored in the memory.
- 39. (previously presented) The analog data generating and processing device of claim 17, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 40. (previously presented) The analog data generating and processing device of claim 17, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (previously presented) The analog data generating and processing device of claim 17, wherein a wire based connection is used to connect the input/output port to the multi-purpose interface of the personal computer.
- 42. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor is detachably coupled to the analog to digital converter.
- 43. (currently amended) The analog data generating and processing device of claim 17, wherein wherein the sensor is adapted to provide data to the personal computer.
- 44. (previously presented) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto and capable of being

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operatively coupled to the multi-purpose interface of the personal computer, the analog data generating and processing device comprising:

means for receiving analog wave signals from a source external to and not located in substantial proximity to the analog data generating and processing device, for generating sets of analog data therefrom, and for digitizing each set of analog data;

means for causing the digitized sets of analog data to be stored in a memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

means for automatically and without user intervention responding to the receipt of a periodic inquiry signal from the personal computer by sending a response signal to the multi-purpose interface, the response signal informing a personal computer that the personal computer can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored in and to be selectively retrievable from a memory in which digital signals are stored; and

means for utilizing a driver associated with the personal computer to allow user selected ones of the digitized sets of analog data to be transferred through a cable that is attached to the multipurpose interface and to the personal computer by means of a driver that is associated with the personal computer.

- 45. (previously presented) A combination comprising the analog data generating and processing device of claim 44 and a personal computer.
- 46. (previously presented) The analog data generating and processing device of claim 44, wherein the analog wave signals comprise electromagnetic radiation.

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47. (previously presented) The analog data generating and processing device of claim 44, wherein the means for receiving is adapted to have two-way communication with a personal computer.

- 48. (previously presented) The analog data generating and processing device of claim 44, wherein the means for receiving analog wave signals forms a part of a medical device.
- 49. (previously presented) The analog data generating and processing device of claim 44, wherein the digitizing means, causing means and responding means comprise a flexible interface.
- 50. (previously presented) The analog data generating and processing device of claim 44, wherein the means for receiving analog wave signals includes an electronic measuring device.
- 51. (previously presented) The analog data generating and processing device of claim 44 receiving means is electrically connected to the causing means by a two-way communication line.
- 52. (previously presented) The analog data generating and processing device of claim 44, wherein the causing means is adapted to cause the sets of digitized analog data to be individually stored in the memory only after the analog data generating and processing device has been connected to the multi-purpose user interface of the personal computer.
- 53. (previously presented) The analog data generating and processing device of claim 44, wherein the response signal is adapted to inform the personal computer that the analog data generating and processing device is a mass storage device.

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54. (previously presented) The analog data generating and processing device of claim 44, wherein the response signal is adapted to inform the personal computer that the analog data generating and processing device is a hard disk drive.

- 55. (previously presented) The analog data generating and processing device of claim 44, wherein the response signal is adapted to lie to the personal computer about the true nature of the analog data generating and processing device.
- 56. (currently amended) The analog data generating and processing device of claim 44, wherein the the-driver is located in a BIOS of the personal computer.
- 57. (previously presented) The analog data generating and processing device of claim 44, wherein the means for receiving receives power when the digitized sets of analog data are being transferred to a personal computer.
- 58. (previously presented) The analog data generating and processing device of claim 44, wherein the means for digitizing receives power when the digitized sets of analog data are being transferred to a personal computer.
- 59. (previously presented) The analog data generating and processing device of claim 44, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 60. (previously presented) The analog data generating and processing device of claim 44, wherein the means for receiving is adapted to provide data to the personal computer.

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- 61. (previously presented) The analog data generating and processing device of claim 44, wherein the means for transferring is adapted to be connected to a SCSI interface of the personal computer.
- 62. (previously presented) The analog data generating and processing device of claim 44, wherein the means for receiving is detachably coupled to the means for causing.
- 63. (previously presented) The analog data generating and processing device of claim 44, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 64. (previously presented) The analog data generating and processing device of claim 44, wherein a root directory is created in the memory which can be accessed by the personal computer.
- 65. (previously presented) The analog data generating and processing device of claim 44, wherein a configuration file is stored in the memory.
- 66. (previously presented) The analog data generating and processing device of claim 44, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 67. (previously presented) The analog data generating and processing device of claim 44, wherein the means for receiving is adapted to provide analog data to a personal computer.
- 68. (previously presented) The analog data generating and processing device of claim 44, wherein a wire based connection is used to operatively connect the multi-purpose interface of the personal computer with the means for receiving from the multi-purpose interface.

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69. (previously presented) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto, comprising:

a connecting device that is to be operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals;

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals that originate from a source that is external to the analog data generating and processing device and that is not located in substantial proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

the processor and memory being adapted to automatically and without user intervention send a response signal to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one periodic inquiry signal therefrom, the response signal informing the personal computer that it can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

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the analog data generating and processing device being adapted to, after it has been automatically recognized by the personal computer, and after the connecting device has been coupled to the multi-purpose interface of the personal computer, utilize a driver associated with the personal computer to allow user selected ones of the digitized sets of analog data to be transferred to the personal computer.

- 70. (previously presented) A combination comprising the analog data generating and processing device of claim 69 and a personal computer.
- 71. (previously presented) The analog data generating and processing device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.
- 72. (currently amended) The analog data generating and processing device of claim 69, wherein the the sensor is adapted to have two-way communication with a personal computer.
- 73. (previously presented) The analog data generating and processing device of claim 69, wherein the analog wave signals are generated by a medical device.
- 74. (previously presented) The analog data generating and processing device of claim 69, wherein the connecting device, circuit, processor and memory form a flexible interface.
- 75. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor comprises an electronic measuring device.
- 76. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor is electrically connected to the processor and memory by a two-way communication line.
- 77. (previously presented) The analog data generating and processing device of claim 69, wherein the sets of digitized analog data are to be individually stored in the memory only

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after the connecting device has been connected to the multi-purpose interface of the personal computer.

- 78. (previously presented) The analog data generating and processing device of claim 69, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is a mass storage device.
- 79. (previously presented) The analog data generating and processing device of claim 69, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is a hard disk drive.
- 80. (currently amended) The analog data generating and processing device of claim 69, wherein the the-response signal is adapted to lie to a personal computer about the true nature of the analog data generating and processing device.
- 81. (previously presented) The analog data generating and processing device of claim 69, wherein the driver is located in a BIOS of the personal computer.
- 82. (previously presented) The analog data generating and processing device of claim 69, wherein the circuit receives power when the digitized sets of analog data are being transferred to a personal computer.
- 83. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor receives power when the digitized sets of analog data are being transferred to a personal computer.
- 84. (previously presented) The analog data generating and processing device of claim 69, wherein receipt and processing of the response signal by the personal computer allows it to

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communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.

- 85. (currently amended) The analog data generating and processing device of claim 69, wherein the the sensor is detachably coupled to the analog to digital converter.
- 86. (previously presented) The analog data generating and processing device of claim 69, wherein the connecting device is adapted to be connected to a SCSI interface of the personal computer.
- 87. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor is adapted to provide data to the personal computer.
- 88. (previously presented) The analog data generating and processing device of claim 69, wherein the digitized versions of the analog data is transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 89. (previously presented) The analog data generating and processing device of claim 69, wherein the processor is adapted to create a root directory in the memory which can be accessed by the personal computer.
- 90. (previously presented) The analog data generating and processing device of claim 69, wherein a configuration file is stored in the memory.
- 91. (previously presented) The analog data generating and processing device of claim 69, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.

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92. (previously presented) The analog data generating and processing device of claim 69, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.

- 93. (previously presented) The analog data generating and processing device of claim 69, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.
- 94. (new) An analog data generating and processing device (ADGPD) for use with a personal computer (PC) having a memory, the PC also having a multi-purpose user interface (MPUI) to which the PC periodically sends device identification signals and to which the PC is capable of sending data transfer requests, the ADGPD comprising:

a circuit that can be operatively coupled to an MPUI of a PC, the circuit including an electronic memory;

a first set of instructions stored in the electronic memory that, when executed, cause analog data to be generated from one or more analog wave signals from a source that is both external to and not located in substantial proximity to the ADGPD, the execution of the first set of instructions also causing digitized analog data that is representative of the analog data to be stored in the electronic memory;

a second set of instructions stored in the electronic memory that, when executed, cause the ADGPD to detect one or more device identification signals sent from a PC and to then automatically and without user intervention send a response signal to the PC, the response signal containing information that can be processed by the PC to allow the PC to understand how it can communicate with and receive data from the ADPGD; and

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a third set of instructions stored in the electronic memory that, when executed, cause the ADGPD to detect a data transfer request sent from a PC and to then transfer and store in the PC memory at least some of the digitized analog data from the electronic memory without having to load an applications level program onto the PC.

- 95. (new) A combination comprising the analog data generating and processing device of claim 94 and a personal computer.
- 96. (new) The ADGPD of claim 94, wherein the analog wave signals comprise electromagnetic radiation.
- 97. (new) The ADGPD of claim 94, wherein the circuit is adapted to have two-way communication with the PC.
- 98. (new) The ADGPD of claim 94, wherein the analog wave signals are generated by a medical device.
 - 99. (new) The ADGPD of claim 94, wherein the circuit forms a flexible interface.
- 100. (new) The ADGPD of claim 94, wherein the digitized analog data is stored in the electronic memory only after the ADGPD is operatively connected to the PC.
- 101. (new) The ADGPD of claim 94, wherein the response signal contains information that can be processed by a PC to inform the PC that the ADGPD is a mass storage device.
- 102. (new) The ADGPD of claim 94, wherein the response signal contains information that can be processed by a PC to inform the PC that the ADGPD is a hard disk drive.
- 103. (new) The ADGPD of claim 94, wherein the response signal contains information that is not consistent with the true nature of the ADGPD.

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- 104. (new) The ADGPD of claim 94, wherein at least some of the digitized analog data is stored in a PC memory by means of a driver located in a BIOS of the PC.
- 105. (new) The ADGPD of claim 94, wherein the entirety of the circuit receives power when the digitized analog data is being transferred to the PC.
- 106. (new) The ADGPD of claim 94, wherein the circuit includes a SCSI interface that is adapted to be connected to the MPUI.
- 107. (new) The ADGPD of claim 94, wherein a wire based connection is used to connect the circuit to the MPUI.
- 108. (new) The ADGPD of claim 94, wherein the circuit includes a sensor for generating analog data, the sensor being detachably coupled to the remaining portion of the circuit.
- 109. (new) The ADGPD of claim 94, wherein the circuit includes a sensor is adapted to provide data directly to the PC.

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REMARKS

Please enter the supplemental preliminary amendment before the RCE request filed in December, 2006 is considered. In this supplemental preliminary amendment, minor corrections are made to the previously presented claims to correct a number of inadvertent typographical errors contained therein, and new claims 94-109 are submitted for the Examiner's consideration.

The new claims should be found to be patentable over all of the prior art of record, for example, for reasons similar to those that are presented in the December, 2006 preliminary amendment with respect to the previously presented claims. To further demonstrate why all of the new claims should be found to be patentable over all prior art of record, an exemplary patentability analysis is presented hereinafter with respect to USP 5,969,750.

All of the new claims require that an analog data generating and processing device (ADGPD) include a set of instructions that, when executed, cause a response signal to be automatically and without user intervention sent to a PC after a periodic inquiry signal from a multi-purpose user interface (e.g., a SCSI bus connector) has been received and processed. This means that, for example, a user does not have to load any software (e.g., an applications level program) onto a PC in order for the PC to be able to recognize how it can communicate with and receive data from the ADGPD.

One reason that the new claims should be found to be patentable over the '750 patent is that the '750 patent does not teach or suggest the above-described *automatic* and *without user intervention* feature of the new claims. While the '750 patent refers to "plug and play" cameras (column 1, lines 5-8) that can be connected to a PC by means of the USB interface disclosed in the USB spec, version 1.0 dated January 16, 1996, a user must load appropriate video

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conferencing software (*i.e.*, an applications level program) onto a PC in order to have the PC be able to receive and process data from the camera. A short analysis in support of this conclusion follows.

At column 3, lines 27-31, the '750 patent states that one of the drawbacks of prior art video conferencing systems is that, for example, a skilled operator is necessary to properly install and configure video conferencing "software" (emphasis added). One goal of the '750 patent is the elimination of the skilled operator. However, the '750 patent contains no disclosure that the software requirement is eliminated or that a PC can recognize how to receive and process data from a device without a user having to load software onto the PC. Thus, the '750 patent affirmatively requires that a user load appropriate video conferencing software onto a PC in order for the PC to be able to receive and process data from a camera. For this reason alone, for example, the new claims should be found to be patentable over the '750 patent.

All of the new claims also require an ADGPD that includes a set of instructions that, when executed, cause at least some digitized analog data to be sent to and stored in a PC memory without having to load an applications level program on the PC. Thus, customary functions of a commercially available PC, not functions provided by an applications level program that a user must load onto the PC, are used for data transfer and storage purposes.

One reason that the '750 patent does not teach or suggest this subject matter is that, as discussed above, the '750 patent affirmatively requires that a user must load appropriate video conferencing software (*i.e.*, an applications level program) onto a PC before data can be transferred or stored in a PC memory. Thus, the '750 patent *does not* teach that a user can utilize customary functions of a commercially available PC to transfer and store data from a camera in a

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PC memory. For this reason alone, the new claims should be found to be patentable over the '750 patent.

Concerning the previously presented claims, all such claims should be found to be patentable over the '750 patent at least for reasons similar to those stated above with respect to the new claims.

It is respectfully submitted that the currently pending claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited. In this regard, the Examiner is respectfully requested to contact the undersigned attorney upon entry of this supplemental preliminary amendment.

Respectfully submitted.

Jeffrey W./Salmon Attorney for Applicant Registration No. 37,435

January 22, 2007 Welsh & Katz, Ltd.

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Electronic Patent	App	lication Fe	e Transm	nittal			
Application Number:	110	11078778					
Filing Date:	11-	-Mar-2005					
Title of Invention:		ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER					
First Named Inventor/Applicant Name:	Mid	chael Tasler					
Filer:	Jet	Jeffrey W. Salmon/Maura Halvey					
Attorney Docket Number:	95	9576/96910					
Filed as Large Entity	•						
Utility Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Claims in excess of 20		1202	16	50	800		
Independent claims in excess of 3		1201	1	200	200		
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:				ZTE (USA) ²	1002 Daga 26		

Description	Fee Code	Quantity	Amount	unt Sub-Total in USD(\$)		
Extension-of-Time:						
Miscellaneous:						
	Total in USD (\$)			1000		

Electronic Acknowledgement Receipt				
EFS ID: 1455733				
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
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Time Stamp:	16:46:07			
Application Type:	Utility			
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Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$1000
RAM confirmation Number	564 ZTE (USA) 1003, Page 367

Deposit Account 230920

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Supplemental Response or Supplemental Amendment	96910amendmnt.pdf	1057581	no	20
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8327	no	2
Warnings:					
Information:					
		Total Files Size (in bytes):	10	065908	

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

CLAIMS AS FILED - PART I (Column 1) (Column 2) TOTAL CLAIMS Column 2 Column 3 Column 4 Column 5 Column 5 Column 5 Column 6 Column 7 Column 1 Column 7 Column 7 Column 1 Column 7 Column 8 Co	PATENT APPLICATION FEE DETERMINATION RECORD 11/078778													
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United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Parent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1850 Alexandria Virginia 22313-1450 www.usptu.gov

APPLICATION NO.	FILING	G DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
11/078,778	03/1	1/2005	Michael Tasler	9576/96910	8978	
Jeffrey W. Salı	7590 mon Fsa	03/28/2007		EXAM	INER	
Welsh & Katz,				KIM, HA	ROLD J	
22nd Floor 120 S. Riversio	de Plaza			ART UNIT	PAPER NUMBER	
Chicago, IL 60				2181	-	
			•		,	
				MAIL DATE	DELIVERY MODE	
				03/28/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

•	Application No. Applicant(s)							
Interview Summary	11/078,778	TASLER, MICHAEL						
interview Summary	Examiner	Art Unit						
	Harold Kim	2181						
All participants (applicant, applicant's representative, PTO	personnel):							
(1) <u>Harold Kim</u> . (3)								
(2) <u>Jeffrey W. Salmon [Reg. No. 37,435]</u> . (4)								
Date of Interview:								
Type: a)⊠ Telephonic b)□ Video Conference c)□ Personal [copy given to: 1)□ applicant	2) <u> </u>	ve]						
Exhibit shown or demonstration conducted: d) Yes If Yes, brief description:	e)⊠ No.							
Claim(s) discussed: 1.								
Identification of prior art discussed: <u>USPAT No. 5,493,335</u>								
Agreement with respect to the claims f) was reached.	g)⊠ was not reached. h)□	N/A.						
Substance of Interview including description of the general reached, or any other comments: <u>Applicant initiated intervidifference between USPAT No.5,493,335 and Claim 1. Ag</u>	ew was on March 9, 2007. 🛭	Applicant explained the						
(A fuller description, if necessary, and a copy of the amendments which the examiner agreed would render the claims allowable, if available, must be attached. Also, where no copy of the amendments that would render the claims allowable is available, a summary thereof must be attached.)								
THE FORMAL WRITTEN REPLY TO THE LAST OFFICE A INTERVIEW. (See MPEP Section 713.04). If a reply to the GIVEN A NON-EXTENDABLE PERIOD OF THE LONGER INTERVIEW DATE, OR THE MAILING DATE OF THIS INTERVIEW A STATEMENT OF THE SUBSTANCE OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW CONTROL OF THE INTERVIEW	last Office action has alread OF ONE MONTH OR THIR ERVIEW SUMMARY FORM	dy been filed, APPLICANT IS TY DAYS FROM THIS I, WHICHEVER IS LATER, TO						
•		VALD SPARKS RY PATENT EXAMINER						
Examiner Note: You must sign this form unless it is an Attachment to a signed Office action.	Examiner's sig	nature, if required						

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record

A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews

Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.

All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:

- -. Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant; attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:

- 1) A brief description of the nature of any exhibit shown or any demonstration conducted,
- 2) an identification of the claims discussed,
- 3) an identification of the specific prior art discussed,
- 4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
- 5) a brief identification of the general thrust of the principal arguments presented to the examiner,
 - (The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
- 6) a general indication of any other pertinent matters discussed, and
- 7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/06

Examiner: Harold J. Kim

For:

FLEXIBLE INTERFACE

I hereby certify under 37 C.F.R. 1.8 that this paper is being transmitted via facsimile pursuant to 37 C.F.R.

1.6(d) to the U.S. Patent and Trademark Office, (571)

273-4148, on the date indicated below.

Attorney

Docket No.:

0757/96910

PROPOSED AGENDA FOR TELEPHONE INTERVIEW WITH EXAMINER KIM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Examiner Kim:

Pursuant to your request, I am sending you this facsimile to formally request that we have a short telephone interview today. I would like the opportunity to briefly discuss with you my belief as to why all of the currently pending claims are patentable over all of the prior art of record in this application. I ask that you review all of the prior art that I have filed, and only consider the attorney argument and remarks made in connection with the above-captioned application.

Respectfully submitted,

زحد

W. Salmon, Esq.

legistration No. 37,435

Dated: March 9, 2007 WELSH & KATZ, LTD.

120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

Telephone:

(312) 655-1500

Facsimile:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING

AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

certifies that this paper is being sent to Examiner Kim by facsimile (571-273-4148) on Wednesday, April 18, 2007.

The undersigned attorney hereby

Attorney

Docket No.: 0757/96910

APPLICANT INITIATED REQUEST FOR TELEPHONE INTERVIEW

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Examiner Kim:

I would like to have a telephone interview with you at your earliest convenience. During the interview, I would like to discuss with you further amendments that I propose to make to all independent claims to further clarify the invention and better demonstrate how the claimed invention is distinguished over all of the prior art such as the prior art we discussed during the last telephone interview we had. The following is a proposed amendment for currently pending claim 17:

17. (currently amended) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which inquiry

Filed: 03/11/05 Date: April 18, 2007

Page - 2 -

signals are periodically sent as to what type of device is operatively connected thereto, the analog data generating and processing device comprising:

an input/output port that is to be operatively connected to the multipurpose interface of the personal computer;

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals from a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives;

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the memory that are utilized by the processor to cause the sets of digitized analog data to be stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

the first set of instructions being further adapted to cause the sets of digitized analog data to be stored in the memory before the input/output port is connected to a multi-purpose interface of the personal computer;

a second set of instructions being stored in the memory that are utilized by the processor to cause a response signal to be automatically and without user intervention sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the APR. 18. 2007 9:40AM WELSH & KATZ NO. 1442 P. 3

Applicant: Michael Tasler Application No.: 11/078,778

Filed: 03/11/05 Date: April 18, 2007

Page - 3 -

input/output port, the response signal informing the personal computer that the personal computer can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

a third set of instructions being stored in the memory that are utilized by the processor, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, to cause user selected ones of the digitized sets of analog data, including any digitized data sets that are generated before the input/output port is connected to the multi-purpose interface of the personal computer, to be transferred from the memory, through the input/output port, through the multi-purpose interface, and to the personal computer.

Assuming, for the sake of argument, that it is proper to combine the prior art you mentioned together, claim 17 presented above should be patentable over such a purported combination of references. One reason for this is that such a combination does not teach or suggest the generation of digitized analog data before an input/output port is connected to a multi-purpose interface of a personal computer.

If you agree with me that claim 17 presented above is patentable, then I propose to make similar amendments to all other independent claims in connection with a further supplemental preliminary amendment.

Filed: 03/11/05 Date: April 18, 2007

Page - 4 -

I look forward to discussing this matter with you at your earliest convenience.

Respectfully submitted,

Yefffey W. Salmon
Attorney for Applicant
Registration No. 37,435

April 18, 2007 R
Welsh & Katz, Ltd.
120 South Riverside Plaza, 22nd Floor
Chicago, IL 60606
Telephone (312) 655-1500
Facsimile (312) 655-1501

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

SECOND SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-0001

Dear Sir:

This Second Supplemental Preliminary Amendment is a supplemental filing with respect to the preliminary amendment filed January 22, 2007. It is respectfully requested that this Second Supplemental Preliminary Amendment be entered before consideration of the previously filed RCE request.

Applicant: Michael Tasler

Application No.: 11/078,778

Filed: 03/11/05

Date: April 24, 2007

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IN THE CLAIMS:

Please cancel claims 25, 44-68 and 77 without prejudice, and please amend claims 17, 21-22, 24, 33, 37-38, 69, 74, 76, 88-90, 94, 100, and 108-109 as noted hereinafter:

1-16. (cancelled).

17. (currently amended) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto, the analog data generating and processing device comprising:

an input/output port that is to be operatively connected to the multi-purpose interface of the personal computer;

a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signals from a source that is external to the housing and that is not located in substantial proximity to the sensor, the sensor being further adapted to generate sets of analog data from the analog wave signals that it receives;

an analog to digital converter that is operatively connected to the sensor and that generates a set of digitized analog data from each set of analog data;

a circuit that includes a processor and a <u>first</u> memory that are operatively connected to the analog to digital converter, a first set of instructions being stored in the <u>first</u> memory that are utilized by the processor to cause the sets of digitized analog data to be stored in the <u>first</u> memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

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the first set of instructions being further adapted to cause the sets of digitized analog data to be stored in the first memory before the input/output port is connected to a multipurpose interface of the personal computer;

a second set of instructions being stored in the <u>first</u> memory that are utilized by the processor to cause a response signal to be automatically and without user intervention sent from the input/output port to the multi-purpose interface after they have been operatively connected together and after an inquiry signal has been received by the input/output port, the response signal informing the personal computer that the personal computer can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

a third set of instructions being stored in the <u>first</u> memory that are utilized by the processor, after the analog data generating and processing device has been automatically recognized by the personal computer, and while the input/output port is operatively connected to the multi-purpose interface, to cause user selected ones of the digitized sets of analog data, <u>including any digitized data sets that are generated before the input/output port is connected to the multi-purpose interface of the personal computer</u>, to be transferred from the <u>first memory</u>, through the input/output port, through the multi-purpose interface, and to the personal computer; and

the third set of instructions being further adapted to cause the user selected digitized data sets to be transferred to the personal computer, after the input/output port has been connected to the multi-purpose interface of the personal computer and after the analog data generating and processing device has been recognized by the personal computer, by means of a

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software driver that is stored in a second memory of the personal computer without user intervention.

18. (previously presented) A combination comprising the analog data generating and processing device of claim 17 and a personal computer.

19. (previously presented) The analog data generating and processing device of claim 17, wherein the analog wave signals comprise electromagnetic radiation.

20. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor is adapted to have two-way communication with the personal computer.

21. (currently amended) The analog data generating and processing device <u>of</u> claim 17, wherein the analog wave signals are generated by a medical device.

22. (currently amended) The analog data generating and processing device of claim 17, wherein the processor, <u>first</u> memory and circuit form a flexible interface.

23. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor comprises an electronic measuring device.

24. (currently amended) The analog data generating and processing device of claim 17, wherein the sensor is electrically connected to the processor and the <u>first</u> memory by a two-way communication line.

25. (cancelled).

26. (previously presented) The analog data generating and processing device of claim 17, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is a mass storage device.

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27. (previously presented) The analog data generating and processing device of claim 17, wherein the response signal is adapted to inform the personal computer that the analog data generating and processing device is a hard disk drive.

- 28. (previously presented) The analog data generating and processing device of claim 17, wherein the response signal is adapted to lie to the personal computer about the true nature of the analog data generating and processing device.
- 29. (previously presented) The analog data generating and processing device of claim 17, wherein the driver is located in a BIOS of the personal computer.
- 30. (previously presented) The analog data generating and processing device of claim 17, wherein the analog to digital converter receives power when the digitized sets of analog data are being transferred to the personal computer.
- 31. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor receives power when the digitized sets of analog data are being transferred to the personal computer.
- 32. (previously presented) The analog data generating and processing device of claim 17, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 33. (currently amended) The analog data generating and processing device of claim 17, wherein the <u>first</u> memory of the analog data generating and processing device comprises a buffer memory.

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- 34. (previously presented) The analog data generating and processing device of claim 17, wherein the input/output port is adapted to be connected to a SCSI interface of the personal computer.
- 35. (previously presented) The analog data generating and processing device of claim 17, wherein the processor comprises a digital signal processor.
- 36. (previously presented) The analog data generating and processing device of claim 17, wherein the sets of digitized analog data are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.
- 37. (currently amended) The analog data generating and processing device of claim 17, wherein a root directory is created in the <u>first</u> memory which can be accessed by the personal computer.
- 38. (currently amended) The analog data generating and processing device of claim 17, wherein a configuration file is stored in the <u>first</u> memory.
- 39. (previously presented) The analog data generating and processing device of claim 17, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 40. (previously presented) The analog data generating and processing device of claim 17, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 41. (previously presented) The analog data generating and processing device of claim 17, wherein a wire based connection is used to connect the input/output port to the multi-purpose interface of the personal computer.

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42. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor is detachably coupled to the analog to digital converter.

43. (previously presented) The analog data generating and processing device of claim 17, wherein the sensor is adapted to provide data to the personal computer.

44-68. (cancelled).

69. (currently amended) An analog data generating and processing device for use with a personal computer having at least one multi-purpose interface to which inquiry signals are periodically sent as to what type of device is operatively connected thereto, comprising:

a connecting device that is to be operatively connected to the multi-purpose interface of the personal computer and that is able to receive therefrom the periodic inquiry signals;

a circuit that includes a sensor and an analog to digital converter, the circuit being adapted to be exposed to analog wave signals that originate from a source that is external to the analog data generating and processing device and that is not located in substantial proximity to the sensor, to generate sets of analog data therefrom, and to generate digitized sets of analog data from the sets of analog data;

a processor and a <u>first</u> memory both of which are operatively connected to the circuit, the processor being adapted to cause the digitized sets of analog data to be stored in the <u>first</u> memory irrespective of whether or not the analog data generating and processing device has been recognized by the personal computer;

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the processor being further adapted to cause the sets of digitized analog data to be stored in the first memory before the connecting device is connected to a multi-purpose interface of the personal computer;

the processor and <u>first_memory</u> being adapted to automatically and without user intervention send a response signal to the multi-purpose interface of the personal computer after the connecting device is operatively connected to the multi-purpose interface and after the connecting device receives at least one periodic inquiry signal therefrom, the response signal informing the personal computer that it can automatically and without user intervention recognize the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom; and

the analog data generating and processing device being adapted to, after it has been automatically recognized by the personal computer, and after the connecting device has been coupled to the multi-purpose interface of the personal computer, utilize a driver associated with the personal computer to allow cause user selected ones of the digitized sets of analog data, including any digitized data sets that are generated before the input/output port is connected to the multi-purpose interface of the personal computer, to be transferred to the personal computer; and

the analog data generating and processing device being adapted to affect the transfer of user selected digitized sets of analog data by means of a software driver that is stored in a second memory of the personal computer without user intervention.

70. (previously presented) A combination comprising the analog data generating and processing device of claim 69 and a personal computer.

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71. (previously presented) The analog data generating and processing device of claim 69, wherein the analog wave signals comprise electromagnetic radiation.

- 72. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor is adapted to have two-way communication with a personal computer.
- 73. (previously presented) The analog data generating and processing device of claim 69, wherein the analog wave signals are generated by a medical device.
- 74. (currently amended) The analog data generating and processing device of claim 69, wherein the connecting device, circuit, processor and <u>first</u> memory form a flexible interface.
- 75. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor comprises an electronic measuring device.
- 76. (currently amended) The analog data generating and processing device of claim 69, wherein the sensor is electrically connected to the processor and <u>first</u> memory by a two-way communication line.
 - 77. (canceled).
- 78. (previously presented) The analog data generating and processing device of claim 69, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is a mass storage device.
- 79. (previously presented) The analog data generating and processing device of claim 69, wherein the response signal is adapted to inform a personal computer that the analog data generating and processing device is a hard disk drive.

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80. (previously presented) The analog data generating and processing device of claim 69, wherein the response signal is adapted to lie to a personal computer about the true nature of the analog data generating and processing device.

- 81. (previously presented) The analog data generating and processing device of claim 69, wherein the driver is located in a BIOS of the personal computer.
- 82. (previously presented) The analog data generating and processing device of claim 69, wherein the circuit receives power when the digitized sets of analog data are being transferred to a personal computer.
- 83. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor receives power when the digitized sets of analog data are being transferred to a personal computer.
- 84. (previously presented) The analog data generating and processing device of claim 69, wherein receipt and processing of the response signal by the personal computer allows it to communicate with the analog data generating and processing device as if it were a mass storage device even though it is not a mass storage device.
- 85. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor is detachably coupled to the analog to digital converter.
- 86. (previously presented) The analog data generating and processing device of claim 69, wherein the connecting device is adapted to be connected to a SCSI interface of the personal computer.
- 87. (previously presented) The analog data generating and processing device of claim 69, wherein the sensor is adapted to provide data to the personal computer.

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88. (currently amended) The analog data generating and processing device of claim 69, wherein the digitized versions of the analog data is are transferred to the personal computer in a format suitable for a mass storage device present in the personal computer.

- 89. (currently amended) The analog data generating and processing device of claim 69, wherein the processor is adapted to create a root directory in the <u>first</u> memory which can be accessed by the personal computer.
- 90. (currently amended) The analog data generating and processing device of claim 69, wherein a configuration file is stored in the <u>first</u> memory.
- 91. (previously presented) The analog data generating and processing device of claim 69, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific mass storage device.
- 92. (previously presented) The analog data generating and processing device of claim 69, wherein a configuration file allows a user to configure the analog data generating and processing device as being a specific hard disk drive.
- 93. (previously presented) The analog data generating and processing device of claim 69, wherein a wire based connection is used to operatively connect the input/output port of the processor circuit to the multi-purpose interface of the personal computer.
- 94. (currently amended) An analog data generating and processing device (ADGPD), comprising:

a circuit that is adapted to be operatively coupled to a multi-purpose user interface (MPUI) of a personal computer (PC) to which the PC periodically sends device identification signals and to which the PC is capable of sending data transfer requests;

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a first set of instructions stored in thean ADGPD memory that are adapted to cause analog data to be generated from one or more analog wave signals from a source that is both external to and not located in substantial proximity to the ADGPD, the first set of instructions being further adapted to cause digitized analog data that is representative of the analog data to be stored in the ADGPD memory;

the first set of instructions being further adapted to cause the sets of digitized analog data to be stored in the ADGPD memory before the circuit is connected to a multipurpose interface of the PC;

a second set of instructions stored in the ADGPD memory that is adapted to cause, after a device identification signal has been received and processed, a response signal to be automatically and without user intervention sent to the PC that contains data which indicates to the PC how the PC can communicate with and receive data from the ADPGD; and

a third set of instructions stored in the ADGPD memory that are adapted to cause, after a data transfer request has been received and processed, a transfer of at least some of the digitized analog data, including any digitized data sets that are generated before the circuit is coupled to the multi-purpose interface of the PC, from the ADGPD memory to the PC; and

the third set of instructions being further adapted to affect the transfer of user selected digitized data sets by means of a software driver that is stored in a memory of the PC and that is a standard program of the PC. without user intervention.

95. (previously presented) A combination comprising the analog data generating and processing device of claim 94 and a personal computer.

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- 96. (previously presented) The ADGPD of claim 94, wherein the analog wave signals comprise electromagnetic radiation.
- 97. (previously presented) The ADGPD of claim 94, wherein the circuit is adapted to have two-way communication with the PC.
- 98. (previously presented) The ADGPD of claim 94, wherein the analog wave signals are generated by a medical device.
- 99. (previously presented) The ADGPD of claim 94, wherein the circuit forms a flexible interface.
- 100. (currently amended) The ADGPD of claim 94, wherein the digitized analog data is stored in the <u>ADGPD</u> memory only after the analog data generating and transmitting device is operatively connected to the PC.
- 101. (previously presented) The ADGPD of claim 94, wherein the response signal is adapted to inform a PC that the ADGPD is a mass storage device.
- 102. (previously presented) The ADGPD of claim 94, wherein the response signal is adapted to inform the PC that the ADGPD is a hard disk drive.
- 103. (previously presented) The ADGPD of claim 94, wherein the response signal is adapted to lie to the PC about the true nature of the ADGPD.
- 104. (previously presented) The ADGPD of claim 94, wherein the driver is located in a BIOS of the PC.
- 105. (previously presented) The ADGPD of claim 94, wherein the entirety of the circuit receives power when the digitized analog data is being transferred to the PC.

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- 106. (previously presented) The ADGPD of claim 94, wherein the circuit includes a SCSI interface that is adapted to be connected to the MPUI.
- 107. (previously presented) The ADGPD of claim 94, wherein a wire based connection is used to connect the circuit to the MPUI.
- 108. (currently amended) The ADGPD of claim 94, wherein the circuit includes a sensor for generating analog data, the sensor being detachably coupled to thea remaining portion of the circuit.
- 109. (currently amended) The ADGPD of claim 94, wherein the circuit includes a sensor that is adapted to provide data directly to the PC.

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REMARKS

It is respectfully requested that this second supplemental preliminary amendment be entered and considered in connection with the previously filed supplemental preliminary amendment and RCE request. For purposes of brevity, claims 25, 44-69 and 77 have been cancelled.

The undersigned attorney thanks the Examiner for the courtesy of the previous telephone interviews. Amendments have been made to all of the currently pending claims to more clearly claim the invention and better highlight how the claims distinguish over all of the prior art, either taken alone or in any purported combination.

As one example, further amendments have been made to further specify the "automatic recognition without user intervention" feature of the currently pending claims. In this regard, all claims have been amended to recite that a transfer of user selected digitized analog data sets takes place by means of a software driver that is stored in a personal computer memory without user intervention. This means that, for example, a user does not have to load any software (e.g., an applications level programs such as Adobe Photoshop or an additional software driver) into the memory of a personal computer in order to be able to affect the transfer of digitized analog data. Instead, a software driver that is a standard component of a personal computer (such as being loaded in a personal computer memory when the personal computer is purchased or delivered to an end user) is utilized to affect the transfer. These amendments are consistent with the previous claim amendments and arguments made in connection with previous filings by the undersigned attorney.

One reason that all currently pending claims should be found to be patentable is that no

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purported combination of references (such as the combination of references discussed during the telephone interview, assuming that it is proper to make such a combination) teaches or suggests, for example, that digitized analog data can be transferred to a personal computer using a software driver that is loaded in a personal computer memory without user intervention. In direct contrast to this, all of the camera or scanner related references of record require user intervention of some sort (*e.g.*, requiring a user to load software onto a personal computer) to effect the transfer of digitized analog data from the camera or scanner to the PC. For this reason alone, it is respectfully submitted that all currently pending claims should be found to be patentable.

As another example which evidences patentability, all currently pending claims have been amended to require that the claimed device be capable of generating digitized analog data sets before the claimed device is connected to a personal computer. The undersigned attorney is not aware of any proper combination of references that teaches or suggests this claim element under the current obviousness standard or under the obviousness standard proposed by the Solicitor General to the Supreme Court in connection with currently pending KSR case.

As one example, and assuming for the sake of argument that a "plug and play keyboard" is prior art and that it is proper to combine such assumed prior art with a camera reference such as US Patent No. 5,493,335, such a purported combination of references would not render obvious the subject matter of the currently pending claims. One reason for this is that all claims require the capability of generating digitized analog data before the claimed device is connected to a personal computer. In direct contrast to this, keyboards create useful data only after they are connected to a personal computer and, therefore, the combination of the keyboard with the '335 patent would be able to create useful data on a plug and play basis only after the combination is

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connected to a personal computer. For this additional reason, for example, it is respectfully submitted that all currently pending claims should be found to be patentable.

This is to confirm the undersigned attorney's request for the Examiner to review all the prior art submitted in connection with the instant application, and to base his decision on the patentability of the currently pending claims only on the remarks made in the instant application, not on arguments or amendments made in any other application of which this application claims priority.

The Examiner's attention is drawn to the fact that each independent claim includes a dependent claims that recites a combination of a personal computer with the device claimed in the corresponding independent claim. It is the specific intention of the client and the undersigned attorney to ensure that all currently pending claims are first directly infringed by the manufacture or sale of the claimed device, not by the combination of the claimed device and a personal computer.

The Examiner is respectfully requested to contact the undersigned attorney upon entry of this second supplemental preliminary amendment.

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In view only of the arguments and amendments made only in the instant application, it is respectfully submitted that the currently pending claims are in condition for allowance and, therefore, a formal notice to that effect is earnestly solicited.

Respectfully submitted

Jeffrey W Salmon Attorney for Applicant Registration No. 37,435

April 24, 2007

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Electronic Acknowledgement Receipt				
EFS ID:	1712431			
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
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Attorney Docket Number:	9576/96910			
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Application Type:	Utility			
Payment information:				

Submitted with Payment

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)				
1	Supplemental Response or Supplemental Amendment	96910SecondSuppPreImAm endment.pdf	908790	no	18				
Warnings:									
Information:									
	Total Files Size (in bytes): 908790								

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING PROCESSING DEVICE AND FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. It is submitted that no fees are due in connection with this Information Disclosure Statement because it is being submitted before the First Office Action has been mailed from the USPTO.

No inferences should be drawn that the attached list represents a comprehensive investigation, that any material disclosed is equivalent to the subject invention, or that any of the documents that have publication dates prior to the priority date of the above application anticipate the invention in this application.

The Examiner is requested to review the document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein or in the papers filed contemporaneously herewith is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosures of the art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the instant patent application. The recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence.

Respectfully submitted,

By: 19 W Salman I

Registration No. 37,435

Dated: April 26, 2007

Enclosures: Form PTO-1449

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Substitute for form 1449A/PTO and 1449B/PTO				Application Number	11/078,778			
INFOR	RMATION D	ISCI	OSURE	Filing Date	March 11, 2005			
1	EMENT BY			First Named Inventor	Michael Tasler			
SIAII	CIVILIA I DI	AFF	LICANI	Art Unit	2181			
(use as many sheets as necessary)			necessary)	Examiner Name	Harold J. Kim			
Sheet	1	of	1	Attorney Docket Number	0757-96910			

U.S. PATENT DOCUMENTS									
Examiner Initials*	Cite No.1	Document Number Number - Kind Code² (if known)	Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear				

FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Te			

		OTHER ART - NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), No.1 publisher, city and/or country where published.						
Birrell et al., Implementing Remote Procedure Calls, ACM Transaction on Computer Systems, Vol.2, No. 1 (1985) 39-59.							
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Examiner	Date	
Signature	Considered	

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¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Electronic Acknowledgement Receipt					
EFS ID:	1718291				
Application Number:	11078778				
International Application Number:					
Confirmation Number:	8978				
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER				
First Named Inventor/Applicant Name:	Michael Tasler				
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Payment information:					

no

File Listing:

Submitted with Payment

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)				
1	Information Disclosure Statement (IDS) Filed	11078778.pdf	89847	no	2				
Warnings:									
Information									
This is not an	USPTO supplied IDS fillable form								
2	Information Disclosure Statement (IDS) Filed	96910.pdf	113612	no	1				
Warnings:									
Information									
This is not an	USPTO supplied IDS fillable form								
3	NPL Documents	96910art.pdf	1929141	no	21				
Warnings:									
Information:									
		Total Files Size (in bytes):	21	132600					

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

PTO/SB/08a (08-03)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

	Application Number Filing Date		11078778 2005-03-11	
INFORMATION DISCLOSURE			ael Tasler	
STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Art Unit		2181	
(Not lot submission ander of or it iss)	Examiner Name Harold J. Kim		J. Kim	
	Attorney Docket Number		0757-96910	

U.S.PATENTS									
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue D)ate	Name of Patentee or Applicant of cited Document		Relev	s,Columns,Lines where ant Passages or Relevant es Appear
	1	5871368		1999-02	!-16	Erdner et al.			
	2	5920709		1999-07	´-06	Hartung et al.			
	3	6086430		2000-07	´-11	Amoni et al.			
	4	6188675		2001-02	-13	Casper et al.			
If you wis	n to a	dd additional U.S. Pater	nt citatio	n inform	ation pl	ease click the	Add button.		
	7		U.S.P	ATENT	APPLIC	CATION PUBL	ICATIONS	r	
Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publica Date	tion	Name of Pate of cited Docu	entee or Applicant ment	Relev	s,Columns,Lines where ant Passages or Relevant es Appear
	1								
If you wish to add additional U.S. Published Application citation information please click the Add button.									
				FOREIG	N PAT	ENT DOCUM	ENTS		
Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²		Kind Code ⁴	Publication Publication Pages, Colum Publication Applicant of cited Passages or I		Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	

Application Number 11078778 Filing Date 2005-03-11 INFORMATION DISCLOSURE First Named Inventor Michael Tasler STATEMENT BY APPLICANT Art Unit 2181 (Not for submission under 37 CFR 1.99) **Examiner Name** Harold J. Kim 0757-96910 Attorney Docket Number 1 If you wish to add additional Foreign Patent Document citation information please click the Add button **NON-PATENT LITERATURE DOCUMENTS** Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item Cite Examiner (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), T5 Initials* No publisher, city and/or country where published. 1 If you wish to add additional non-patent literature document citation information please click the Add button **EXAMINER SIGNATURE Date Considered Examiner Signature** *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. 2 Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). 3 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

4 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 5 Applicant is to place a check mark here if

EFS Web 2.0

English language translation is attached.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number		11078778		
Filing Date		2005-03-11		
First Named Inventor Micha		nel Tasler		
Art Unit		2181		
Examiner Name Harok		d J. Kim		
Attorney Docket Number		0757-96910		

	CERTIFICATION STATEMENT								
Plea	ase see 37 CFR 1.97 and 1.98 to make the appropriate selec	tion(s):							
	That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).								
OF	₹								
	That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).								
	See attached certification statement.								
	Fee set forth in 37 CFR 1.17 (p) has been submitted herewi	th.							
X	None								
۸۵		TURE	18 Please see CEP 1 4/d) for the						
	A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.								
Sign	nature / / / / / / / / / / / / / / / / / / /	Date (YYYY-MM-DD)	2007-05-04						
Nar	Name/Print Jeffrey Salmon Registration Number 37,435								
pub	This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR								

application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria,**

EFS Web 2.0

VA 22313-1450.

Electronic Acknowledgement Receipt				
EFS ID:	1745273			
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com			
Filer:	Jeffrey W. Salmon/Gail Terri			
Filer Authorized By:	Jeffrey W. Salmon			
Attorney Docket Number:	9576/96910			
Receipt Date:	04-MAY-2007			
Filing Date:	11-MAR-2005			
Time Stamp:	14:46:12			
Application Type:	Utility			
Payment information:				

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Submitted with Payment	no
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File Listing:

Document Number	Document Description File Name File Size(Bytes)		Multi Part /.zip	Pages (if appl.)	
1	Information Disclosure Statement (IDS) Filed	P96910IDS.pdf	227795	no	3
Warnings:					
Information:					

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

Total Files Size (in bytes):

New Applications Under 35 U.S.C. 111

This is not an USPTO supplied IDS fillable form

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New International Application Filed with the USPTO as a Receiving Office

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227795



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/078,778	03/11/2005	Michael Tasler	9576/96910	8978
Jeffrey W. Salr	7590 05/04/200 mon. Esq	7	EXAM	INER
Welsh & Katz,			KIM, HA	ROLD J
22nd Floor 120 S. Riversio	le Plaza		ART UNIT	PAPER NUMBER
Chicago, IL 60	606		2181	
		•		
			MAIL DATE	DELIVERY MODE
			05/04/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
Interview Summary	11/078,778	TASLER, MICHAEL				
	Examiner	Art Unit				
	Harold Kim	2181				
All participants (applicant, applicant's representative, PTO	personnel):					
(1) <u>Harold Kim</u> .	(3)					
(2) <u>Jeffrey W. Salmon [Reg. No. 37,435]</u> .	(4)					
Date of Interview:	•	•				
Type: a)⊠ Telephonic b)□ Video Conference c)□ Personal [copy given to: 1)□ applicant 2	²)∏ applicant's representativ	e]				
Exhibit shown or demonstration conducted: d) Yes If Yes, brief description:	e)⊠ No.					
Claim(s) discussed: <u>1</u> .						
Identification of prior art discussed: <u>USPAT No. 5,493,335</u> .						
Agreement with respect to the claims f) was reached. g) was not reached. h) N/A.						
Substance of Interview including description of the general reached, or any other comments: <u>Applicant initiated intervied difference between USPAT No. 5,493,335 and Claim 1. Apwithin next week. Agenda from the applicant is also attach</u>	ew was on April 18, 2007. Apoplicant stated that he will file	plicant explained the				
(A fuller description, if necessary, and a copy of the amend allowable, if available, must be attached. Also, where no callowable is available, a summary thereof must be attached	opy of the amendments that v					
THE FORMAL WRITTEN REPLY TO THE LAST OFFICE A INTERVIEW. (See MPEP Section 713.04). If a reply to the GIVEN A NON-EXTENDABLE PERIOD OF THE LONGER INTERVIEW DATE, OR THE MAILING DATE OF THIS INTERVIEW DATE, OR THE SUBSTANCE OF THE INTERVIEW OF THE INTERVIEW OF THE SUBSTANCE OF THE INTERVIEW OF THE SUBSTANCE OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF THE INTERVIEW OF T	last Office action has already OF ONE MONTH OR THIRT ERVIEW SUMMARY FORM,	/ been filed, APPLICANT IS Y DAYS FROM THIS WHICHEVER IS LATER, TO				
	DON	IALD SPARKS BY PATENT EXAMINER				
Examiner Note: You must sign this form unless it is an Attachment to a signed Office action.	Examiner's sign	nature, if required				

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record

A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.

All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:

- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by
 attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does
 not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the Interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:

- 1) A brief description of the nature of any exhibit shown or any demonstration conducted,
- 2) an identification of the claims discussed,
- 3) an identification of the specific prior art discussed,
- 4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
- 5) a brief identification of the general thrust of the principal arguments presented to the examiner,
 - (The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
- 6) a general indication of any other pertinent matters discussed, and
- 7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Substitute for form 1449/PTO

(Use as many sheets as necessary)

Complete if Known				
Application Number	11/078,778			
Filing Date	03/11/2005			
First Named Inventor	Tasler, Michael			
Art Unit	2181			
Examiner Name	Harold J. Kim			
Attorney Docket Number	0757/96910			

			U. S. PATEN	T DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		^{US-} 6,895,449	05/17/2005	Tasler	
		US-			
	 	US-			
		US-			
:	 	US-			
		US-			
		US-			
	 	US-			
		US-			

	FOREIGN PATENT DOCUMENTS							
Examiner Cite Foreign Patent Document Initials* No. 1		Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages				
		Country Code ³ "Number ⁴ "Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T		

Examiner	Date	
Signature	Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 'Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

Electronic Acknowledgement Receipt			
EFS ID:	1809458		
Application Number:	11078778		
International Application Number:			
Confirmation Number:	8978		
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER		
First Named Inventor/Applicant Name:	Michael Tasler		
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com		
Filer:	Jeffrey W. Salmon/Maura Halvey		
Filer Authorized By:	Jeffrey W. Salmon		
Attorney Docket Number:	9576/96910		
Receipt Date:	24-MAY-2007		
Filing Date:	11-MAR-2005		
Time Stamp:	16:59:32		
Application Type:	Utility		
Payment information:			

no

File Listing:

Submitted with Payment

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)	
1	Information Disclosure Statement (IDS) Filed	96910SuppIDSPTO1449.pdf	116312	no	1	
Warnings:						
Information:						
This is not an USPTO supplied IDS fillable form						

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Total Files Size (in bytes):

New Applications Under 35 U.S.C. 111

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116312

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler Group No.: 2181

Serial No.: 11/078,778 Conf. No.: 8978

Filed: 3/11/05 Examiner: Harold J. Kim

For: ANALOG DATA GENERATING

AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. It is submitted that no fees are due in connection with this Information Disclosure Statement because it is being submitted before the First Office Action has been mailed from the USPTO.

The Examiner is asked to assume only for purposes of examination of the instant application that the cameras and software described in the documents submitted herewith actually are prior art to all currently pending claims. However, this request should not be construed as being an admission regarding the prior art status of any of the documents or the cameras/software described therein. Applicant expressly reserves the right to revoke this

request, and to dispute in all forums whether the documents submitted herewith and the cameras/software described therein actually are prior art.

The Examiner's attention is drawn to the second sentence on page one of the QV-Link software printout filed herewith, which states that this "application software makes it possible to connection the Casio Digital Camera to your personal computer." Similarly, page 81 of the manual states that "special software" (the QV-Link software described above) allows a user to "transfer images from the QV-10 to a personal computer."

It is respectfully submitted that all currently pending claims should be found to be patentable over all assumed prior art submitted herewith because, for example, such assumed prior art does not teach or suggest the *automatic and without user intervention* feature of the claims. One reason for this is that all such assumed prior art affirmatively requires a user to load applications level software, *i.e.* the QV-Link software, onto a PC in order to be able to transfer pictures to it, which is antithetical to the *automatic and without user intervention* claim element.

No inferences should be drawn that the list filed herewith represents a comprehensive investigation or that any material disclosed is equivalent to the subject invention. The Examiner is requested to review the document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein or in the papers filed contemporaneously herewith is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosures of the art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the instant patent application. The recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence.

It is respectfully submitted that all currently pending claims are in condition for allowance and, therefore, that a formal notice to that effect is earnestly solicited.

Respectfully submitted,

Bv:

V. Salmon, Esc

Registration No. 37,435

Dated: June 15, 2007

Enclosures: Form PTO-1449 **WELSH & KATZ, LTD.**

120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

Telephone: (312) Facsimile: (312)

(312) 655-1500 (312) 655-1501

E-mail: jwsalmon@welshkatz.com

Form PTO-144 (Rev. 8-88)	U.S. Department of Commerce Attorn Patent and Trademark Office				ket No. /96910		Serial No. 11/078,778		78
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Examiner Initial*	Document Number	Date	Nam	е	Class	Subo	class	Filing D	
		FOREIG	N PATENT DOCU	MENTS					
			_		Class				slation
	Document Number	Date	Count	Country		Subclass		Yes	No
	OTHER DO	OCUMENTS (Inclu	uding Author, Title,	Date, Pertinent	Pages, E	tc.)			
	Technical Specs	– QV-10A – Q	V Series – Came	eras – Produc	ts – CAS	SIO (1	page)	
	QV-LINK Versi	on 2.0 for Wind	low (QVLINK-R	README.TX	T) (5 pa	iges)			
	QV-10B Owner'	s Manuel (Casi	o) (23 pages)						
Examiner	*		Date Considered						
*Examiner:	Initial if citation con citation if not in cor applicant.								

Electronic Acknowledgement Receipt				
EFS ID:	1879484			
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com			
Filer:	Jeffrey W. Salmon/Maura Halvey			
Filer Authorized By:	Jeffrey W. Salmon			
Attorney Docket Number:	9576/96910			
Receipt Date:	15-JUN-2007			
Filing Date:	11-MAR-2005			
Time Stamp:	18:54:17			
Application Type:	Utility			
Payment information:				

no

File Listing:

Submitted with Payment

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)		
1	Information Disclosure Statement (IDS) Filed	96910IDS1449.pdf	191168	no	4		
Warnings:							
Information:							
This is not an	USPTO supplied IDS fillable form						
2	NPL Documents	96910Ref1.pdf	81301	no	1		
Warnings:		,					
Information:							
3	NPL Documents	96910Ref2.pdf	308379	no	5		
Warnings:							
Information:	Information:						
4	NPL Documents	96910Ref3.pdf	4235086	no	23		
Warnings:		1					
Information:	Information:						
		Total Files Size (in bytes):	48	315934			

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler Group No.: 2181

Serial No.: Conf. No.: 8978 11/078,778

Filed: 3/11/05 Examiner: Harold J. Kim

ANALOG DATA GENERATING For:

> AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. It is submitted that no fees are due in connection with this Information Disclosure Statement because it is being submitted before the First Office Action has been mailed from the USPTO.

The Examiner is asked to assume only for purposes of examination of the instant application that the cameras and software described in the documents submitted herewith actually are prior art to all currently pending claims. However, this request should not be construed as being an admission regarding the prior art status of any of the documents or the cameras/software described therein. Applicant expressly reserves the right to revoke this

request, and to dispute in all forums whether the documents submitted herewith and the cameras/software described therein actually are prior art.

A manual for Kodak's camera model no. DC20 is submitted herewith. Numbered page 3 of the manual states that the "DC 20 camera package has everything you need to easily capture images and transfer them to your computer. Simply use the enclosed software to create personalized images to share with family and friends."

Page 1-1 of the manual for Kodak's camera model no. DC25 states that the camera described therein offers "2 megabytes of picture storage on an optional Picture Card." Page 1-2 of the DC25 manual refers to "Kodak Picture Easy Software – a quick and easy way to transfer pictures from your DC25 to your computer" as well as to "PictureWorks' PhotoEnhancer Special Fun Edition [software] – transfer pictures from the DC25 to your computer and let the fun begin! Quickly view, enhance and used your pictures in templates to create cards, invitations, calendars, and more." Both of these applications level software programs are included on a CD-ROM disc sold by Kodak.

A two-page printout from Kodak's website is submitted herewith, and describes certain Kodak software which is used for transferring pictures from certain Kodak digital cameras to a personal computer. In particular, this printout describes the Kodak Picture Enhancer Software which a user must load onto a computer in order to be able to transfer pictures to the computer.

A one-page printout from Kodak's website is submitted herewith, and describes the Kodak Picture Easy Software. A user must load the Picture Easy Software onto a computer in order to be able to transfer pictures to it.

US Patent No. 6,147,703 (filed Dec. 19, 1996) is entitled electronic still camera with image review. The first paragraph of the background of the invention of this patent refer to

"Picture Disk software from Eastman Kodak, which is provided to a customer on a diskette along with scanned images from a roll of film, presents a screen in a Microsoft Windows environment with thumbnails of images."

US Patent No. 5,402,170 (issued Mar. 28, 1995) is entitled hand-manipulated electronic camera tethered to a personal computer. The cover page of this patent indicates that it is owned by Eastman Kodak. In Figure 3 and at column 5, lines 53-55, the patent discloses that, to get images from the camera disclosed therein to a personal computer, a user "opens a 'tethered camera' application program in the computer 12."

It is respectfully submitted that all currently pending claims should be found to be patentable over all assumed prior art submitted herewith because, for example, such assumed prior art does not teach or suggest the *automatic and without user intervention* feature of the claims. One reason for this is that all such assumed prior art affirmatively requires a user to load applications level software, *i.e.* software sold by Kodak, onto a PC in order to be able to transfer pictures to it, which is antithetical to the *automatic and without user intervention* claim element.

No inferences should be drawn that the list filed herewith represents a comprehensive investigation or that any material disclosed is equivalent to the subject invention. The Examiner is requested to review the document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein or in the papers filed contemporaneously herewith is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular date which may be relevant to the instant

patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosures of the art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the instant patent application. The recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence.

It is respectfully submitted that all currently pending claims are in condition for allowance and, therefore, that a formal notice to that effect is earnestly solicited.

Respectfully submitted,

Dated: June 18, 2007

Form PTO-1449 Enclosures:

WELSH & KATZ, LTD.

120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

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(312) 655-1501 E-mail: jwsalmon@welshkatz.com

Form PTO-144 (Rev. 8-88)							0757/96910			Serial No. 11/078,778	
INFORMATION DISCLOSURE STATEMEN			IENT		Applicant Michael Tasler						
(Use several sheets if necessary)			if necessary)	Filing Date		Grou 11/05		ıp No. 2181			
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Examiner Initial*	Document Number	Da	Date		lame	:	Class	Subo	class	Filing [
	6,147,703	12	2/19/1996	Miller, et al.							
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	Kodak DC20 Camera Manual (37 pages)										
Kodak DC 25 Camera Manual (47 pages)											
Kodak Picture Easy Software (1 page)											
	Kodak Picture	Woi	ks Software	(2 pages)				****			
Examiner				Date Considered	<u></u>						
*Examiner:				or not citation is ot considered. In							

Electronic Acknowledgement Receipt				
EFS ID:	1883420			
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com			
Filer:	Jeffrey W. Salmon/Maura Halvey			
Filer Authorized By:	Jeffrey W. Salmon			
Attorney Docket Number:	9576/96910			
Receipt Date:	18-JUN-2007			
Filing Date:	11-MAR-2005			
Time Stamp:	17:10:14			
Application Type:	Utility			
Payment information:				

no

File Listing:

Submitted with Payment

Document Number	Document Description	File Name	Multi Part /.zip	Pages (if appl.)	
1	Information Disclosure Statement (IDS) Filed	061807IDS96910.pdf	252512	no	5
Warnings:					
Information:	:				
This is not an	USPTO supplied IDS fillable form				
2	NPL Documents	Ref196910.pdf	13212743	no	37
Warnings:					
Information:					
3	NPL Documents	Ref296910.pdf	2951238	no	47
Warnings:					
	in the PDF is too large. The pages sho		F is submitted, the pag	ges will be res	ized upon
Information:					
4	NPL Documents	Ref496910.pdf	106438	no	2
Warnings:		•			
Information:	:				
5	NPL Documents Ref396910.pdf		86744	no	1
Warnings:					
Information:	:				
		Total Files Size (in bytes):	16	609675	

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

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New International Application Filed with the USPTO as a Receiving Office

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached Form PTO-1449 that may be material to the examination of this application. It is submitted that no fees are due in connection with this Information Disclosure Statement because it is being submitted before the First Office Action has been mailed from the USPTO.

The Examiner is asked to assume only for purposes of examination of the instant application that the cameras and software described in the documents submitted herewith actually are prior art to all currently pending claims. However, this request should not be construed as being an admission regarding the prior art status of any of the documents or the cameras/software described therein. Applicant expressly reserves the right to revoke this

request, and to dispute in all forums whether the documents submitted herewith and the cameras/software described therein actually are prior art.

A manual for Canon's EOS-DCS 3C digital camera is being submitted herewith. The manual states at page 1-5 that a "TWAIN-compliant software application" is "required." Page 6-1 of the manual states that while "the camera and computer are connected, you use the supplied Kodak software driver to: access, view, and select images from your camera."

A spec sheet for Canon's EOS-DCS 3C digital camera is being submitted herewith. The spec sheet states that camera includes an built-in SCSI interface.

A spec sheet for Canon's PS600 digital camera is being submitted herewith. The spec sheet states that the camera includes a parallel/PCMCIA (Type III) interface, and is compatible with Windows 95. The spec sheet contains no indication that the PS600 camera does not require a user to use applications level software that is loaded onto a PC in order to be able to transfer pictures to the PC.

A two-page printout concerning Olympus' digital camera model number Camedia C-800L is being submitted herewith. The printout states that "Olympus provides its own software with the C-800L. This downloads thumbnail images, each taking about three seconds, and saves files as JPEG (low, medium and high compression), TIF, BMP and J6I. "

It is respectfully submitted that all currently pending claims should be found to be patentable over all assumed prior art submitted herewith because, for example, such assumed prior art does not teach or suggest the *automatic and without user intervention* feature of the claims. One reason for this is that all such assumed prior art affirmatively requires a user to load applications level software onto a PC in order to be able to transfer pictures to it, which is antithetical to the *automatic and without user intervention* claim element.

No inferences should be drawn that the list filed herewith represents a comprehensive investigation or that any material disclosed is equivalent to the subject invention. The Examiner is requested to review the document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein or in the papers filed contemporaneously herewith is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

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It is respectfully submitted that all currently pending claims are in condition for allowance and, therefore, that a formal notice to that effect is earnestly solicited.

Respectfully submitted,

Dated: June 19, 2007

Enclosures: Form PTO-1449

WELSH & KATZ, LTD. 120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

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Form PTO-144 (Rev. 8-88)	Form PTO-1449 U.S. Department of Commerce Rev. 8-88) Patent and Trademark Office			Attorney Docket No. Serial No. 0757/96910 11/078,773				78	
	Applicant Michael Tasler								
	(Use several shee	ets if necessary)		Filing Date 03/11/05			Group No. 2181		
		U.S	. PATENT DOCUME	NTS					
Examiner Initial*	Document Number			Name		Subclass		Filing Date If Appropriate	
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1		FOREI	GN PATENT DOCU	MENTS					
				_				Translation	
	Document Number	Date	Country		Class	Subc	lass	Yes	No
	OTHER DO	OCUMENTS (Inc	cluding Author, Title,	Date, Pertinent	Pages, Et	tc.)			
	Manual for Canon	EOS-DCS 3C	Digital Camera (31	4 pages)					
	Spec Sheet for Ca	non EOS-DCS	3C Digital Camera	(2 pages)					
	Spec Sheet for Canon PS600 Digital Camera (1 page)								
	Information Regar	ding Olympus (Camedia C-800L D	igital Camera					
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Electronic Acknowledgement Receipt				
EFS ID:	1888568			
Application Number:	11078778			
International Application Number:				
Confirmation Number:	8978			
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER			
First Named Inventor/Applicant Name:	Michael Tasler			
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com			
Filer:	Jeffrey W. Salmon/Maura Halvey			
Filer Authorized By:	Jeffrey W. Salmon			
Attorney Docket Number:	9576/96910			
Receipt Date:	19-JUN-2007			
Filing Date:	11-MAR-2005			
Time Stamp:	18:15:09			
Application Type:	Utility			
Payment information:				

no

File Listing:

Submitted with Payment

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	061907IDS.pdf	213560	no	4
Warnings:					
Information:					
This is not an l	JSPTO supplied IDS fillable form				
2	NPL Documents	061907Ref1Part1.pdf	2562329	no	85
Warnings:					
Information:					
3	NPL Documents	061907Ref1Part2.pdf	2982347	no	90
Warnings:					
Information:					
4	NPL Documents	061907Ref1Part3.pdf	3569382	no	100
Warnings:					
Information:					
5	NPL Documents	061907Ref1Part4.pdf	1080299	no	38
Warnings:					
Information:					
6	NPL Documents	061907Ref2.pdf	94082	no	2
Warnings:					
Information:					
7	NPL Documents	061907Ref3.pdf	43413	no	1
Warnings:					·
Information:					
8	NPL Documents	061907Ref4.pdf	167528	no	2
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

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Commissioner for Patents Box IDS- NON FEE P.O. Box 1450 Alexandria, VA 22313-1450

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request, and to dispute in all forums whether the documents submitted herewith and the cameras/software described therein actually are prior art.

A manual for Sony's DCS F1 digital camera is being submitted herewith. The manual states at page 20 that, to transfer pictures from the camera to a personal computer, a "personal computer connecting kit" that is "supplied with the camera" must be used. Significantly, the kit includes "application software that enables you to utilize image data transmitted from the camera."

A manual for the PC connecting kit sold with the Sony DCS F1 camera is being submitted herewith. The manual describes that a user must load an applications level program onto a personal computer in order to be able to transmit image data to it.

A two-page spec sheet for the Sony DCS F1 camera is being submitted herewith. The spec sheet states that "ArcSoft's PhotoStudio DSC image manipulate software [is] included" with the camera when it is sold.

It is respectfully submitted that all currently pending claims should be found to be patentable over all assumed prior art submitted herewith because, for example, such assumed prior art does not teach or suggest the *automatic and without user intervention* feature of the claims. One reason for this is that all such assumed prior art affirmatively requires a user to load applications level software onto a PC in order to be able to transfer pictures to it, which is antithetical to the *automatic and without user intervention* claim element.

No inferences should be drawn that the list filed herewith represents a comprehensive investigation or that any material disclosed is equivalent to the subject invention. The Examiner is requested to review the document(s) and determine the extent of the materiality of the document disclosures with respect to the present invention.

The discussion of any art and the citation of any document(s) herein or in the papers filed contemporaneously herewith is not to be construed as an admission that the art or document disclosure is necessarily within the invention field of endeavor, that the art or document disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that the art or document disclosure is otherwise necessarily prior art as defined by the patent law with respect to the instant invention and application.

Also, there is reserved the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosures of the art and document(s) recited herein, that may be cited by the Examiner in rejecting a claim in the instant patent application. The recitation herein of the art and document(s) is not to be construed as an assertion that more pertinent art could not possibly be in existence.

It is respectfully submitted that all currently pending claims are in condition for allowance and, therefore, that a formal notice to that effect is earnestly solicited.

Respectfully submitted,

11 1/1

Jeffrey W. Salmon, Esq

Dated: June 20, 2007

Enclosures: Form PTO-1449

WELSH & KATZ, LTD.

120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

Telephone:

(312) 655-1500

Facsimile:

(312) 655-1501

E-mail: jwsalmon@welshkatz.com

Form PTO-1449 U.S. Department of Comm (Rev. 8-88) Patent and Trademark Offi			Attorney Do	cket No. 7/96910		Seria	I No. 11/078,7	78		
	INFORMATION DISC			ENT	Applicant	Mic	chael T	asler		
	(Use several sh	eets	if necessary)		Filing Date	ate Group No. 2181			***************************************	
			U.S.	PATENT DOCUM	1					
Examiner		т		T		1	Υ		Filing [noto.
Initial*	Document Number	Da	ite	Name		Class	Subo	class		opriate
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	Document Number	er	Date	Cou	ntry	Class	Subo	class	Yes	No
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	OTHER	ooc	UMENTS (Incl	uding Author, Title	, Date, Pertinen	t Pages, E	tc.)			
	Manual for Sony	Dig	gital Still Cam	era DSC-F1 (60	pages)	·····				
	Sony PC Connecting Kit – Operating Instructions (127 pages)									
Sony DSC-F1 Digital Still Camera (2 page)										

Examiner				Date Considered						
*Examiner:				or not citation is in t considered. Incl						

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2181

Serial No.:

11/078,778

Conf. No.: 8978

Filed:

3/11/05

Examiner: Harold J. Kim

For:

ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL

COMPUTER (As Amended)

Attorney

Docket No.: 0757/96910

REQUEST FOR PERSONAL INTERVIEW

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Examiner Kim:

I would like to schedule a personal interview with you at your office in the USPTO in the near future. Please contact me at your earliest convenience to set up the personal interview.

I would like to discuss with you in person the issues raised by the references that I have given you about various digital cameras and software provided by Eastman Kodak, Sony, Polaroid, Canon, Olympus, and Casio. I have asked you to assume that this information is prior art. Assuming that it is prior art, this information, together with the other camera related patents I have made of record, evidences that it was the accepted state of the art in the digital camera field around the time of the claimed invention to require a user to load applications software onto a computer before image data could be transferred to the computer from a digital camera.

Deviations away from the accepted state of the art are evidence of patentability of an invention. See, W.L. Gore & Associates, Inc. v. Garlock, Inc., 721 F.2d 1540, 1552 (C.A.Fed.1983) (The fact that "Dr. Gore . . . proceeded contrary to the accepted wisdom of the prior art . . . is strong evidence of nonobviousness."). See also, Tec Air, Inc. v. Denso Mfg. Mich., Inc., 192 F.3d 1353, 1360 (Fed. Cir. 1999) ("To rebut a prima facie case of obviousness based on prior art, objective evidence of nonobviousness may be used, including whether the invention was contrary to accepted wisdom of the prior art."). The recent Supreme Court decision in the KSR case does not overrule and is not inconsistent with the above-noted Gore or Tec Air decisions of the Federal Circuit.

All of the currently pending claims require an automatic and without user intervention feature that allows, for example, a computer to understand how to talk to and receive data from the claimed device without a user having to load applications software onto the computer. It is respectfully submitted that, in accordance with the above-referenced case law, all currently pending claims should be found to be patentable. One reason for this is that the present invention deviates away from the accepted state of the art evidenced by the camera and software references of record, which affirmatively require user intervention – a user must load applications software onto a computer to be able to transfer pictures to it from a digital camera.

It is respectfully submitted that all currently pending claims are in condition for allowance and, therefore, that a formal notice to that effect is earnestly solicited.

Respectfully submitted,

Dated: June 20, 2007

Enclosures: Form PTO-1449

WELSH & KATZ, LTD.

120 South Riverside Plaza, 22nd Floor

Chicago, Illinois 60606

Telephone: Facsimile:

(312) 655-1500 (312) 655-1501

E-mail: jwsalmon@welshkatz.com

Electronic Acknowledgement Receipt			
EFS ID:	1892725		
Application Number:	11078778		
International Application Number:			
Confirmation Number:	8978		
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER		
First Named Inventor/Applicant Name:	Michael Tasler		
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com		
Filer:	Jeffrey W. Salmon/Maura Halvey		
Filer Authorized By:	Jeffrey W. Salmon		
Attorney Docket Number:	9576/96910		
Receipt Date:	20-JUN-2007		
Filing Date:	11-MAR-2005		
Time Stamp:	16:44:17		
Application Type:	Utility		
Payment information:			

no

Submitted with Payment

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)	
1	Information Disclosure Statement (IDS) Filed	062007IDS96910.pdf	204869	no	4	
Warnings:			'			
Information:						
This is not an I	JSPTO supplied IDS fillable form					
2	NPL Documents	062007Ref1.pdf	2176412	no	60	
Warnings:						
Information:						
3	NPL Documents	062007Ref2Part1.pdf	2498941	no	60	
Warnings:						
Information:						
4	NPL Documents	062007Ref2Part2.pdf	2629673	no	67	
Warnings:						
Information:						
5	NPL Documents	062007Ref3.pdf	626144	no	2	
Warnings:					<u> </u>	
Information:	Information:					
6	Letter Requesting Interview with Examiner	96910RequestPersonalInterv iew.pdf	111844	no	2	
Warnings:						
Information:						
		Total Files Size (in bytes):	82	247883		

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

EXPRESS ABANDONMENT UNDER 37 CFR 1.138

Fax directly to the Pre-Grant Publication Division at (703) 305-8568; or mail to: Mail Stop Express Abandonment Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Total of 1 forms are submitted.

′''	a to a concentration and analysis it	displays a valid Chib control Hamber.
	Application Number	11/078,778
	Filing Date	03/11/05
	First Named Inventor	Tasler
	Art Unit	2181
	Examiner Name	Harold J. Kim
-	Attorney Docket Number	0757/96910

Please check only one of boxes 1 or 2 below: (If no box is checked, this paper will be treated as a request for express abandonment as if box 1 is checked.)							
Express Abandonment I request that the above-identified application be expressly abandoned as of the filing date of this paper.							
2. Express Abandonment in Favor of a Continuing Application I request that the above-identified application be expressly abandoned as of the filing date accorded the continuing application filed previously or herewith.							
NOTE : A paper requesting express abandonment of an application is not effective unless and until an appropriate JSPTO official recognizes and acts on the paper. See the Manual of Patent Examining Procedure (MPEP), section 711.01.							
TO AVOID PUBLICATION, USE FORM PTO/SB/24A INSTEAD OF THIS FORM.							
TO REQUEST A REFUND OF SEARCH FEE AND EXCESS CLAIMS FEE (IF ELIGIBLE), USE FORM PTO/SB/24B INSTEAD OF THIS FORM.							
I am the: applicant.							
assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)							
attorney or agent of record. Attorney or agent registration number is $37,435$							
attorney or agent acting under 37 CFR 1.34, who is authorized under 37 CFR 1.138(b) because the application is expressly abandoned in favor of a continuing application (box 2 above must be checked). Attorney or agent registration number							
Joffy W. Lelm 13 August 2007							
Signature Date							
Jeffrey W. Salmon (312) 655-1500 Typed or printed name Telephone Number							
Note: Signature of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.							

This collection of information is required by 37 CFR 1.138. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process an application). Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Express Abandonment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Electronic Acknowledgement Receipt			
EFS ID:	2079724		
Application Number:	11078778		
International Application Number:			
Confirmation Number:	8978		
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER		
First Named Inventor/Applicant Name:	Michael Tasler		
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551501 jwsalmon@welshkatz.com		
Filer:	Jeffrey W. Salmon/Maura Halvey		
Filer Authorized By:	Jeffrey W. Salmon		
Attorney Docket Number:	9576/96910		
Receipt Date:	14-AUG-2007		
Filing Date:	11-MAR-2005		
Time Stamp:	14:12:39		
Application Type:	Utility under 35 USC 111(a)		
Payment information:			

no

Submitted with Payment

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Letter Express Abandonment of the	96910ExpressAbandonment.	104374	no	1
1	application	pdf	dc6bc5f0a33ff3787fc05bb1d768e0088 5352e54		
Warnings:					
Information	1				
		Total Files Size (in bytes):	10	04374	

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

				
APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/078,778	03/11/2005	Michael Tasler	9576/96910 8978	
Jeffrey W. Salr	7590 08/31/200	7	EXAM	INER
Welsh & Katz,			KIM, HA	ROLD J
22nd Floor 120 S. Riversid	le Plaza		ART UNIT	PAPER NUMBER
Chicago, IL 60	606		2181	
				···
			MAIL DATE	DELIVERY MODE
			08/31/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)		
Nation of About and	11/078,778	TASLER, MICHAEL		
Notice of Abandonment	Examiner	Art Unit		
	Harold Kim	2181		
The MAILING DATE of this communication a				
This application is abandoned in view of:		·		
Applicant's failure to timely file a proper reply to the Of (a) A reply was received on (with a Certificate of period for reply (including a total extension of time (b) A proposed reply was received on, but it does not period on, but it does not period on, and the proposed reply was received on, but it does not period on	of Mailing or Transmission date of month(s)) which exp	red on		
(A proper reply under 37 CFR 1.113 to a final rejec application in condition for allowance; (2) a timely fi Continued Examination (RCE) in compliance with 3	led Notice of Appeal (with app		or	
(c) ☐ A reply was received on but it does not cons final rejection. See 37 CFR 1.85(a) and 1.111. (Se		fide attempt at a proper reply, to the n	non-	
(d) ☐ No reply has been received.				
Applicant's failure to timely pay the required issue fee a from the mailing date of the Notice of Allowance (PTO)		e, within the statutory period of three n	nonths	
 (a) ☐ The issue fee and publication fee, if applicable, v), which is after the expiration of the statutory Allowance (PTOL-85). 				
(b) ☐ The submitted fee of \$ is insufficient. A bala	nce of \$ is due.			
The issue fee required by 37 CFR 1.18 is \$. The publication fee, if require	ed by 37 CFR 1.18(d), is \$		
(c) ☐ The issue fee and publication fee, if applicable, has	not been received.			
 Applicant's failure to timely file corrected drawings as re Allowability (PTO-37). 	equired by, and within the three	-month period set in, the Notice of		
 (a) ☐ Proposed corrected drawings were received on after the expiration of the period for reply. 	(with a Certificate of Mailin	g or Transmission dated), which	h is	
(b) No corrected drawings have been received.				
The letter of express abandonment which is signed by the applicants.	the attorney or agent of record	, the assignee of the entire interest, or	all of	
5. The letter of express abandonment which is signed by 1.34(a)) upon the filing of a continuing application.	an attorney or agent (acting in	a representative capacity under 37 CF	FR	
6. The decision by the Board of Patent Appeals and Interference rendered on and because the period for seeking court review of the decision has expired and there are no allowed claims.				
7. ☐ The reason(s) below:		DONALD PARKS SUPERVISORY FOR AMIL	N'SR	
Petitions to revive under 37 CFR 1.137(a) or (b), or requests to with minimize any negative effects on patent term. U.S. Palent and Trademark Office	draw the holding of abandonment	under 37 CFR 1.181, should be promptly fi	led to	
	e of Abandonment	Part of Paper No. 200	70827	



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/078,778	03/11/2005	Michael Tasler	9576/96910	8978
Jeffrey W. Saln	7590 09/07/2007		EXAM	INER
Welsh & Katz,			KIM, HA	ROLD J
22nd Floor 120 S. Riversid	e Plaza	•	ART UNIT	PAPER NUMBER
Chicago, IL 60	606	•	. 2181	
			<u></u>	
			MAIL DATE	DELIVERY MODE
			09/07/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
Notice of Abandonment	11/078,778	TASLER, MICHAEL				
Notice of Abandonnient	Examiner	Art Unit				
,	Kim, Harold J	2181				
The MAILING DATE of this communication app	ears on the cover sheet with the c	orrespondence address				
This application is abandoned in view of:						
Applicant's failure to timely file a proper reply to the Office letter mailed on (a) A reply was received on (with a Certificate of Mailing or Transmission dated), which is after the expiration of the period for reply (including a total extension of time of month(s)) which expired on (b) A proposed reply was received on, but it does not constitute a proper reply under 37 CFR 1.113 (a) to the final rejection.						
(A proper reply under 37 CFR 1.113 to a final rejection application in condition for allowance; (2) a timely filed Continued Examination (RCE) in compliance with 37 C	Notice of Appeal (with appeal fee);					
(c) A reply was received on but it does not constitutional rejection. See 37 CFR 1.85(a) and 1.111. (See 6		mpt at a proper reply, to the non-				
(d) ☐ No reply has been received.						
 Applicant's failure to timely pay the required issue fee and from the mailing date of the Notice of Allowance (PTOL-8) 		the statutory period of three months				
(a) The issue fee and publication fee, if applicable, was received on (with a Certificate of Mailing or Transmission dated), which is after the expiration of the statutory period for payment of the issue fee (and publication fee) set in the Notice of Allowance (PTOL-85).						
(b) The submitted fee of \$ is insufficient. A balance	e of \$ is due.					
The issue fee required by 37 CFR 1.18 is \$ The issue fee required by 37 CFR 1.18 is \$	The publication fee, if required by 37	CFR 1.18(d), is \$				
(c) The issue fee and publication fee, if applicable, has no	ot been received.					
 Applicant's failure to timely file corrected drawings as requ Allowability (PTO-37). 	ired by, and within the three-month p	period set in, the Notice of				
 (a) Proposed corrected drawings were received on after the expiration of the period for reply. 	(with a Certificate of Mailing or Tran	smission dated), which is				
(b) \(\sum \) No corrected drawings have been received.						
The letter of express abandonment which is signed by the the applicants.	e attorney or agent of record, the ass	ignee of the entire interest, or all of				
5. The letter of express abandonment which is signed by an 1.34(a)) upon the filing of a continuing application.	attorney or agent (acting in a repres	entative capacity under 37 CFR				
. The decision by the Board of Patent Appeals and Interference rendered on and because the period for seeking court review of the decision has expired and there are no allowed claims.						
7. 🔲 The reason(s) below:		. 1				
	Sa	Barbara Debnam □Management & Program Analyst□Art Unit: 3900				
Patitions to revive under 37 CER 1 137(a) or (b), or requests to withdray	withe holding of abandonment under 27 i	CED 1 101 about he assembly filed to				

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted via the Office electronic filing system in accordance with § 1.6(a)(4).

Dated: Aggost 13, Signature: (Jeffrey W. Salmon)

Docket No.:

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Michael Tasler

Application No.: 11/078,778

Confirmation No.: 8978

Filed: March 11, 2005

Art Unit: 2181

For: FLEXIBLE INTERFACE

Examiner: Harold J. Kim.

NOTICE OF LOSS OF RIGHT TO CLAIM SMALL ENTITY STATUS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

When the instant application was filed on March 11, 2005, small entity status was claimed. By means of an assignment document that was executed on March 8, 2006, and that was recorded in the USPTO on March 15, 2006 at reel/frame 07314/0114, Papst Licensing GmbH & Co. KG received full title to the above-noted application.

Since Papst Licensing is not entitled to claim small entity status in connection with its patent prosecution endeavors, all of the fees paid during the prosecution of the instant application by the undersigned attorney on or after March 8, 2006 were "large entity" fees. However, no paper was filed in the USPTO to formally notify the USPTO that the previous claim of small entity status should be revoked.

August 12, 2008 Page 2

The purpose of this paper is to request that the USPTO update its records to reflect the fact that, as of March 8, 2006, small entity status is no longer claimed in the instant application.

Dated: August 13, 2008

Respectfully submitted,

Jeffrey W. Salmon

Registration No.: 37,435

MARSHALL, GERSTEIN & BORUN LLP

233 S. Wacker Drive, Suite 6300

Sears Tower

Chicago, Illinois 60606-6357

(312) 474-6300

Attorney for Applicant

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REVOCATION OF POWER OF ATTORNEY WITH **NEW POWER OF ATTORNEY** AND **CHANGE OF CORRESPONDENCE ADDRESS**

Application Number	11/078,778
Filing Date	March 11, 2005
First Named Inventor	Michael Tasler
Art Unit	2181
Examiner Name	Harold J. Kim
Attorney Docket Number	

I hereby	I hereby revoke all previous powers of attorney given in the above-identified application.							
A	Power of Attorney is subm	itted herewith.						
OR								
XIh	nereby appoint the practitio	ners associated	d with t	he Cus	tomer Number:	0)4743	
D PI	ease change the correspor	ndence address	s for the	e above	∍-identified application	on to:		
OR	The address associated Customer Number:	diw t						
X Firm	vidual Name Jeffrey W. Sa							
Address	Marshall, Gerstein & Bo 233, South Wacker Driv)					
City	Chicago					***************************************		
Country	United States	State	r	Illinois		Zip	60606	
Telephone	312-474-6300		Email	jsalmo	on@marshallip.co	m		
I am	the:							
Ar	pplicant/Inventor.							
	ssignee of record of the ent atement under 37 CFR 3.7							
	ŞIG	NATURE of Ar	plican	nt or As	signee of Record			
Signature	, 0.	Hazz	9					
Name	Daniel Papst, Paten	t Counsel						
Date	Date 08/13/2008 Telephone 312-423-3450						312-423-3450	
	ignatures of all the inventors or as ore than one signature is required		f the enti	ire interes	t or their representative(s) are requ	uired. Submit multiple	
	*Total of for	rms are submitted.						

PTO/SB/96 (01-08)

Approved for use through 06/30/2008. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

	STATEMENT UND	ER 37 CFR 3.	73(b)	
Patent Owner:	Papst Licensing GmbH & Co	. KG	***************************************	AND B 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May 100 May
Application No.:	11/078,778	F	iled:	May 11, 2005
Entitled: Flexible Interfa	ce			
Papst Licensing Gi (Name of Assignee)	mbH & Co. KG , a	G e of Assignee, e.g., corpo	erman Corpor	ration university, government agency, etc.)
states that it is:				
1. x the assignee of the	e entire right, title, and interest;	or		
2. an assignee of les	s than the entire right, title and	interest.	;	
(The extent (by pe	ercentage) of its ownership inter	est is	%)	
in the patent application/pate	nt identified above by virtue of e	either:		
recorded in the Unit	the inventor(s) of the patent aped States Patent and Trademai , or for which a copy ther	k Office at Reel	entified above.	The assignment was
OR				
1. From: <u>Micha</u> The documer	ne inventor(s), of the patent applied I Tasler It was recorded in the United 2023 , Frame 0515	To: Labo States Patent and	ortechnik Tasle Trademark O	er GmbH ffice at
	echnik Tasler GmbH			
	nt was recorded in the United			
Reel 0	7314 , Frame <u>0114</u>	, or for which	n a copy there	of is attached.
Additional docu	ments in the chain of title are	listed on a supple	emental sheet.	
	R 3.73(b)(1)(i), the documentary currently is being, submitted for			
	ppy (i.e., a true copy of the originaccordance with 37 CFR Part			
The undersigned (whose titl	e is supplied below) is author Signature	ized to act on beh	alf of the assi	nee.
	Daniel Papst		31:	2-425-3450
Print	ed or Typed Name		Tele	phone Number
P	atent Counsel			
	Title			

Electronic Acknowledgement Receipt					
EFS ID:	3776586				
Application Number:	11078778				
International Application Number:					
Confirmation Number:	8978				
Title of Invention:	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER				
First Named Inventor/Applicant Name:	Michael Tasler				
Correspondence Address:	Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago IL 60606 US 3126551500 jwsalmon@welshkatz.com				
Filer:	Scott E. Baxendale/zyneene williams				
Filer Authorized By:	Scott E. Baxendale				
Attorney Docket Number:	9576/96910				
Receipt Date:	13-AUG-2008				
Filing Date:	11-MAR-2005				
Time Stamp:	17:44:43				
Application Type:	Utility under 35 USC 111(a)				
Daymont information:					

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Name File Size(Bytes) /Message Digest		Pages (if appl.)	
1	Miscellaneous Incoming Letter	11078778NoticetoChangeEn	84409	no	2	
'	Wildowia incoming Letter	tity.PDF	c44333a61e07dd8566d8a4e6c5a83d2 13ef3ec88	110		
Warnings:						
Information	1					
2	Oath or Declaration filed	11078778RevocationPOA.P	103682	no	1	
۷	Oath of Declaration filed	DF	10b45e9fee209bb325f3c6b6bf7687e40 4b427e4	110		
Warnings:						
Information	:					
3	Assignee showing of ownership per	11078778Statement373b.PD	94162	no	1	
_	37 CFR 3.73(b).	239719233b1d244f66b1f1b88304e49f6			'	
Warnings:						
Information						
		Total Files Size (in bytes):	28	32253		

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.



CHICAGO, IL 60606

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMI United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS PO. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov UNITED STATES DEPARTMENT OF COMMERCE

APPLICATION NUMBER FILING OR 371(C) DATE FIRST NAMED APPLICANT ATTY. DOCKET NO./TITLE 11/078,778 03/11/2005 Michael Tasler 9576/96910

4743 MARSHALL, GERSTEIN & BORUN LLP 233 S. WACKER DRIVE, SUITE 6300 **SEARS TOWER**

CONFIRMATION NO. 8978 POA ACCEPTANCE LETTER



Date Mailed: 11/20/2008

NOTICE OF ACCEPTANCE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 08/13/2008.

The Power of Attorney in this application is accepted. Correspondence in this application will be mailed to the above address as provided by 37 CFR 1.33.

/dahall/							
 				 _	(FT.)		

Office of Data Management, Application Assistance Unit (571) 272-4000, or (571) 272-4200, or 1-888-786-0101



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

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FIRST NAMED APPLICANT ATTY. DOCKET NO./TITLE

APPLICATION NUMBER 11/078,778

FILING OR 371(C) DATE 03/11/2005

Michael Tasler

9576/96910

Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago, IL 60606 CONFIRMATION NO. 8978 POWER OF ATTORNEY NOTICE



Date Mailed: 11/20/2008

NOTICE REGARDING CHANGE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 08/13/2008.

• The Power of Attorney to you in this application has been revoked by the assignee who has intervened as provided by 37 CFR 3.71. Future correspondence will be mailed to the new address of record(37 CFR 1.33).

/dahall/				
		_		

Office of Data Management, Application Assistance Unit (571) 272-4000, or (571) 272-4200, or 1-888-786-0101

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1st Named Inventor : Michael Tasler Examiner : Kim, Harold J.

Serial No. : 11/078,778 Group Art Unit : 2181

Filing Date : March 11, 2005 Confirmation No. : 8978

Docket No. : 9576/96910 Status : Expressly Abandoned

Title : ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE

WITH A PERSONAL COMPUTER

Mail Stop "Office of Petitions" Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PETITION FOR EXPEDITED REVIEW UNDER 37 CFR 1.182

Papst Licensing GmbH & Co. KG ("Papst" or "Applicant") petitions the Director under 37 § C.F.R. 1.182 for expedited review of the accompanying petition under 37 C.F.R. § 1.55 and 37 C.F.R. § 1.78 for a delayed priority claim. Applicant requests expedited review due to currently pending litigation and prosecution of patents related to U.S. Application No. 11/078,778. Early notification of the granting of this petition is respectfully requested.

CONCLUSION

Based on the reasons set forth above, Applicant respectfully requests that the Director grant this petition.

The Director is hereby authorized to charge any fees which may be required regarding this filing, or credit any overpayment, to Deposit Account No. 19-0513. Should no proper payment be enclosed herewith, as by a check or credit card payment form being in the wrong amount, unsigned, post-dated, otherwise improper or informal or entirely missing, the Director is authorized to charge the unpaid amount to Deposit Account No, 19-0513.

Date: November 10, 2016

/Jason A. Murphy/ Jason A. Murphy Registration No. 63,423 Customer No. 69995 Schmeiser, Olsen & Watts 22 Century Hill Drive, St. 302 Latham, New York 12110 Tel. (518) 220-1850 Fax: (518) 220-1857

11/078,778

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1st Named Inventor : Michael Tasler Examiner : Kim, Harold J.

Serial No. : 11/078,778 Group Art Unit : 2181 Filing Date : March 11, 2005 Confirmation No. : 8978

Docket No. : 9576/96910

Title : ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE

WITH A PERSONAL COMPUTER

Mail Stop "Office of Petitions" Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PETITION UNDER 37 C.F.R. § 1.55 and § 1.78 FOR DELAYED CLAIM OF PRIORITY

Papst Licensing GmbH & Co. KG ("Papst" or "Applicant"), petitions the Director under 37 C.F.R. § 1.55 and §1.78 for a delayed claim of priority. The above-referenced U.S. Application No. 11/078,778 ("'778 Application") is entitled to domestic priority to U.S. Application No. 10/219,105, filed August 15, 2002, which is a divisional application of U.S. Application No. 09/331,002, filed June 14, 1999, and is also entitled to foreign priority to PCT Application No. PCT/EP98/01187, filed March 3, 1998, which claims priority to German Application No. DE 19708755.8, filed March 4, 1997. It has recently come to Applicant's attention that the filing documents accompanying the '778 Application mistakenly omit a specific reference to U.S. Application No. 09/331,002, the National Stage of PCT Application No. PCT/EP98/01187; this omission is a result of an administrative error. Applicant intended to claim priority to each prior-filed U.S. Application and foreign application in the filing documents accompanying the '778 Application, which is evidenced by the record. As such, a delay in expressly claiming such priority to U.S. Application No. 09/331,002 has resulted, which Applicant seeks to correct by this Petition. Applicant further contends that the entire delay between the date the benefit claim was due and the date the benefit claim was filed was unintentional.

For the reasons set forth in this Petition, Applicant contends that the Patent Office should GRANT this Petition under 37 C.F.R. § 1.55 and §1.78.

STATEMENT OF FACTS

- 1. On March 4, 1997, Applicant filed German Patent Application No. 19708755.8.
- 2. Within twelve months of filing German Patent Application No. 19708755.8, Applicant filed PCT Application No. PCT/EP98/01187 on March 3, 1998, claiming priority to German Patent Application No. 19708755.8.
- 3. Applicant timely entered the U.S. National Stage by filing U.S. Application No. 09/331,002 on June 14, 1999, now U.S. Patent No. 6,470,399. This National Stage entry application properly claimed priority to PCT Application No. PCT/EP98/01187 and German Patent Application No. 19708755.8.
- 4. While U.S. Application No. 09/331,002 was co-pending, Applicant filed a divisional application, U.S. Application No. 10/219,105, on August 15, 2002, now U.S. Patent No. 6,895,449. U.S. Application 10/219,105 properly claimed domestic priority to U.S. Application No. 09/331,002, and foreign priority to PCT/EP98/01187 and German Patent Application No. 19708755.8.²
- 5. The '778 application, which is the subject of this Petition, was timely filed as a continuation application of U.S. Application No. 10/219,105 on March 11, 2005.³ The Transmittal Form accompanying the '778 Application shows that the box marked "Continuation" is selected, and lists U.S. Application No. 10/219,105 as the prior-filed application.⁴
- 6. The Oath and Declaration filed with the '778 Application lists the foreign priority information, specifically listing PCT Application No. PCT/EP98/01187 and German Patent Application No. 19708755.8.⁵
- 7. A Bibliographic Data Sheet for the '778 Application issued by the Patent Office on June 20, 2006 lists the continuing data (U.S. Application No. 10/219,105) and the foreign applications (German Patent Application No. 19708755.8 and PCT Application No. PCT/EP98/01187).

¹ U.S. Patent No. 6,470,399, cover page, (86), (30).

² U.S. Patent No. 6,895,449, cover page, (62), (30).

³ U.S. Application No. 11/078,778, Utility Patent Application Transmittal Form, provided as Appendix A.

⁴ See Appendix A.

⁵ U.S. Application No. 11/078,778, Oath and Declaration, provided as Appendix B.

⁶ See '778 Application, Bibliographic Data Sheet, provided as Appendix C.

- 8. On the same Bibliographic Data Sheet for the '778 Application, the Examiner verified and acknowledged by signature that foreign priority was claimed, and that 35 U.S.C. § 119 (a-d) conditions were met.⁷
- 9. The '778 Application published on July 21, 2005 as U.S. Patent Application Publication No. 2005/0160199, with accurate foreign application priority data listed on the cover page.⁸
- 10. On July 28, 2006, the Patent Office issued a Notice of Allowability for the '778 Application.⁹ In the Notice of Allowability, the Examiner acknowledged "all" of the claims for foreign priority.¹⁰ In particular, the Examiner indicated that the "Certified copies of the priority documents have been received in Application No. 09/331,002".¹¹
- 11. Several continuation applications were filed after U.S. Application No. 11/078,778 that claim domestic priority to U.S. Application 09/331,002 filed on June 14, 1999, and foreign priority to PCT/EP98/01187 filed on March 3, 1998 and German Application No. 19708755.8 filed on March 4, 1997 (U.S. Application No. 11/467,073, now U.S. Patent No. 8,966,144,¹² U.S. Application No. 11/467,092, now U.S. Patent No. 9,189,437,¹³ U.S. Application No. 11/928,283, published as US2008/0209088,¹⁴ and U.S. Application No. 12/891,443, now U.S. Patent No. 8,504,746¹⁵).
- 12. On August 24, 2016, Applicant was made aware of an administrative error that mistakenly omits a specific reference to U.S. Application No. 09/331,002 in the filing documents accompanying the '778 Application, prompting the Applicant to file this Petition.

⁷ See Appendix C.

⁸ U.S. Patent Application Publication No. 2005/0160199, cover page.

⁹ See '778 Application, Notice of Allowability, provided as Appendix D.

¹⁰ Id.

¹¹ Id.

¹² U.S. Patent No. 8,966,144, Col. 1, lines 8–13.

¹³ U.S. Patent No. 9,189,437. Col. 1, lines 8-13.

¹⁴ U.S. Patent Application Publication No. 2008/0209088, ¶ [0001].

¹⁵ U.S. Patent No. 8,504,746, Col. 1, lines 8–16.

REGULATIONS

37 C.F.R. § 1.55 Claim For Foreign Priority

(a) In general. An applicant in a nonprovisional application may claim priority to one or more prior foreign applications under the conditions specified in 35 U.S.C. 119(a) through (d) and (f), 172, 365(a) and (b), and 386(a) and (b) and this section.

. . .

- (e) Delayed priority claim. Unless such claim is accepted in accordance with the provisions of this paragraph, any claim for priority under 35 U.S.C. 119(a) through (d) or (f), 365(a) or (b), or 386(a) or (b) not presented in the manner required by paragraph (d) or (m) of this section during pendency and within the time period provided by paragraph (d) of this section (if applicable) is considered to have been waived. If a claim for priority is considered to have been waived under this section, the claim may be accepted if the priority claim was unintentionally delayed. A petition to accept a delayed claim for priority under 35 U.S.C. 119(a) through (d) or (f), 365(a) or (b), or 386(a) or (b) must be accompanied by:
 - (1) The priority claim under 35 U.S.C. 119(a) through (d) or (f), 365(a) or (b), or 386(a) or (b) in an application data sheet (§ 1.76(b)(6)), identifying the foreign application to which priority is claimed, by specifying the application number, country (or intellectual property authority), day, month, and year of its filing, unless previously submitted;
 - (2) A certified copy of the foreign application, unless previously submitted or an exception in paragraph (h), (i), or (j) of this section applies;
 - (3) The petition fee as set forth in § 1.17(m); and
 - (4) A statement that the entire delay between the date the priority claim was due under this section and the date the priority claim was filed was unintentional. The Director may require additional information where there is a question whether the delay was unintentional.

37 C.F.R. § 1.78 Claiming Benefit Of Earlier Filing Date and Cross-References to Other Applications

. . .

(d)(2) Except for a continued prosecution application filed under § 1.53(d), any nonprovisional application, international application designating the United States, or international design application designating the United States that claims the benefit of one or more prior-filed nonprovisional applications, international applications designating the United States, or international design applications designating the United States must contain or be amended to contain a reference to each such prior-filed application, identifying it by application number (consisting of the series code and serial number), international application number and international filing date, or international registration number and filing date under § 1.1023. If the later-filed application is a nonprovisional application, the reference required by this paragraph must be included in an application data sheet (§ 1.76(b)(5)). The reference also must identify the relationship of the applications, namely, whether the later-filed application is a continuation, divisional, or continuation-in-part of the prior-filed nonprovisional application, international application, or international design application.

. . .

- (e) Delayed claims under 35 U.S.C. 120, 121, 365(c), or 386(c) for the benefit of a prior-filed nonprovisional application, international application, or international design application. If the reference required by 35 U.S.C. 120 and paragraph (d)(2) of this section is presented after the time period provided by paragraph (d)(3) of this section, the claim under 35 U.S.C. 120, 121, 365(c), or 386(c) for the benefit of a prior-filed copending nonprovisional application, international application designating the United States may be accepted if the reference required by paragraph (d)(2) of this section was unintentionally delayed. A petition to accept an unintentionally delayed claim under 35 U.S.C. 120, 121, 365(c), or 386(c) for the benefit of a prior-filed application must be accompanied by:
 - (1) The reference required by 35 U.S.C. 120 and paragraph (d)(2) of this section to the prior-filed application, unless previously submitted;
 - (2) The petition fee as set forth in § 1.17(m); and
 - (3) A statement that the entire delay between the date the benefit claim was due under paragraph (d)(3) of this section and the date the benefit claim was filed was unintentional. The Director may require additional information where there is a question whether the delay was unintentional.

DISCUSSION

I. THE DELAY IN CLAIMING PRIORITY WAS UNINTENTIONAL

A brief review of the record makes clear that Applicant intended to claim domestic priority to U.S. Application No. 09/331,002, through U.S. Application No. 10/219,105, and foreign priority back to German Patent Application No. 19708755.8 through PCT Application No. PCT/EP98/01187. For instance, the Transmittal Form accompanying the '778 Application indeed lists U.S. Application No. 10/219,105 as the prior-filed application, 16 and the Oath and Declaration lists both PCT Application No. PCT/EP98/01187 and German Patent Application No. 19708755.8.¹⁷ The Patent Office even confirms that foreign priority was claimed in the '778 Application. 18 Related applications filed previous and subsequent to the '778 Application also claimed domestic priority to U.S. Application No. 09/331,002, as well as foreign priority back to March 4, 1997. Considering the above facts, it was reasonable for Applicant to believe that the priority chain of the '778 Application was properly claimed, as intended since the time of filing the '778 Application.

Very recently, on August 24, 2016, Applicant was made aware that the filing documents accompanying the '778 Application mistakenly omit a specific reference to previously-filed U.S. Application No. 09/331,002, despite specifically referring to U.S. Application No. 10/219,105 and each foreign priority document. This omission is simply an administrative error that has gone unnoticed for over eleven years.

However, Applicant hereby submits this Petition for a delayed claim of priority to U.S. Application No. 09/331,002 and foreign priority therefrom. The delay for this claim is unintentional, for at least the reasons provided herein: 1) the convincing evidence that Applicant always intended to claim priority to U.S. Application No. 09/331,002 and foreign priority therefrom, 2) acknowledgement by the Patent Office that the '778 Application was entitled to priority back to March 4, 1997, 3) recent discovery of an administrative error, and 4) the swift nature with which Applicant petitions the Patent Office upon learning of the error.

¹⁶ See Appendix A.¹⁷ See Appendix B.

¹⁸ See Appendix C.

The following information further evidences that the entire delay was unintentional, and that the Patent Office should GRANT this Petition.

A. APPLICANT INTENDED TO CLAIM PRIORITY BACK TO THE EARLIEST EFFECTIVE FILING DATE

Applicant has always made known to the Patent Office that it intended to take advantage of the earliest effective filing date for patent applications concerning the subject matter disclosed in the '778 Application. Applicant timely entered the U.S. National Stage by filing U.S. Application No. 09/331,002 on June 14, 1999. That National Stage entry application properly claimed priority to PCT Application No. PCT/EP98/01187 and German Patent Application No. 19708755.8. Exhibiting the same desire to claim priority to the earliest effective filing date, Applicant filed a divisional application, U.S. Application No. 10/219,105, which properly claimed domestic priority to U.S. Application No. 09/331,002, and foreign priority to PCT Application No. PCT/EP98/01187, which claims priority to German Patent Application No. 19708755.8. Thus, the two related U.S. Non-Provisional Applications filed before the '778 Application expressly claimed priority to each prior-filed U.S. application, as well as the foreign priority documents.

When filing the '778 Application, Applicant intended to claim priority in the same manner as before and take advantage of the earliest effective filing date. Specifically, Applicant intended to claim priority as follows: continuation of U.S. Application No. 10/219,105, which is a divisional of U.S. Application No. 09/331,002, which is the National Stage of, and claims the benefit of and priority to, PCT Application No. PCT/EP98/01187, which claims priority to German Patent Application No. 19708755.8. Applicant claimed the identical priority chain in those prior applications, and the Patent Office allowed Applicant to benefit from the earliest effective filing date. Moreover, the Oath and Declaration filed with the '778 Application lists the foreign priority information, specifically listing PCT Application No. PCT/EP98/01187 and German Patent Application No. 19708755.8. Indeed, if Applicant had no intention of claiming priority through U.S. Application No. 09/331,002 back to the earliest effective date provided by the German priority document, then these foreign priority documents would not have been

²¹ Appendix B.

¹⁹ U.S. Patent No. 6,470,399, cover page, (86), (30).

²⁰ U.S. Patent No. 6,895,449, cover page, (62), (30).

present in the Oath and Declaration. But the foreign priority documents were listed in the Oath and Declaration, precisely to claim priority to the earliest effective date.

Applicant continued the practice of claiming the earliest effective priority date in several continuing applications filed subsequent to the '778 Application. For example, several continuation applications were filed that in fact claim domestic priority back to U.S. Application 09/331,002 filed June 14, 1999, and foreign priority to PCT Application No. PCT/EP98/01187 filed March 3, 1998 and German Application No. 19708755.8 filed on March 4, 1997. The following applications each claimed priority to U.S. Application No. 09/331,002 and beyond: U.S. Application No. 11/467,073,²² 11/467,092,²³ U.S. Application No. 11/928,283,²⁴ and U.S. Application No. 12/891,443.²⁵

In view of the repeated practice of claiming priority to U.S. Application No. 09/331,002 and foreign priority in related applications both previous and subsequent to the '778 Application, Applicant contends that the '778 Application was intended to also claim priority in the same manner – through U.S. Application 09/331,002. Further, no compelling or even satisfactory reason exists as to why Applicant would purposefully omit a specific reference to U.S. Application No. 09/331,002 when every other application takes advantage of the earliest effective filing date by claiming priority through U.S. Application No. 09/331,002. The most logical explanation is that an administrative error occurred at the time of filing the '778 Application, which went unnoticed until very recently. Applicant is thus taking swift action to claim priority to U.S. Application No. 09/331,002 and foreign priority therefrom, the delay in which was unintentional.

Accordingly, Applicant contends that the Patent Office should GRANT this Petition.

B. THE PATENT OFFICE AFFORDED THE '778 APPLICATION FOREIGN PRIORITY DURING EXAMINATION OF THE '778 APPLICATION

In addition to Applicant believing that the priority chain was properly established at the time of filing, the Patent Office likewise treated the '778 Application as effectively claiming

11/078,778

²² U.S. Patent No. 8,966,144, Col. 1, lines 8–13.

²³ U.S. Patent No. 9,189,437. Col. 1, lines 8–13.

²⁴ U.S. Patent Application Publication No. 2008/0209088, ¶ [0001].

²⁵ U.S. Patent No. 8,504,746, Col. 1, lines 8–16.

priority to at least March 4, 1997 via the PCT Application and the German priority document. For instance, the Patent Office issued a Bibliographic Data Sheet for the '778 Application on June 20, 2006, which lists the continuing data (U.S. Application No. 10/219,105) and the foreign applications (PCT Application No. PCT/EP98/01187 and German Patent Application No. 19708755.8).26 On the same Bibliographic Data Sheet for the '778 Application, the Examiner verified and acknowledged by signature that foreign priority was claimed, and that 35 U.S.C. § 119 (a-d) conditions were met.²⁷

Further, in a Notice of Allowability, the Patent Office indicated that "Certified copies of the priority documents have been received in Application No. 09/331,002"28 evidencing the Patent Office (and Applicant's) understanding that a complete priority chain back to the 09/331,002 Application and the foreign applications had been properly established.

Not only do these Patent Office documents evidence that the Patent Office treated the '778 Application as having a proper priority chain, but, when viewed by Applicant, they would have served as reinforcement that the priority chain was indeed filed correctly, and that no further or corrected claim for priority is required. Thus, any delay in filing this Petition was unintentional, given the Patent Office treatment of the '778 Application at and after the time of filing.

Accordingly, Applicant contends that the Patent Office should GRANT this Petition.

11 THE SPECIFIC REQUIREMENTS OF 37 C.F.R. § 1.55 AND §1.78 ARE MET

Applicant contends that the requirements of 37 C.F.R. § 1.55 and § 1.78 are met with this Petition. Specifically, 37 C.F.R. §1.55 requires, in pertinent part, "[a] petition to accept a delayed claim for priority...must be accompanied by:

- (1) The priority claim...in an application data sheet (§ 1.76(b)(6)), identifying the foreign application to which priority is claimed, by specifying the application number, country (or intellectual property authority), day, month, and year of its filing, unless previously submitted;
- (2) A certified copy of the foreign application, unless previously...;
- (3) The petition fee as set forth in $\S 1.17(m)$; and

28 See Appendix D.

 $^{^{26}}$ See Appendix C. 27 Id.

(4) A statement that the entire delay between the date the priority claim was due under this section and the date the priority claim was filed was unintentional.²⁹

Applicant submits along with the Petition a priority claim in a corrected application data sheet. A preliminary amendment to amend the specification to include a reference to the priority chain in the specification is also provided herewith. The certified copy of the foreign application (German Patent Application No. 19708755.8) has already been submitted on the record as recognized by the Patent Office in the Notice of Allowability. The fee has been paid simultaneously with the submission of this Petition. Applicant contends that the entire delay between the date the priority claim was due and the date that the priority claim was filed was unintentional. As noted above, Applicant initially claimed priority over eleven years ago, but due to a very recent discovery of an administrative error at the time of filing the '778 Application, Applicant swiftly files this Petition.

Likewise, Applicant contends that the specific requirement of 37 C.F.R. § 1.78 are met by this Petition and the accompanying documentation. Specifically, 37 C.F.R. §1.78 requires, "[a] petition to accept an unintentionally delayed claim…must be accompanied by:

- (1) The reference required by 35 U.S.C. 120 and paragraph (d)(2) of this section to the prior-filed application, unless previously submitted;
- (2) The petition fee as set forth in § 1.17(m); and
- (3) A statement that the entire delay between the date the benefit claim was due under paragraph (d)(3) of this section and the date the benefit claim was filed was unintentional.³³

Applicant submits along with the Petition a reference to the prior-filed application, in a corrected application data sheet.³⁴ The preliminary amendment submitted herewith further references the

²⁹ 37 C.F.R. § 1.55.

³⁰ See Appendix E.

³¹ See Appendix F.

³² See Appendix D.

^{33 37} C.F.R. § 1.78.

³⁴ See Appendix E.

priority chain in the specification.³⁵ The fee has been paid simultaneously with the submission of

this Petition. Applicant contends that the entire delay between the date the priority claim was

due and the date that the priority claim was filed was unintentional for at least the reasons

provided above.

Accordingly, Applicant contends that the Patent Office should GRANT this Petition.

M. CONCLUSION

Based on the reasons set forth above, Applicant respectfully requests that the Patent

Office GRANT this petition under 37 C.F.R. § 1.55 and §1.78.

The Patent Office is hereby authorized to charge any fees which may be required

regarding this filing, or credit any overpayment, to Deposit Account No. 19-0513. Should no

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35 See Appendix F.

11/078,778

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ZTE (USA) 1003, Page 471

APPENDIX A

PTC/SB/05 (08-03)
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UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

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	First Inventor	TASLER	~
	Title	Flexible Interface	
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Prior application information. Examiner Harold J. Kim Art Unit: 2182 For CONTINUATION OF DIVISIONAL APPS only; The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.							
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Name (Print/Type)	Michael A. Gle	enn			~~~~		~~~~~	March 11	~~~~~

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- Fee Transmittal (1 page, in duplicate);
- Copy of Combined Oath and Declaration (signed) (3 pages);
- Copy of Revocation of Prior and Grant of New Power of Attorney (1 page);
- Specification (23 pages);
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APPENDIX B

Declaration and Power of Attorney For Patent Apple Day Erklärung Für Patentanmeldungen Mit Vollmacht

German Language Declaration

Als nachstehend benannter Erfinder erkläre ich hiermit an Eides Statt:	As a below named inventor, I hereby declare that:					
dass mein Wohnsitz, meine Postanschrift, und meine Staats- angehörigkeit den im Nachstehenden nach meinem Namen aufgeführten Angaben entsprechen.	My residence, post office address and citizenship are as stated below next to my name.					
dass ich, nach bestem Wissen der ursprüngliche, erste und alleinige Erlinder (falls nachstehend nur ein Name angegeben ist) oder ein ursprünglicher, erster und Miterfinder (falls nachstehend mehrere Namen aufgeführt sind) des Gegenstandes bin, für den dieser Antrag gestellt wird und für den ein Patent heartragt wird. Die Erfindung mit dem Titel:	I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled					
	Flexible Interface					
deren Beschreibung	the specification of which					
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Ich bestätige hiemit, dass ich den Inhalt der obigen Palen- tanmeldung einschliesslich der Ansprüche durchgesehen und verständen habe, die eventuell durch einen Zusatzantrag wie oben erwähnt abgeänden wurde.	I hereby state that I have reviewed and understand the con- tents of the above identified specification, including the claims, as amended by any amendment referred to above.					
Ich erkenne meine Pflicht zur Offenbarung irgendwelcher In- formationen, die für die Prüfung der vorliegenden Anmektung in Einklang mit Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) von Wichegkeit sind, an.	I acknowledge the duty to disclose information which is ma- terial to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).					
ich beanspruche hiermit auständische Prioritätsvorteile ge- mäss Abschnitt 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 119 aller unten angegebenen Ausland- sanmeldungen für ein Patent oder eine Erfindersurkunde, und habe auch alle Austandsanmeldungen für ein Patent oder eine Erfindersurkunde nachstehend gekennzeichnet, die ein Anmeldedatum haben, das vor dem Anmeldedatum der An- meldung liegt, für die Priorität beansprucht wird.	I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any loreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:					
Page	1 of 3					

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Priorität beansprücht	113		Priority (Claimed
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I hereby claim the benefit under Title 35. United States Code, §120 of any United States application(s) fisted below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35. United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37. Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

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Page 2 of 3

German Language Declaration



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James M. Graziano	28,300	Thomas Swenson	36,696
Carl A. Forest	28,494	Curtis A. Vock	38,356
Dan Cleveland, Jr.	36,106	Kirk D. Williams	42,229
Michael J. Setter	37,936	Steven W. Weinrieb	26,520

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Oired Telephone Calls to: (name and telephone number)

Postanschrift:

Send Correspondence to:

Carl A. Forest, Ph.D.

c/o DUFT, GRAZIANO & FOREST, P.C.

1790-30th Street

- Suite 140 -

Boulder, Colorado 80301-1018, U.S.A.

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Page 3 of 3

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Michael Tasler, et al.

Docket No. SCHO0102D

Serial No.: 10/219,105

Group Art Unit: Unassigned

Filed: 08/15/2002

Examiner: Unassigned

Title: Flexible Interface for Communication Between a Host and an

Analog I/O Device Connected to the Interface...

Revocation of Prior and Grant of New Power of Attorney

As Assignee of record of the entire interest of the above-identified patents, all powers of attorney previously given are hereby revoked and the following attorneys and agents are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office connected therewith.

MICHAEL A. GLENN, Reg. No. 30,176 DONALD M. HENDRICKS, Reg. No. 40,355 KIRK WONG, Reg. No. 43,284 CHRISTOPHER PEIL, Reg. No. 45,005 JULIA THOMAS, Reg. No. 52,283

Please send all correspondence for this application as follows:

GLENN PATENT GROUP
3475 Edison Way, Suite L
Menlo Park, CA 94025
Please direct any calls to 650-474-8400.

Please change the Attorney Docket No. to SCHO0102D.

In accordance with 37 CFR 3.73, the assignee hereby certifies that the evidentiary documents with respect to its ownership have been reviewed and that, to the best of assignee's knowledge and belief, title is in the assignee seeking to take this action.

01/10/2003

Date

Name: Michael Tasler

Title:

APPENDIX C



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

Bib Data Sheet

CONFIRMATION NO. 8978

SERIAL NUMBER 11/078,778	FILING OR 371(c) DATE 03/11/2005 RULE		LASS 710	GROUP ART UNIT 2181			RNEY DOCKET NO. 576/96910	
## APPLICANTS Michael Tasler, Wurzburg, GERMANY; ** CONTINUING DATA **********************************								
Foreign Priority claimed 35 USC 119 (a-d) conditions met yes no Met after Allowance Verified and Acknowledged Examiner's Signature initials ADDRESS Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago, IL60606								
TITLE Flexible interface								
FILING FEE FEES: Authority has been given in Paper RECEIVED No to charge/credit DEPOSIT ACC 2080 No for following:					parage	Fees (Fi	rocessing	g Ext. of time)

APPENDIX D

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE Onited States Patent and Trademark Office address: COMMISSIONER FOR PATENTS

NOTICE OF ALLOWANCE AND FEE(S) DUE

7590

07/28/2006

Jeffrey W. Salmon, Esq. Welsh & Katz, Ltd. 22nd Floor 120 S. Riverside Plaza Chicago, IL 60606 EXAMINER

KIM, HAROLD J

ART UNIT PAPER NUMBER

2181

DATE MAILED: 07/28/2006

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/078.778	03/11/2005	Michael Tasler	9576/96910	8978

TITLE OF INVENTION: ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$0	\$1000	10/30/2006

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

- A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.
- B. If the status above is to be removed, check box 5b on Part B Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

- A. Pay TOTAL FEE(S) DUE shown above, or
- B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or Fax (571)-273-2885

NSTRUCTIONS: This appropriate. All further indicated unless corrected anitonance fee notifications.	ed below or directed of	for transmitting the ISSI ng the Patent, advance o herwise in Block 1, by (UE FEE and PUBLICAT rders and notification of rand specifying a new corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corresponding to the corr	ION FEE (if requinaintenance fees values) spondence address;	ired). I vill be and/o	Blocks 1 through 5 sh mailed to the current of r (b) indicating a separ	ould be completed where correspondence address as ate "FEE ADDRESS" for
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APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
11/078,778	03/11/2005	***************************************	Michael Tasler	***************************************	**********	9576/96910	8978
TITLE OF INVENTION	: ANALOG DATA GEI	NERATING AND PROC	ESSING DEVICE FOR U	SE WITH A PERS	ONAL	COMPUTER	
APPLN, TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSU	e fee	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$0		\$1000	10/30/2006
EXAM	INER	ART UNIT	CLASS-SUBCLASS				
KIM, HA	ROLD J	2181	710-015000	ð			
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	ication (or "Fee Address 2 or more recent) attach	" Indication form hed. Use of a Customer	(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.				
. ASSIGNEE NAME A	ND RESIDENCE DATA	A TO BE PRINTED ON	THE PATENT (print or ty)	oe)		izazizazia geografia na	······································
PLEASE NOTE: Unl	less an assignee is ident h in 37 CFR 3.11. Com	tified below, no assignee pletion of this form is NO	data will appear on the p of a substitute for filing an	atent. If an assign assignment.	ee is i	dentified below, the do	cument has been filed for
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lease check the appropr	iate assignee category or	r categories (will not be p	rinted on the patent):	I Individual L. I Co	orporat	ion or other private grou	ip entity L. Government
a. The following fee(s):	are submitted:	4	b. Payment of Fcc(s): (Plea	ise first reapply ai	3y prev	viously paid issue fee s	hown above)
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	s SMALL ENTITY state		D b. Applicant is no long	ger claiming SMAI	LL EN	TITY status. See 37 CF	R 1.27(g)(2).
IOTE: The Issue Fee and iterest as shown by the r	d Publication Fee (if req records of the United Sta	uired) will not be accepte ates Patent and Trademark	d from anyone other than to Office.				
Authorized Signature	BANKET BANKS			Date			
Typed or printed name	e			Registration N	lo		
his collection of inform application. Confident	ation is required by 37 C	FR 1.311. The information U.S.C. 122 and 37 CFR	on is required to obtain or r	etain a benefit by t	he pub	lic which is to file (and	by the USPTO to process)

an application. Confidentially is governed by 35 O.S.C. 122 and 37 CPR 1.14. This concerton is estimated to take 12 minutes to complete, including gamering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
11/078,778	03/11/2005	Michael Tasler	9576/96910	8978	_
75	90 07/28/2006		EXAM	IINER	9
Jeffrey W. Salmo	n. Esa.		KIM, HAROLD J		
Welsh & Katz, Ltd			ART UNIT	PAPER NUMBER	, married
22nd Floor 120 S. Riverside Pl Chicago, IL 60606			2181 DATE MAILED: 07/28/200	6	.,

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 40 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 40 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)
	11/078,778	TASLER, MICHAEL
Notice of Allowability	Examiner	Art Unit
	Harold Kim	2181
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comm GHTS. This application is and MPEP 1308.	in this application. If not included nunication will be mailed in due course. THIS subject to withdrawal from issue at the initiative
,		mu the NOL med on 0/30/2000.
2. X The allowed claim(s) is/are 17, 19-44, 46-69, 71-93. [now]	<u>1-74]</u> .	
 Acknowledgment is made of a claim for foreign priority ur a)	been received. been received in Applicati cuments have been receive	on No. <u>09/331,002</u> . ed in this national stage application from the
noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subm	IENT of this application.	
INFORMAL PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the second process.	on's Patent Drawing Revie s Amendment / Comment o .84(c)) should be written on	or in the Office action of the drawings in the front (not the back) of
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s) 1. Notice of References Cited (PTO-892)	5. ☐ Notice of I	nformal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		Summary (PTO-413),
3. ⊠ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No :8), 7. ⊠ Examiner	./Mail Date s Amendment/Comment
Paper No./Mail Date 20060607; 20060630 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛭 Examiner:	s Statement of Reasons for Allowance
	9.	FRITZ FLEMING SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100

Application/Control Number: 11/078,778 Page 2

Art Unit: 2181

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

- 2. Authorization for this examiner's amendment was given in telephone interviews with Jeffrey W. Salmon, Reg. No. 37,435 on 7/20/2006.
- 3. Cancel claims 94-113.

Reasons for Allowance

4. The following is an examiner's statement of reasons for allowance:

The prior art cited on the attached form PTO-892 is the most relevant prior art known. However, Applicant's claimed invention distinguishes over the prior art for the following reasons. The claims are allowable over the prior art of record because none of the references, either alone or in combination, discloses or renders obvious that a personal computer having at lease one multi-purpose interface to which inquiry signals are periodically sent, and an analog data generating and processing device having an input/output port that is operatively connected to the multi-purpose interface of a personal computer including a sensor that is mounted on a housing, the sensor being adapted to receive analog wave signal that are generated by a source that is external to

Application/Control Number: 11/078,778 Page 3

Art Unit: 2181

the housing, the sensor to generate sets of analog data from the analog wave signals that is receives, generates a set of digitized analog data from each set of analog data, a first set of instructions being stored in a memory that are utilized by a processor to cause the sets of digitized analog data to be individually stored in the memory irrespective of whether or not the analog data generating and processing device has been recognized by a personal computer, and a response signal being automatically and without user intervention sent from an input/output port to a multi-purpose interface after an inquiry signal has been received by the input/output port, the receipt and processing of the receipt signal by the personal computer causing it to automatically and without user intervention recognized the analog data generating and processing device as being a device having digital data that is stored therein and selectively retrievable therefrom. The cited references at most, contain only one or the other, but not all the limitations claimed. There would also be no reason to make any of the possible combinations.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harold Kim whose telephone number is 703-305-1948. The examiner can normally be reached on Monday-Friday 9 AM - 5 PM.

Application/Control Number: 11/078,778 Page 4

Art Unit: 2181

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Fritz Fleming, can be reached on 571-272-4145. The fax phone numbers for the organization where this application or proceeding is assigned is 571-273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-746-7240.

Harold J. Kim

Patent Examiner

July 20, 2006/HK

FRITZ FLEMING SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100

Hzm. Henry

7/24/2006

ZTE (USA) 1003, Page 490

APPENDIX E

Application Data Sheet 37 CFR 1.76					Attorney Cocket Number		9576/96910					
					Application Number).				
Title of In	vention	ANALO	DG DA	TA GENE	RATING	AND PRO	OCESS	ING DE	VICE FOR	RUSE	WITH A PERSONAL COMPUTER	
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Address	2											
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Applica	ition In	forma	itior	1 :								
Title of th	ne Inventio	on	ANAL	OG DAT	A GENE	RATING A	ND PR	OCESS	ING DEVI	CE FO	R USE WITH A PERSONAL COMPUT	ER
Attorney	Docket N	umber	PLG	i.51819-N	iyc			S	mall En	tity St	atus Claimed	
Applicati	on Type		Nont	orovision	 al							
Subject I	Matter		Utilit	Σ							•••••	
Suggeste	ed Class (i	if any)						S	ub Clas	s (if a	iny)	
Suggeste	ed Techno	ology C	enter	(if any)							
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					1						ZTE (USA) 1003, Pa	ge 492

Annlination Da	to Chast 27 CCD 1 7C	Attorney Cocket Number	9576/96910
Application Data Sheet 37 CFR 1.76		Application Number	
Title of Invention			JSE WITH A PERSONAL COMPUTER

Publication Information:

Request Early Publication (Fee required at time of Recuest 37 CFR 1.219)
Request Not to Publish. I hereby request hat the attached application not be published under 35 U.S. C. 122(b) and certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multiateral international agreement, that requires publication at eighteen months after filing.

Representative Information:

this information in the Appli Enter either Customer	cation Data Sheet does not c Number or complete	onstitute a power of attorney in	r of attorney in the application. Providing the application (see 37 CFR 1.32). section below. If both sections ring processing.
Please Select One:	Customer Number	US Patent Practitioner	○ Limited Recognition (37 CFR 11.9)
Customer Number	69995		

Domestic Benefit/National Stage Information:

This section allows for the applicant to either claim benefit under 35 U.S.C. 119(e), 120, 121, or 365(c) or indicate National Stage entry from a PCT application. Providing this information in the application data sheet constitutes the specific reference required by 35 U.S.C. 119(e) or 120, and 37 CFR 1.78(a)(2) or CFR 1.78(a)(4), and need not otherwise be made part of the specification.

Prior Application	on Status	Patented			Re	nove	
Application Number	Conti	nuity Type	Prior Application Number	Filing Date (YYYY-MM-DD)	Patent Number	Issue Date (YYYY-MM-DD)	
<u>11078778</u>	Continuat	ion of	10219105	2002-08-15	6895449	2005-05-17	
Prior Application	on Status	Patented	<u> </u>				
Application Number	Conti	nuity Type	Prior Application Number	Filing Date (YYYY-MM-DD)	Patent Number	issue Date (YYYY-MM-DD)	
<u>10219105</u>	Division	<u>of</u>	09331002	1999-06-14	6470399	2002-10-22	
Prior Application	on Status	Expired				nove	
Application N	umber	Cont	inuity Type	Prior Application Nur	nber Filing Da	iber Filing Date (YYYY-MM-DD)	
09331002 a 371 of inter			mational PCT/EP98/01187 1998-03-03				
Additional Dome			ge Data may be ger	nerated within this forr	n		

Foreign Priority Information:

This section allows for the applicant to claim benefit of foreign priority and to identify any prior foreign application for which priority is not claimed. Providing this information in the application data sheet constitutes the claim for priority as required by 35 U.S.C. 119(b) and of OFR 1.50(a).

Application Data Sheet 37 CFR 1.76		Attorney Cocket Number Application Number	9576/96910
Title of Invention	ANALOG DATA GENERATING	AND PROCESSING DEVICE FOR	USE WITH A PERSONAL COMPUTER

		Re	nove							
Application Number	Country	Parent Filing Date (VVVV-MM-DD)	Priority Claimed							
<u>19708755.8</u>	DE	<u>1997-03-04</u>								
Additional Foreign Priority Add hulton	Additional Foreign Priority Data may be generated within this form by selecting the									

Assignee Information:

	the application data sheet does not sal	ostitute for compliance with	any requirement of part 3 of Title 37						
Assignee 1									
If the Assignee is an Org	anization check here. 🛛								
Organization Name	Papst Licensing GmbH & Co., KG	Papst Licensing GmbH & Co., KG							
Mailing Address Inform	ation.								
Address 1	BAHNHOFSTRASSE 33								
Address 2									
City	ST. GEORGEN	State/Province							
Country		Postal Code	<u>78112</u>						
Phone Number		Fax Number							
Email Address									
Additional Assignee Data button.	nay be generated within this for	n by selecting the Add							

Signature:

A signature of the applicant or representative is required in accordance with 37 CFR 1.33 and 10.18. Please see 37 CFR 1.4(d) for the form of the signature.									
Signature	/Jason A. Murphy/		Date (YYYY-MM-DD)	2016-11-10					
First Name	Jason	Last Name	Murphy	Registration Number	63423				

This collection of information is required by 37 CFR 1.76. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This selfaction is estimated to take 23 minutes to complete, including gathering, preparing, and submitting the completed application data sheet form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450 Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450**.

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that. (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office hay not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

- 1. The information on this form will be treated confidentially in the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these records.
- 2. A record from this system of records may be disclosed, as a reutine use, in the source of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
- A record in this system of records may be disclosed, as a putine use, to a Member of Congress submitting a request involving an
 individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of
 the record.
- 4. A record in this system of records may be disclosed, as a mutine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
- A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
- A record in this system of records may be disclosed, as a nutine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Erergy Act (42 U.S.C. 218(c)).
- 7. A record from this system of records may be disclosed, as a routine use, to the Administrator. General Services, or his/her designee, during an inspection of records conducted by GSA as part of this agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
- A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.
- A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of lay or regulation.

ZTE (USA) 1003, Page 495

APPENDIX F

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1st Named Inventor : Michael Tasler Examiner : Kim, Harold J.

Serial No. : 11/078,778 Group Art Unit : 2181

Filing Date : March 11, 2005 Confirmation No. : 8978

Docket No. : 9576/96910

Title : ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE

WITH A PERSONAL COMPUTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Kindly enter the following:

In the Specification:

Please add the following paragraph above the first paragraph on page 1 of the specification; no new matter is entered:

CROSS-REFERENCE TO RELATED APPLICATIONS

This Application is a continuation application of U.S. Application No. 10/219,105, filed August 15, 2002, now U.S. Patent No. 6,895,449, which is a divisional of U.S. Application No. 09/331,002, filed June 14, 1999, now U.S. Patent No. 6,470,399, which is the National Stage application of PCT Application No. PCT/EP98/01187, filed March 3, 1998, which claims priority to German Patent Application No. 19708755.8 filed March 4, 1997.

CONCLUSION

The Director is hereby authorized to charge any fees which may be required regarding this filing, or credit any overpayment, to Deposit Account No. 19-0513. Should no proper payment be enclosed herewith, as by a check or credit card payment form being in the wrong amount, unsigned, post-dated, otherwise improper or informal or entirely missing, the Director is authorized to charge the unpaid amount to Deposit Account No, 19-0513.

Date: November 10, 2016

/Jason A. Murphy/ Jason A. Murphy Registration No. 63,423 Customer No. 69995 Schmeiser, Olsen & Watts 22 Century Hill Drive, St. 302 Latham, New York 12110 Tel. (518) 220-1850 Fax: (518) 220-1857

11/078,778

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1st Named Inventor : Michael Tasler Examiner : Kim, Harold J.

Serial No. : 11/078,778 Group Art Unit : 2181

Filing Date : March 11, 2005 Confirmation No. : 8978

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WITH A PERSONAL COMPUTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Date: November 10, 2016

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3

11/078,778

Application Data Sheet 37 CFR 1.7						Attorney Docket Number			lumber	<u>9576/96910</u>			
						Application Number							
Title of	de of Invention ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER												
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Application Data Sheet 37 CFR 1.76		Application Number				
Title of Invention	ANALOG DATA GENERATING AND PROCESSING DEVICE FOR USE WITH A PERSONAL COMPUTER					

Publication Information:

Request Early Publication (Fee required at time of Recuest 37 CFR 1.219)
Request Not to Publish. I hereby request hat the attached application not be published under 35 U.S. C. 122(b) and certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multiateral international agreement, that requires publication at eighteen months after filing.

Representative Information:

Representative information should be provided for all practitioners having a power of attorney in the application. Providing this information in the Application Data Sheet does not constitute a power of attorney in the application (see 37 CFR 1.32). Enter either Customer Number or complete the Representative Name section below. If both sections are completed the Customer Number will be used for the Representative Information during processing.								
Please Select One:	Customer Number	US Patent Practitioner	Limited Recognition (37 CFR 11.9)					
Customer Number	<u>69995</u>							

Domestic Benefit/National Stage Information:

This section allows for the applicant to either claim benefit under 35 U.S.C. 119(e), 120, 121, or 365(c) or indicate National Stage entry from a PCT application. Providing this information in the application data sheet constitutes the specific reference required by 35 U.S.C. 119(e) or 120, and 37 CFR 1.78(a)(2) or CFR 1.78(a)(4), and need not otherwise be made part of the specification.

Prior Application Status		<u>Patented</u>	Remove				
Application Number	Continuity Type		Prior Applicatior Number	Filing Date (YYYY-MM-DD)	Patent Number	Issue Date (YYYY-MM-DD)	
<u>11078778</u>	11078778 Continuation of		<u>10219105</u>	<u>2002-08-15</u>	<u>6895449</u>	<u>2005-05-17</u>	
Prior Applicati	on Status	<u>Patented</u>	Remove			nove	
Application Number	Continuity Type		Prior Applicatior Number	Filing Date (YYYY-MM-DD)	Patent Number	Issue Date (YYYY-MM-DD)	
<u>10219105</u>	10219105 <u>Division of</u>		<u>09331002</u>	<u>1999-06-14</u>	6470399	<u>2002-10-22</u>	
Prior Application	on Status	Expired			Rei	1046	
Application Number		Conti	Continuity Type Prior Applicatio		iber Filing Date (YYYY-MM-DD)		
09331002		a 371 of international PCT/EP98/01187		1998-03-03			
Additional Domestic Benefit/National Stage Data may be generated within this form							

Foreign Priority Information:

by selecting the Add button.

This section allows for the applicant to claim benefit of foreign priority and to identify any prior foreign application for which priority is not claimed. Providing this information in the application data sheet constitutes the claim for priority as required by 35 U.S.C. 119(b) and 97 CFR 1.55(a).

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Application Data Sheet 37 CFR 1.76		Attorney Cocket Number	<u>9576/96910</u>
		Application Number	
Title of Invention	ANALOG DATA GENERATING	AND PROCESSING DEVICE FOR	USE WITH A PERSONAL COMPUTER

		Re	3086
Application Number	Country ^l	Parent Filing Date (YYYY-MM-DD)	Priority Claimed
<u>19708755.8</u>	<u>DE</u>	<u>1997-03-04</u>	Yes ○ No
Additional Foreign Priority Add button	Data may be generated wit	thin this form by selecting the	

Assignee Information:

	the application data sheet does not su nment recorded in the Office	bstitute for compliance with	any requirement of part 3 of Title 37			
Assignee 1						
If the Assignee is an Org	anization check here.					
Organization Name	Papst Licensing GmbH & Co., KG	apst Licensing GmbH & Co., KG				
Mailing Address Inform	ation:					
Address 1	BAHNHOFSTRASSE 33					
Address 2						
City	ST. GEORGEN	State/Province				
Country		Postal Code	<u>78112</u>			
Phone Number		Fax Number				
Email Address						
Additional Assignee Data button.	nay be generated within this for	m by selecting the Add				

Signature:

A signature of the applicant or representative is required in accordance with 37 CFR 1.33 and 10.18. Please see 37 CFR 1.4(d) for the form of the signature.					
Signature	/Jason A. Murphy/			Date (YYYY-MM-DD)	2016-11-10
First Name	Jason	Last Name	Murphy	Registration Number	63423

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Doc Code: PA.. Document Description: Power of Attorney

PTO/AIA/82B (07-13) Approved for use through 11/30/2014. OMB 0651-0051

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POWER OF ATTORNEY BY APPLICANT

	y revoke all prev ces below.	rious powers of attorney given in	the applicatio	n identified in <u>either</u> th	ne attached transmi	ttal letter or
	A	pplication Number	i	Filing Date		
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	I hereby appoint to transact all buthe attached tran OR I hereby appoint all business in the	the Patent Practitioner(s) associated siness in the United States Patent ar ismittal letter (form PTO/AIA/82A) or Practitioner(s) named in the attached by United States Patent and Trademaittal letter (form PTO/AIA/82A) or ider	d with the follow nd Trademark C identified abov d list (form PTC ark Office conne	ring Customer Number a Office connected therewi e. 69995 O/AIA/82C) as my/our attected therewith for the paragraphs	is my/our attorney(s) of the for the application of the application of the former (s) at the term of the former (s) at the temperature of the former (s) at the temperature of the former (s) at the temperature of the former (s) at the temperature of the former (s) at the temperature of the former (s) at the temperature of the former (s) at the temperature of the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (s) at the former (referenced in and to transact
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I am the	Applicant (if the	Applicant is a juristic entity, list the Ap	opticant name i	n the box):		
Pap	st Licens	sing GmbH & Co.,	KG			
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	Legal Represent	ative of a Deceased or Legally Incap	acitated Invent	or (title not required belo	św)	
		son to Whom the Inventor is Under a				uristic entity)
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·		concurrently being filed with this doci	ument) (provide	signer's title if applican		
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		e title is supplied below) is authorized t	to act on behalf	**************************************	***************************************	
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Nam		Daniel Papst				
Title		Managing Director	£			
		s form must be signed by the applicant re than one applicant, use multiple form		with 37 GFR 1.33. See 37	UHR 1.4 for signature	requirements
Tota	l of	forms are submitted.				

This collection of information is required by 37 CFR 1.131, 1.32, and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Office, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS: SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Electronic Patent Application Fee Transmittal					
Application Number:	11078778				
Filing Date:	11-	Mar-2005			
Title of Invention:	FLEXIBLE INTERFACE				
First Named Inventor/Applicant Name:	Michael Tasler				
Filer:	Jason Alexander Murphy/Caterina Tuminello				
Attorney Docket Number:	9576/96910				
Filed as Large Entity					
Filing Fees for Utility under 35 USC 111(a)					
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:					
Pages:					
Claims:					
Miscellaneous-Filing:					
Petition:					
PETITION FEE- 37 CFR 1.17(F) (GROUP I)		1462	1	400	400
PET. DELAY SUB OR RESTORE PRIORITY-CLAIM		1454	1	1700	1700
Patent-Appeals-and-Interference:					

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)	
Post-Allowance-and-Post-Issuance:					
Extension-of-Time:					
Miscellaneous:					
	Tot	al in USD	(\$)	2100	

Electronic Acknowledgement Receipt			
EFS ID:	27478613		
Application Number:	11078778		
International Application Number:			
Confirmation Number:	8978		
Title of Invention:	FLEXIBLE INTERFACE		
First Named Inventor/Applicant Name:	Michael Tasler		
Customer Number:	4743		
Filer:	Jason Alexander Murphy/Caterina Tuminello		
Filer Authorized By:	Jason Alexander Murphy		
Attorney Docket Number:	9576/96910		
Receipt Date:	10-NOV-2016		
Filing Date:	11-MAR-2005		
Time Stamp:	17:14:26		
Application Type:	Utility under 35 USC 111(a)		

Payment information:

Submitted with Payment	yes
Payment Type	CARD
Payment was successfully received in RAM	\$2100
RAM confirmation Number	111416INTEFSW17155100
Deposit Account	190513
Authorized User	Caterina Tuminello

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

37 CFR 1.16 (National application filing, search, and examination fees)

37 CFR 1.17 (Patent application and reexamination processing fees)

ZTE (USA) 1003, Page 510

37 CFR 1.19 (Document supply fees)37 CFR 1.20 (Post Issuance fees)37 CFR 1.21 (Miscellaneous fees and charges)

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.	
			202617			
1	Petition for review by the Office of Petitions	51819_PetitionExpediteReview _1_182_11-10-16.pdf	979c40f97341de4fb0174f50beb224eb2472 a0e4	no	2	
Warnings:						
Information:						
			8181211			
2	Petition for review by the Office of Petitions	51819_PetitionDelayedPriority Claim_wApp_11-10-16.pdf	d497afde4ef567df53ae500a3652d9d9a7ed f66f	no	39	
Warnings:						
Information:						
			155043			
3	Preliminary Amendment	51819_PreliminaryAmendment _11-10-16.pdf	d6c6a7311c7d4b8f54d44eb2435041684cc 34925	no	3	
Warnings:						
Information:						
			1166745	no	4	
4	Application Data Sheet	51819_ADS_11-10-16.pdf	01cc0a781e09544f69ac0a7c1af455a32281 48d8			
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			488882			
5	Power of Attorney	51819_POA.pdf	384fd76b3946105cb49beba0b375eca424e 11506	no	1	
Warnings:		ļ				
Information:						
			32321			
6	Fee Worksheet (SB06)	fee-info.pdf	8dbce337ac03ff9e0d2724d72d1e238a392 d3ac1	no	2	
Warnings:		1				

Information:	
Total Files Size (in bytes):	10226819

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.



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FILING OR 371(C) DATE FIRST NAMED APPLICANT ATTY. DOCKET NO./TITLE APPLICATION NUMBER 11/078,778 03/11/2005 9576/96910 Michael Tasler

4743 MARSHALL, GERSTEIN & BORUN LLP 233 SOUTH WACKER DRIVE 6300 WILLIS TOWER CHICAGO, IL 60606-6357

CONFIRMATION NO. 8978 POWER OF ATTORNEY NOTICE



Date Mailed: 11/21/2016

NOTICE REGARDING CHANGE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 11/10/2016.

 The Power of Attorney to you in this application has been revoked by the assignee who has intervened as provided by 37 CFR 3.71. Future correspondence will be mailed to the new address of record(37 CFR 1.33).

> Questions about the contents of this notice and the requirements it sets forth should be directed to the Office of Data Management, Application Assistance Unit, at (571) 272-4000 or (571) 272-4200 or 1-888-786-0101.

/sleutchit/



UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION	FILING or	GRP ART				
NUMBER	371(c) DATE	UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
11/078 778	03/11/2005	2181	3630	9576/96910	16	3

69995 SCHMEISER, OLSEN & WATTS 2500 Westchester Avenue, Suite 210 Purchase, NY 10577 CONFIRMATION NO. 8978 CORRECTED FILING RECEIPT



Date Mailed: 11/21/2016

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections

Inventor(s)

Michael Tasler, Wurzburg, GERMANY;

Applicant(s)

Michael Tasler, Wurzburg, GERMANY;

Assignment For Published Patent Application

Papst Licensing GmbH & Co., KG

Power of Attorney: The patent practitioners associated with Customer Number 69995

Domestic Applications for which benefit is claimed - None.

A proper domestic benefit claim must be provided in an Application Data Sheet in order to constitute a claim for domestic benefit. See 37 CFR 1.76 and 1.78.

Foreign Applications for which priority is claimed (You may be eligible to benefit from the **Patent Prosecution Highway** program at the USPTO. Please see http://www.uspto.gov for more information.) - None. Foreign application information must be provided in an Application Data Sheet in order to constitute a claim to foreign priority. See 37 CFR 1.55 and 1.76.

Permission to Access Application via Priority Document Exchange: No

Permission to Access Search Results: No

Applicant may provide or rescind an authorization for access using Form PTO/SB/39 or Form PTO/SB/69 as appropriate.

If Required, Foreign Filing License Granted: 04/08/2005

page 1 of 3

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 11/078,778**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

FLEXIBLE INTERFACE

Preliminary Class

710

Statement under 37 CFR 1.55 or 1.78 for AIA (First Inventor to File) Transition Applications: No

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

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Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at http://www.uspto.gov/web/offices/pac/doc/general/index.html.

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Title 35, United States Code, Section 184

Title 37, Code of Federal Regulations, 5.11 & 5.15

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APPLICATION NUMBER FILING OR 371(C) DATE FIRST NAMED APPLICANT ATTY. DOCKET NO./TITLE

11/078,778 03/11/2005 Michael Tasler

9576/96910 **CONFIRMATION NO. 8978**

69995 SCHMEISER, OLSEN & WATTS 2500 Westchester Avenue, Suite 210 Purchase, NY 10577

POA ACCEPTANCE LETTER

Date Mailed: 11/21/2016

NOTICE OF ACCEPTANCE OF POWER OF ATTORNEY

This is in response to the Power of Attorney filed 11/10/2016.

The Power of Attorney in this application is accepted. Correspondence in this application will be mailed to the above address as provided by 37 CFR 1.33.

> Questions about the contents of this notice and the requirements it sets forth should be directed to the Office of Data Management, Application Assistance Unit, at (571) 272-4000 or (571) 272-4200 or 1-888-786-0101.

/sleutchit/		