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BEFORE THE PATENT TRIAL AND APPEAL BOARD

LG Electronics, Inc. Petitioner,

> v. FastVDO LLC Patent Owner.

Patent No. 5,850,482

Inter Parte Review No.

U.S. Patent No. 5,850,482 to Meany et al.

Exhibit 1001

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[11] **Patent Number:** 5,850,482

Date of Patent:

[45]

Meany et al.

[54] ERROR RESILIENT METHOD AND APPARATUS FOR ENTROPY CODING

- [75] Inventors: James J. Meany, Des Peres; Christopher J. Martens, Creve Coeur, both of Mo.
- [73] Assignee: McDonnell Douglas Corporation, St. Louis, Mo.
- [21] Appl. No.: 633,896
- [22] Filed: Apr. 17, 1996
- [51] Int. Cl.⁶ G06K 9/00

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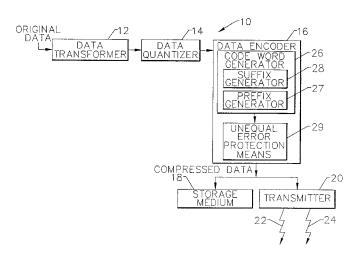
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[57] ABSTRACT

The error resilient method and apparatus for encoding data includes an encoder including a code word generator for generating a plurality of code words representative of respective portions of the data. The code word generator encodes data pursuant to split field coding in which each code word includes a prefix field and an associated suffix field. The prefix field includes information representative of a predetermined characteristic of the associated suffix field, such as the predetermined number of characters which form the associated suffix field. In addition, the suffix fields include information representative of at least some of the original data. Consequently, if the prefix field of a code word is decoded correctly, i.e, without the occurrence of bit error, the error resilient method and apparatus can correctly determine the length of the associated suffix field and the range of coefficient values to be represented by the associated suffix field such that the associated suffix field is resilient to errors. In order to increase the probability that the prefix field will be correctly decoded, the method and apparatus protects the prefix and suffix fields of the encoded data to greater and lesser degrees, respectively, such that the data can be more efficiently compressed.

30 Claims, 6 Drawing Sheets



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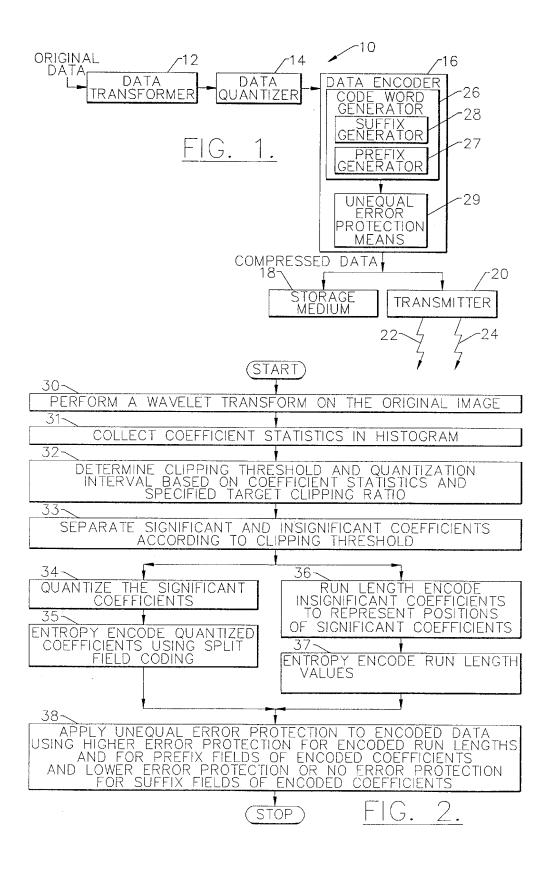
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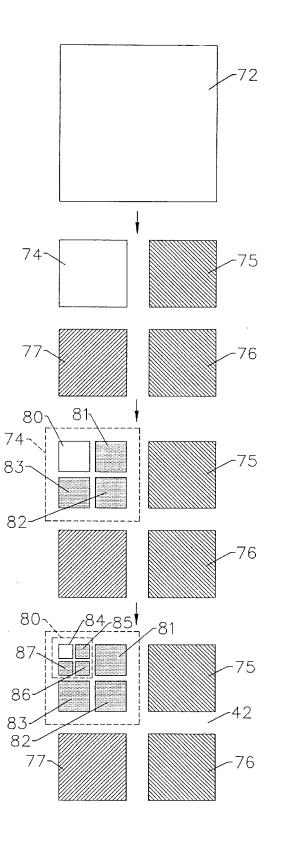


FIG. <u>3</u>.

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