



DECLARATION OF GERARD P. GRENIER

I, Gerard P. Grenier, am over twenty-one (21) years of age. I have never been convicted of a felony, and I am fully competent to make this declaration. I declare the following to be true to the best of my knowledge, information and belief:

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8. The article below has been attached as Exhibits A to this declaration:

A.	Y. Nakamura, et al., "256 QAM Modem for Multicarrier 400 Mbit/s Digital Radio" IEEE Journal on Selected Areas in Communications, Vol. 5, Issue 3, April 1987.
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9. I obtained a copy of Exhibit A through IEEE Xplore, where it is maintained in the ordinary course of IEEE's business. Exhibit A is a true and correct copy of the Exhibit as it existed on or about January 5, 2017.
10. The article abstracts from IEEE Xplore shows the date of publication. IEEE Xplore populates this information using the metadata associated with the publication
11. Y. Nakamura, et al., "256 QAM Modem for Multicarrier 400 Mbit/s Digital Radio" was published in IEEE Journal on Selected Areas in Communications, Vol. 5, Issue 3.

IEEE Journal on Selected Areas in Communications, Vol. 5, Issue 3 was published in April 1987. Copies of this publication were made available no later than the last day of the stated publication month. The article is currently available for public download from the IEEE digital library, IEEE Xplore.

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-

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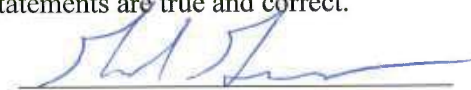


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Author(s)

Y. Nakamura ; Y. Saito ; S. Aikawa

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Abstract:

This paper describes the performance of a 256 QAM modem with 400 Mbit / s transmission capacity. A variety of novel techniques are introduced as ways to achieve good performance. Key techniques include 1) an accurate 256 QAM modulator employing a new monolithic multiplier IC, 2) a carrier recovery circuit which satisfies such requirements: good phase jitter performance and no false lock phenomenon, 3) a highly stable high-level decision circuit, and 4) a forward error correcting code. As an overall modem performance, BER characteristics and signatures are presented. The equivalent CNR degradations of 1 dB(at BER of 10^{-4}) and 2 dB (at BER of 10^{-9})are obtained using a single Lee-error correcting code and a seven-tap baseband transversal equalizer. The residual bit errors are decreased below the order of 10^{-10} . The performance of a 256 QAM multicarrier modem has given prospect for the development of 400 Mbit/s digital microwave radio system.

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Quadrature amplitude modulation, Modems, Digital communication, Bit error rate, Phase modulation, Modulation coding, Monolithic integrated circuits, Jitter, Error correction codes, Degradation

INSPEC: Non-Controlled Indexing

Quadrature amplitude modulation, Digital modulation/demodulation

Authors

Y. Nakamura
Nippon Telegraph and Telephone Corp., Kanagawa, Japan

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