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United States Patent [19]

Fujii et al.

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[54] **DECODER FOR COMPRESSED AND MULTIPLEXED VIDEO AND AUDIO DATA**

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[30] **Foreign Application Priority Data**

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Mar. 29, 1995 [JP] Japan 7-071132

[51] **Int. Cl.**[6] **H04N 7/12**

[52] **U.S. Cl.** **370/464**; 348/423; 348/10

[58] **Field of Search** 348/423, 467, 348/385, 382, 7, 10, 714; 370/389, 464, 522, 503, 428, 457, 509, 535, 536, 537

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[57] **ABSTRACT**

An apparatus for filtering TS packets multiplexed with a plurality of programs and sending the filtered packets to decoders. A packet landing buffer is provided in a RAM used for a microprocessor for system control. After a channel is selected, the microprocessor filters video and audio data and performs a value added service process.

38 Claims, 23 Drawing Sheets

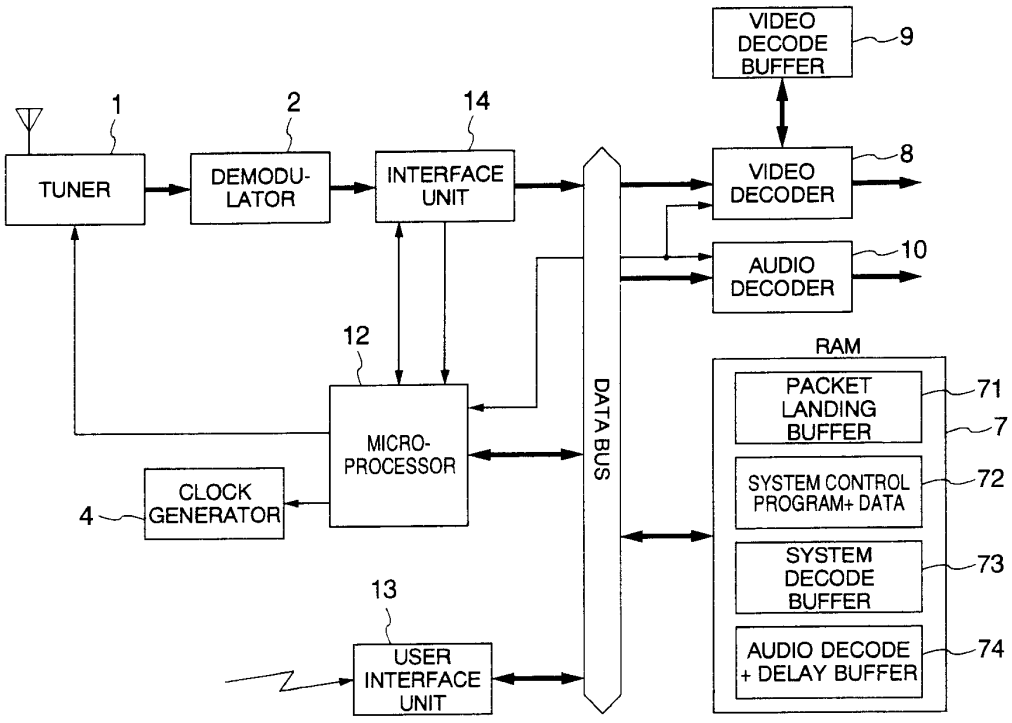


FIG.1

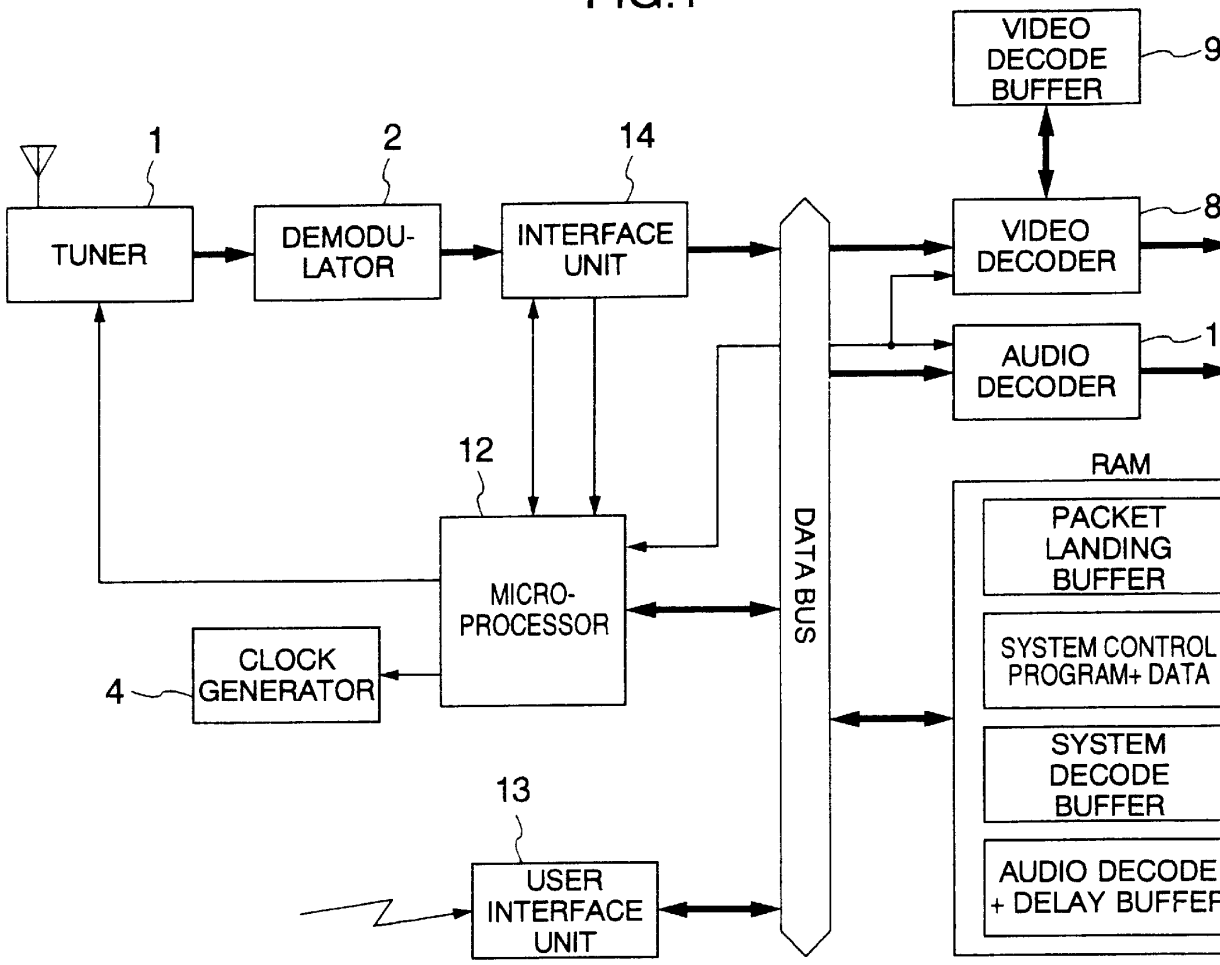


FIG.2

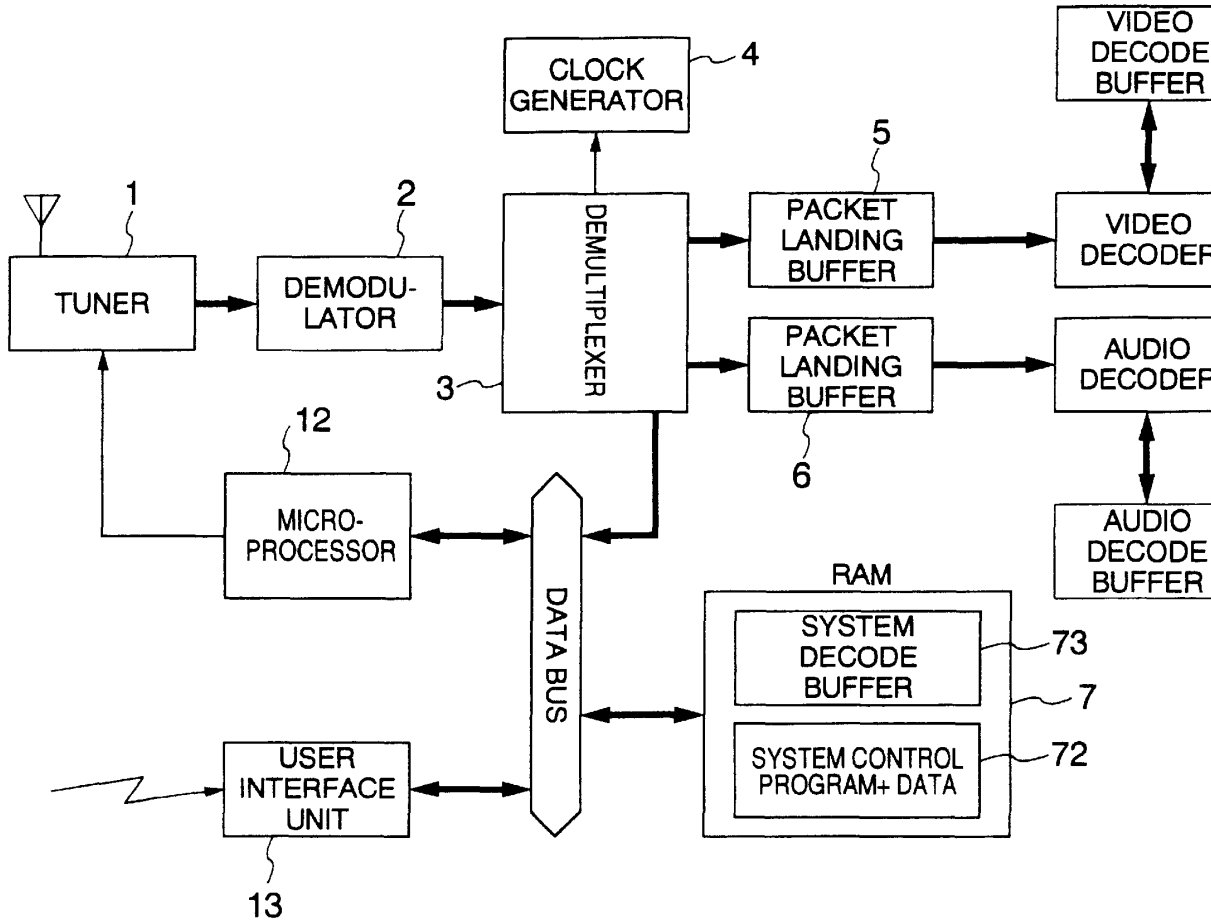


FIG.3A

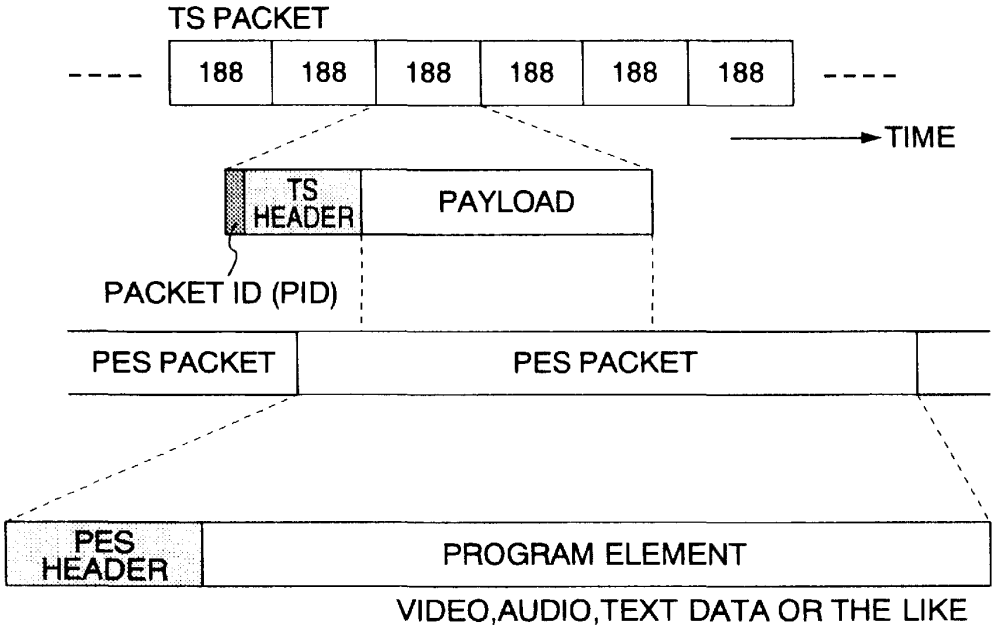


FIG.3B

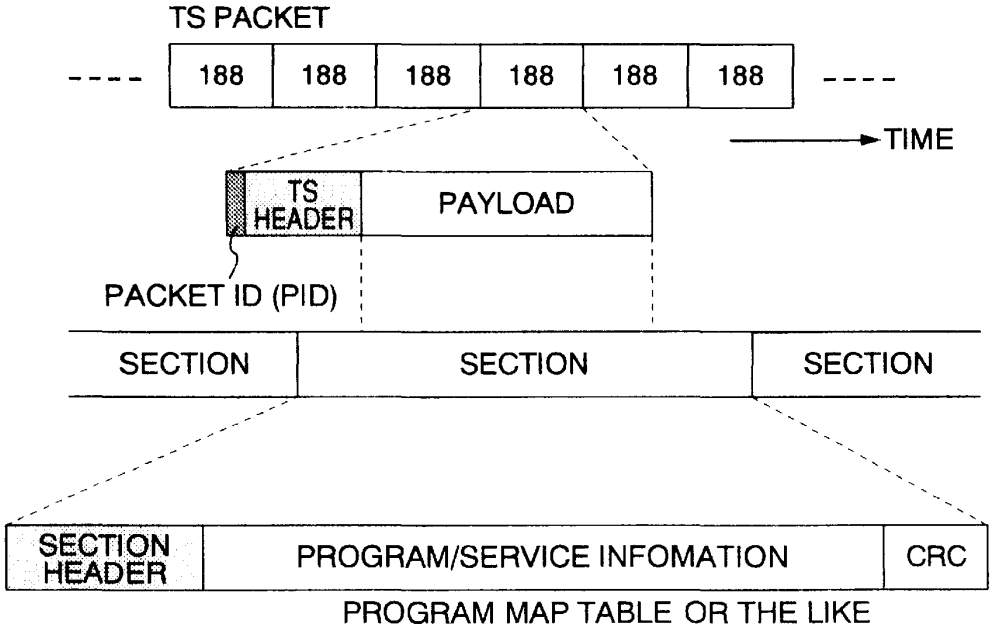
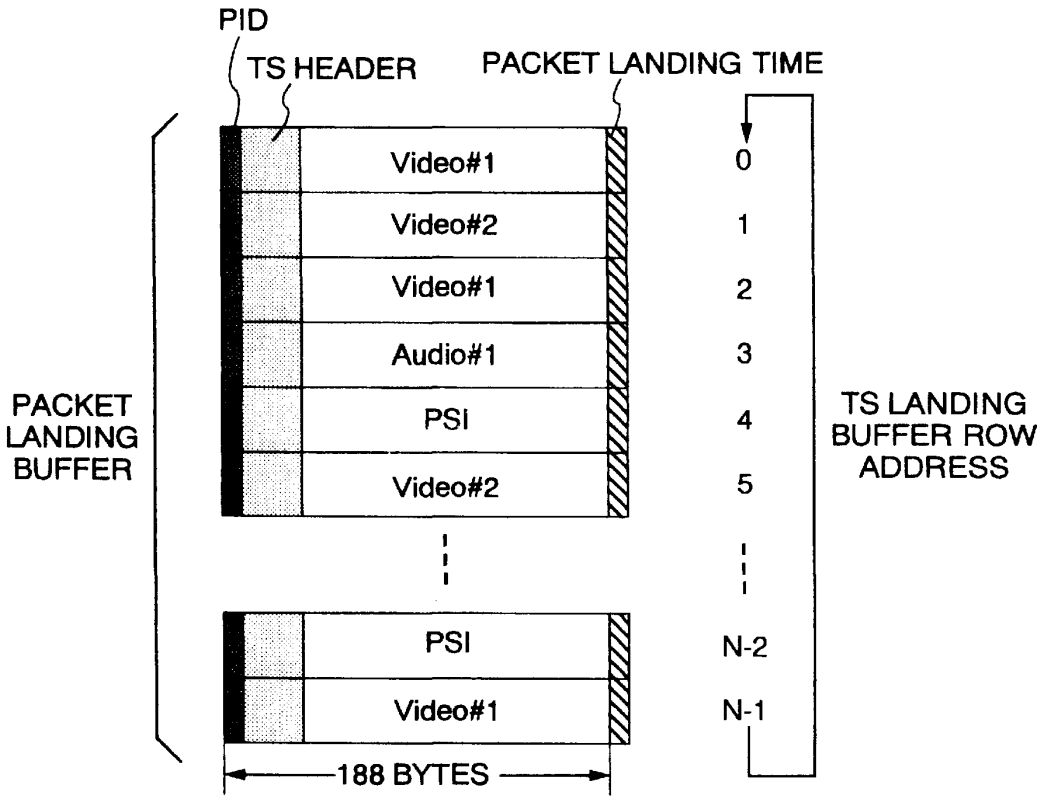


FIG.4



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