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Baker et al.

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[56]

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5,793,954

[45] Date of Patent:

Aug. 11, 1998

[54]	SYSTEM AND METHOD FOR GENERAL	5,062,055	10/1991	Chinnaswamy et al 364/551.01
	PURPOSE NETWORK ANALYSIS	5,210,530	5/1993	Kammerer et al 340/825.08
[75]	Inventors: Peter D. Baker, Aliso Viejo; Karen	5,442,639	8/1995	Crowder et al 371/20.1

[73] Assignee: NB Networks, Aliso Viejo, Calif.

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Assistant Examiner—Thomas Peeso

Attorney, Agent, or Firm-Lyon & Lyon LLP

[57] ABSTRACT

A network interface system and related methods. A single logic control module, which may be implemented in hardware or software, is utilized to perform any of a number of data manipulation functions including, for example, parsing, filtering, data generation or analysis, based upon one or more programmably configurable protocol descriptions which may be stored in and retrieved from an associated memory.

References Cited

Dec. 20, 1995

U.S. PATENT DOCUMENTS

[51] Int. Cl.⁶ H04L 12/28

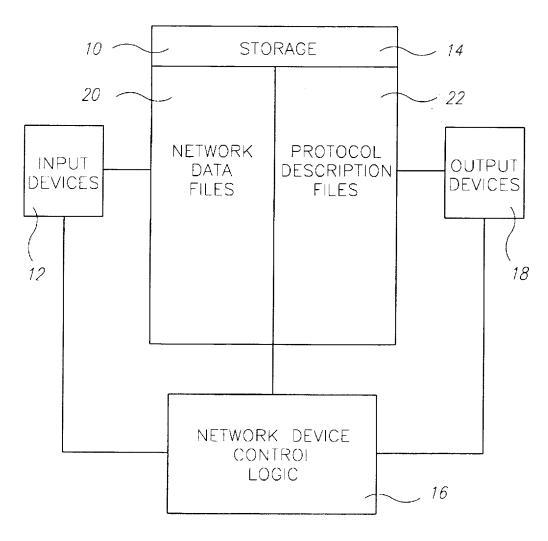
Neal, Los Angeles, both of Calif.

364/551.01; 340/825.06; 371/35, 48, 53,

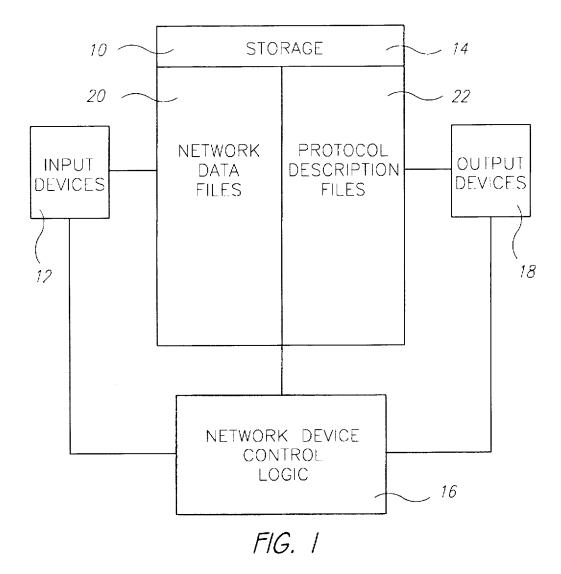
67.1, 68.2, 20.1, 3; 395/182.02, 182.19,

183.13, 183.15, 183.22, 185.01, 200.8

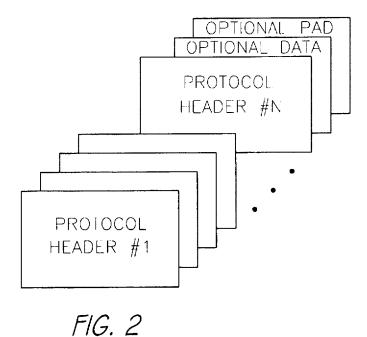
4 Claims, 20 Drawing Sheets











FIELD #1 SUBRECORD

PROTOCOL

CONTROL

RECORD



	Ethern	Ethernet Control Record	cord		
Protocol Name	NumBits	NumBits NumFields CurField	CurField	Fields	Options
Ethernet MAC Header	112	5	0	Figure 4a	[None]

4	
Fla.	

1000	Field Name	Bit	<u></u>	Left	Right	Check	Frame	Left Right Check Frame Header	Statistics	Lookup	Lookup Filter	Format
Y 200		Offset Ler	ath	Shift	Shift	Sum	ath Shift Shift sum Length Length	Length		Structure	Structure	
-	O Det Vendor Address		24	0	24 0 16	0	0	0	[None]	Fig 4b	Fig 4b Fig 10.ldx 0	hex
		1		1	1		(c	[Mono!	[Mone]	Fig 10 ldx 1	hox
	Dst Station Address	24	24	0	٩	0	5	>	(MOINE)	1	- VD. O	×)
,	Sec Vendor Addraga	48	24	0	16	0	0	0	[None]	Fig 4c	[None]	hex
7	מני לפונים ליינים	2				-	Í	•	147	(1)	TAIOOOI	,
~	Src Station Address	72	24	0	16	0	O	0	None	[NOI)e]	(alloy)	Y2
,		5	0,	46	46	C	c	<u> </u>	INON	Fig 4d	[None]	hex
4	Type	96	٥		2				151.51			

Fig. 4A

	Des	tination Vendo	Destination Vendor Address Lookup Structure	op Structure	
Protocol	Next Index	Minimum	Maximum	Mask	Translation
[None]	-	000000×0	000000×0	ALL	"Fast Routers, In
[None]		0×000001	OXFFFFFF	ALL	"Unknown"

F19. 4E

	Sc	ource Vendor A	Source Vendor Address Lookup Structure	Structure	
Protocol	Next Index	Minimum	Maximum	Mask	Translation
[None]	8	0×000000	000000×0	ALL	"Fast Routers, Inc."
[None]	3	0x000001	OXFFFFF	ALL	"Unknown"
(Super)					:

F19. 4C

		Translation		UNKNOWII		ئ تا		"Unknown"	
ıcture		Mask		ALL		ALL		ALL	
Ethernet Type Lookup Structure		Maximum		0x8887		0x8888		OXFFFF FFFFF	
Ethernet Tv	,	Minimum		0000×0		0x8888		0x8889	20000
		Next Index		v)	ď)	ער	כ
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