

United States Patent [19]

Schoenborn

Patent Number: [11]

5,242,536

Date of Patent:

Sep. 7, 1993

[34]	PROCESS	OPIC POLISILICON EICHING	Sci. Technol., A. 0734–2101/84/041537–13, Society. Controlled Film Formation		
[75] [73]	Inventor:	Philippe Schoenborn, San Jose, Calif.			
	Assignee:	LSI Logic Corporation, Milpitas, Calif.		cki and Kosic	
f0.13	A1 NT.		Calandia		0:-

[21] Appl. No.: 632,461

[22] Filed: Dec. 20, 1990

[51] Int. Cl.⁵ H01L 21/00 156/657; 156/662 [58] Field of Search 656/657, 662, 643, 646

ANICOTRODIC DOLVEIL TOON PROTING

[56]

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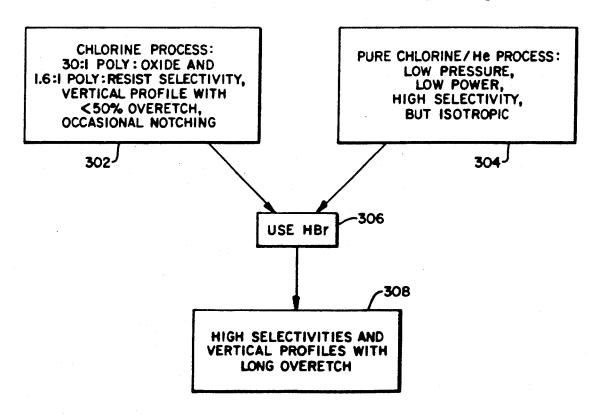
Selectivity and Feature Size Control, Dry Etching.

Primary Examiner—Thi Dang Attorney, Agent, or Firm-Gerald E. Linden; Michael D. Rostoker

[57] **ABSTRACT**

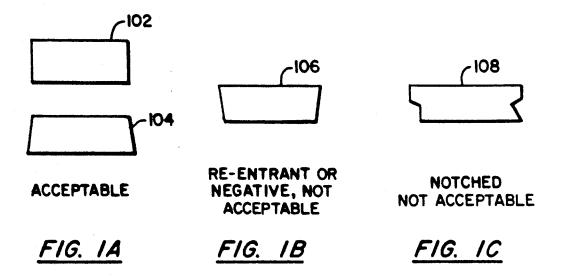
An anisotropic polysilicon etching process in Cl₂/HBr/He is disclosed. The use of HBr allows etching to occur under high poly:oxide selectivity conditions (e.g., above 40:1) that would otherwise produce lateral etching of the poly under the photoresist mask (isotropy). The selectivity of poly:resist is also increased (e.g., above 4:1). Poly sidewall passivation is enhanced without relying on resist redeposition. Gate oxide loss is also minimized, and anisotropy is realized with increased overetch (e.g., 60%). Exemplary process settings are: 1) 250 mTorr, 190 Watts, 0.5 cm gap, 100 sccm Cl₂, 50 sccm HBr and 40 sccm He; and 2) 270 mTorr, 200 Watts, 0.5 cm gap, 80 sccm Cl₂, 55 sccm HBr and 45

19 Claims, 16 Drawing Sheets





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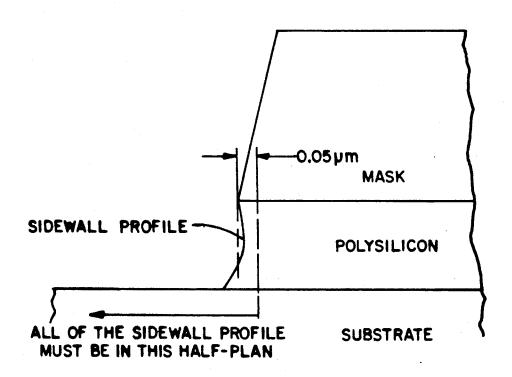
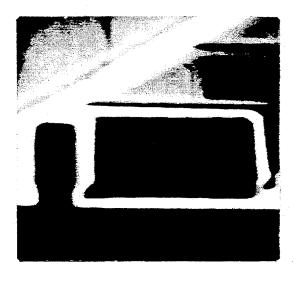


FIG. ID



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FIG. 2A

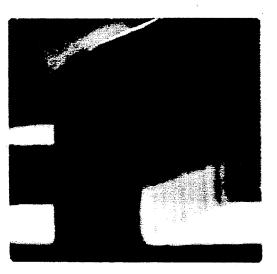
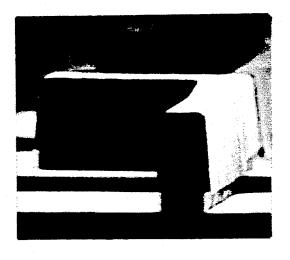


FIG. 2B



F1G. 4

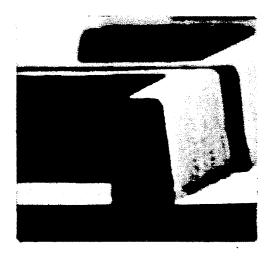
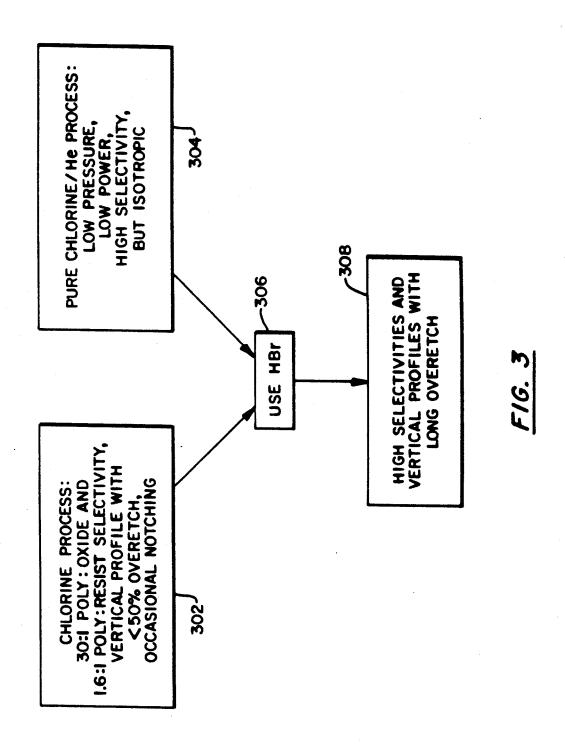
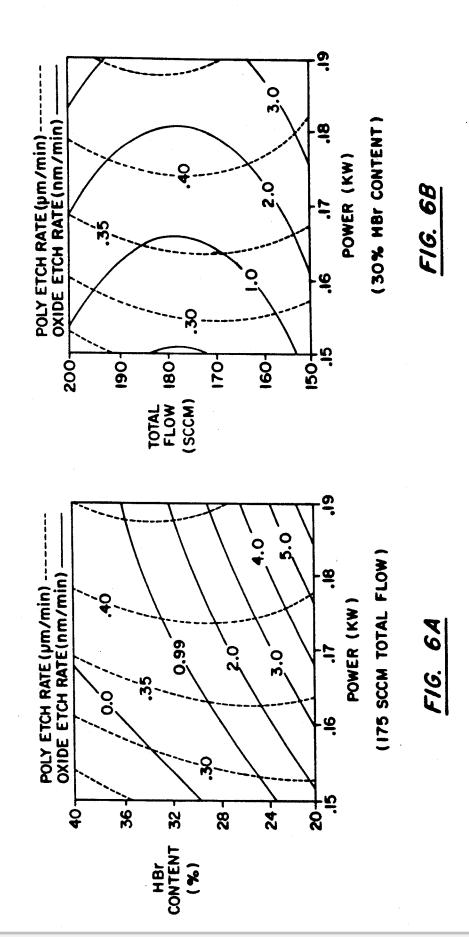


FIG. 5





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