IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

Apple Inc., Petitioner

v.

California Institute of Technology Patent Owner

> IPR2017-00210 U.S. Patent No. 7,116,710

PETITIONER'S UPDATED EXHIBIT LIST



Pursuant to the Board granting Petitioner's Motions to Submit
Supplemental Information on October 27, 2017, Petitioner hereby submits
the Supplemental Information authorized by the Board and this updated
Exhibit List. Exhibit 1027 in Petitioner's Motion is now numbered
Exhibit 1042. Exhibit 1028 in Petitioner's Motion is now numbered
Exhibit 1043.

Dated: October 30, 2017 /Michael Smith/

Michael Smith Registration No. 71,190



CERTIFICATE OF SERVICE

I hereby certify that on October 31, 2017, I caused a true and correct copy of the foregoing materials:

- Petitioner's Updated Exhibit List
- Exhibits 1029-1043

to be served via electronic mail on the following correspondents of record as listed in Patent Owners' Mandatory Notices:

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PETITIONER'S UPDATED EXHIBIT LIST

IPR2017-00210

Exhibit	Description
1001	U.S. Patent 7,116,710
1002	Replacement copy of Frey, B. J. and MacKay, D. J. C., "Irregular Turbocodes," <i>Proc. 37th Allerton Conf. on Comm., Control and Computing</i> , Monticello, Illinois, published on or before March 20, 2000
1003	Replacement copy of D. Divsalar, H. Jin, and R. J. McEliece, "Coding theorems for "turbo-like" codes," <i>Proc. 36th Allerton Conf. on Comm., Control and Computing</i> , Allerton, Illinois, pp. 201-10, March, 1999
1004	Luby, M. <i>et al.</i> , "Analysis of Low Density Codes and Improved Designs Using Irregular Graphs," <i>STOC</i> '98, pp. 249-59, published in 1998
1005	Pfister <i>et al.</i> , "The Serial Concatenation of Rate-1 Codes Through Uniform Random Interleavers" (Exhibit 1 to the Siegel Declaration)
1006	Declaration of Professor James Davis, Ph.D. ("Davis Declaration")
1007	Gallager, R., Low-Density Parity-Check Codes, Monograph, M.I.T. Press, 1963
1008	Berrou <i>et al.</i> , "Near Shannon Limit Error-Correcting Coding and Decoding: Turbo Codes," <i>ICC '93</i> , Technical Program, Conference Record 1064, Geneva 1993
1009	MacKay, D. J. C, and Neal, R. M. "Near Shannon Limit Performance of Low Density Parity Check Codes," <i>Electronics Letters</i> , vol. 32, pp. 1645-46, 1996
1010	Benedetto, S. et al., Serial Concatenation of Block and Convolutional Codes, 32.10 Electronics Letters 887-8, 1996
1011	Luby, M. et al., "Practical Loss-Resilient Codes," STOC '97, 1997



0.5. Tatent No. 7,110,710
Declaration of Robin Fradenburgh Concerning the "Proceedings, 36th Allerton Conference on Communications, Control, and Computing" Reference
Frey, B. J. and MacKay, D. J. C., "Irregular Turbo-Like Codes" presented at the 1999 Allerton Conference on Communications, Control, and Computing.
Prosecution History of the '710 Patent, Response Dated May 5, 2005
Table of Contents of <i>Proceedings of the 37th Allerton Conference</i> on Communication, Control and Computing from September 22-24, 1999
Joint Claim Construction Statement (Case No. 2:13-cv-07245, Dkt. No. 47)
Expert Report of Dr. Brendan Frey (Case No. 2:13-cv-07245)
Aamod Khandekar, "Graph-based Codes and Iterative Decoding," Thesis submitted June 10, 2002
Richardson, Shokrollahi, and Urbanke, "Design of Provably Good Low-Density Parity Check Codes"
Replacement copy of the Declaration of Paul H. Siegel
U.S. Provisional Application No. 60/205,095
Mitzenmacher, M. "Studying balanced allocations with differential equations," <i>Combinatorics, Probability & Computing</i> 8.5, pp. 473-482, September 1999
Barg <i>et al.</i> , "Linear-time Binary Codes Correcting Localized Erasures," <i>IEEE Transactions on Information Theory</i> 45.7, pp. 2545-2550, November 1999
Shokrollahi, "New Sequences of Linear Time Erasure Codes Approaching the Channel Capacity," <i>Applied Algebra, Algebraic</i> <i>Algorithms and Error-Correcting Codes</i> , pp. 65-76, 1999
Declaration Of Richard Goldenberg In Support Of Unopposed Motions To Submit Replacement Exhibits Pursuant To 37 C.F.R. § 42.104(c)



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