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Dated: February 9, 2009

Signature: 
(Jeffrey Salmon)

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Michael Tasler

Group No.: 2184

Serial No.: 11/928,283

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Filed: August 24, 2006

Examiner: C.K. Lee

For: ANALOG DATA GENERATING AND
PROCESSING DEVICE FOR USE WITH
A PERSONAL COMPUTER

Attorney
Docket No.: 31436/43995

PRELIMINARY AMENDMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

Please enter this preliminary amendment prior to the examination of the above-captioned application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.

AMENDMENTS TO THE CLAIMS

WE CLAIM:

Please cancel claim 1 and add new claims 2-68 as noted hereinafter:

1. (cancelled).
2. (new) An analog data generating and processing device (ADGPD) for use with a personal computer (PC) having a multi-purpose interface (MPI) and at least one software driver, the ADGPD comprising:

an i/o port designed to be operatively coupled to an MPI of a PC;

a program memory;

a data storage memory;

an ADGPD processor operatively coupled to the i/o port, the program memory and the data storage memory; and

wherein the ADGPD processor is adapted to be involved in a data generation process by which at least one file of digitized analog data is stored in the data storage memory and to be involved in an automatic recognition process in which, after the i/o port has been operatively coupled to the MPI of the PC, the ADGPD processor executes at least one instruction set stored in the program memory and thereby causes at least one parameter regarding the ADGPD's ability to transfer files of digital data in response to commands issued from the at least one software driver to be automatically sent through the i/o port and to the MPI of the PC without any type of user intervention at any time by means of the PC.

3. (new) The ADGPD of claim 2, wherein the at least one parameter is consistent

with the ADGPD being a mass storage device.

4. (new) The ADGPD of claim 3,
wherein the ADGPD processor and the program memory are configured to cause,
after the at least one parameter has been sent to the i/o port, file allocation table information to be
sent to the i/o port,

wherein the ADGPD processor and the program memory are configured to cause
a virtual boot sequence to be sent to the i/o port which includes at least information that is
representative of a number of sectors of a storage disk, and

wherein the file allocation table information includes at least a start location of a
file allocation table.

5. (new) The ADGPD of claim 4, wherein the at least one parameter is consistent
with the ADGPD being adapted to operate in a manner consistent with a hard disk drive.

6. (new) The ADGPD of claim 2, wherein the ADGPD processor is formed in a
single chip.

7. (new) The ADGPD of claim 2, wherein the ADGPD processor includes a single
central processing unit.

8. (new) The ADGPD of claim 2, wherein the ADGPD processor comprises a single
microprocessor.

9. (new) The ADGPD of claim 2, wherein the ADGPD processor comprises a single
digital signal processor.

10. (new) The ADGPD of claim 2, wherein the program memory is formed in a

single chip.

11. (new) The ADGPD of claim 2, wherein the program memory comprises a single memory device.

12. (new) The ADGPD of claim 2, wherein the data storage memory comprises a single memory device.

13. (new) The ADGPD of claim 2, wherein the at least one parameter is consistent with the ADGPD being responsive to a SCSI command set.

14. (new) The ADGPD of claim 2, wherein the at least one parameter is not consistent with the true nature of the ADGPD.

15. (new) The ADGPD of claim 2, wherein the at least one parameter does not indicate that the ADGPD processor is involved in the data generation process.

16. (new) The ADGPD of claim 2, wherein the at least one parameter is consistent with the ADGPD being an input/output device customary in a host device.

17. (new) The ADGPD of claim 16, wherein the at least one parameter is consistent with the ADGPD being a hard disk drive.

18. (new) The ADGPD of claim 2, wherein the ADGPD comprises at least a portion of a medical device.

19. (new) The ADGPD of claim 2, wherein the ADGPD comprises at least a portion of a data acquisition system.

20. (new) The ADGPD of claim 2, wherein the i/o port comprises a parallel port.

21. (new) The ADGPD of claim 2, wherein the i/o port comprises a SCSI connector.

22. (new) The ADGPD of claim 2, wherein the i/o port is designed to be operatively coupled to a cable.

23. (new) The ADGPD of claim 2, wherein the data storage memory comprises a semiconductor based memory.

24. (new) The ADGPD of claim 2, wherein the ADGPD includes a flexible interface.

25. (new) The ADGPD of claim 2, wherein the ADGPD includes a universal interface.

26. (new) The ADGPD of claim 2, wherein the ADGPD includes a stand-alone interface device.

27. (new) The ADGPD of claim 2, wherein the ADGPD processor and the program memory are configured to be involved with the automatic recognition process as a response to a SCSI inquiry command.

28. (new) The ADGPD of claim 2, wherein the ADGPD processor is adapted to execute the at least one instruction set to thereby directly cause the at least one parameter regarding the ADGPD to be automatically sent to the PC.

29. (new) A combination comprising the ADGPD of claim 2 and a PC.

30. (new) An analog data generating and processing device (ADGPD) for use with a personal computer (PC) having a multi-purpose interface (MPI) and at least one software driver, the ADGPD comprising:

an i/o port designed to be operatively coupled to an MPI of a PC;

a program memory;

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