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UNITED STATE	S PATENT AND	TRADEMARK	COFFICE
BEFORE THE P	ATENT TRIAL	AND APPEAL	BOARD

# PETITION FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 8,504,746

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## **TABLE OF CONTENTS**

I.		Mandatory notices (37 C.F.R. § 42.8(a)(1))	2
II.		Grounds for standing (37 C.F.R. § 42.104(a))	∠
III.		Identification of challenge (37 C.F.R. § 42.104(b)).	∠
	A.	Citation of prior art.	∠
	B.	Statutory grounds for the challenge.	6
IV.		The '746 patent	7
	A.	Overview	7
	B.	The challenged claims of the '746 patent are not entitled to priority benef of the March 1997 German application.	
	C.	Level of ordinary skill in the art.	.15
	D.	Claim construction.	.16
V.		Ground 1: The combination of Ard, Schmidt, and Webb renders claims 1 6–8, 14, 20, 21, 30, and 34 obvious.	
	A.	Overview of Ard.	.18
	B. Overview of Schmidt.		.21
	C.	Overview of Webb.	.23
	D.	The combination renders claim 1 obvious	.24
		1. The combination discloses the "analog data acquisition device" recit in the preamble of claim 1 [1P]	
		a) "analog data acquisition device"2	25
		b) Computer architecture/operation component2	6
		2. The combination discloses the analog data acquisition device architectural limitations	0
		a) "program memory" [1A]3	0
		b) "an analog signal acquisition channel for receiving a signal from a analog source" [1B]	
		c) The "processor" limitation [1C]	3
		3. The combination discloses the "data generation process" limitations [1D]	



# Petition for Inter Partes Review of U.S. Patent No. 8,504,746

		a) "data generation process" [1D.1]37
		b) "analog data is processed and digitized" [1D.2]38
		c) "file system" [1D.3]
	4.	The combination discloses claim limitation [1E]39
		a) "parameter indicative of the class of devices" [1E.2]39
		b) "the processor executes at least one instruction set" [1E.1]43
		c) "not within the class of devices" [1E.3]
	5.	The combination discloses claim element [1F]44
		a) file transfer process [1F.2]
		b) "at least one other instruction set" [1F.1]46
		c) appearance of the device as part of the class of devices [1F.3]46
	6.	The combination discloses claim limitation [1G]47
E.	Th	e combination renders claim 6 obvious49
F.	Th	e combination renders claim 7 obvious49
G.	Th	e combination renders claim 8 obvious50
H.	Th	e combination renders claim 14 obvious5
I.	Th	e combination renders claim 20 obvious
J.	Th	e combination renders claim 21 obvious55
K.	Th	e combination renders claim 30 obvious50
L.	Th	e combination renders independent claim 34 obvious58
	1.	The combination discloses "[a] method for analog data acquisition and interfacing to a host device wherein the host device includes a device driver" as recited in the preamble of claim 34 [34P]59
	2.	The combination discloses the "interfacing" step of claim 34 [34A].59
	3.	The combination discloses the "acquiring" step of claim 34 [34B]60
	4.	The combination discloses the "sending" step of claim 34 [34C]61
	5.	The combination discloses the "transferring" step of claim 34 [34D].62
		ound 2: The combination of Ard, Schmidt, Webb, and Araghi renders ims 4 and 11 obvious64
Α.		e combination renders claim 4 obvious6



VI.

# Petition for Inter Partes Review of U.S. Patent No. 8,504,746

В.	The combination renders claim 11 obvious	.65
VII.	Ground 3: The combination of Ard, Schmidt, Webb, and Steinle renders claims 10 and 35 obvious.	.67
A.	The combination renders claim 10 obvious	.67
B.	The combination renders claim 35 obvious.	.70
VIII. Ground 4: The combination of Ard, Schmidt, Webb, and Reisch claim 23 obvious.		.72
IX.	Conclusion.	.73



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In re Papst Licensing Digital Camera Patent Litigation,	
778 F.3d 1255 (Fed. Cir. 2015)	16
Martin v. Mayer,	
823 F.2d 500 (Fed. Cir. 1987)	10
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415 F.3d 1303 (Fed. Cir. 2005)	18
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35 U.S.C. § 102(e)	5, 6



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