[11] **4,430,673** 

Salomon et al. [45] Feb. 7, 1984

| [24] | FRUGRAMMABLE SCAN/READ       |
|------|------------------------------|
|      | CIRCUITRY FOR CHARGE COUPLED |
|      | DEVICE IMAGING DETECTORS     |
|      |                              |

[75] Inventors: Phil M. Salomon, Tujunga; Kalman

Smilowitz, Playa del Rey, both of

Calif.

[73] Assignee: The United States of America as

represented by the Administrator of the National Aeronautics and Space Administration, Washington, D.C.

[21] Appl. No.: 276,749

[22] Filed: Jun. 24, 1981

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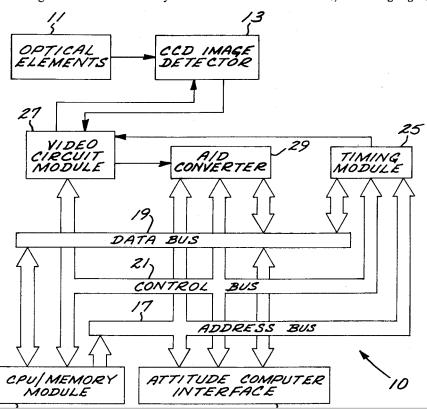
Trackers", Salomon and Glavich, SPIE, vol. 252 (1981), p. 64, originally presented Jul. 28, 1980. "Charge-Coupled Device Trackers For High Accuracy Guidance Applications", Salomon, Optical Engineering, Jan./Feb. 1981, vol. 20, No. 1, p. 135.

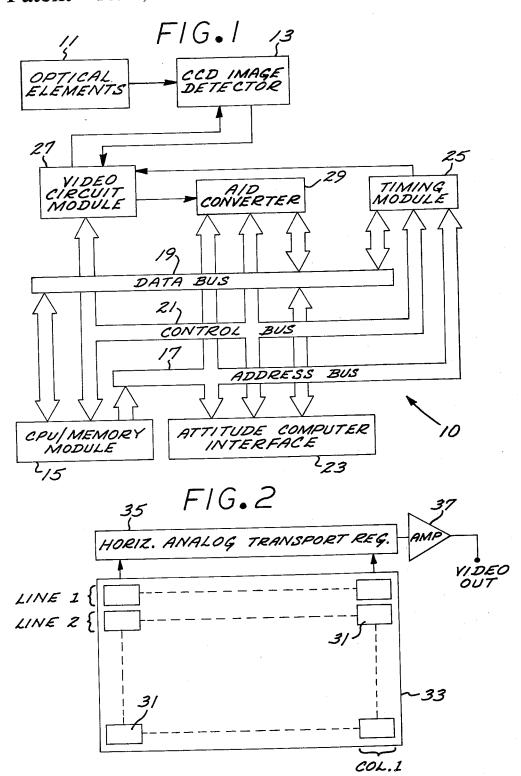
Primary Examiner—Michael A. Masinick Attorney, Agent, or Firm—Paul F. McCaul; Thomas H. Jones; John R. Manning

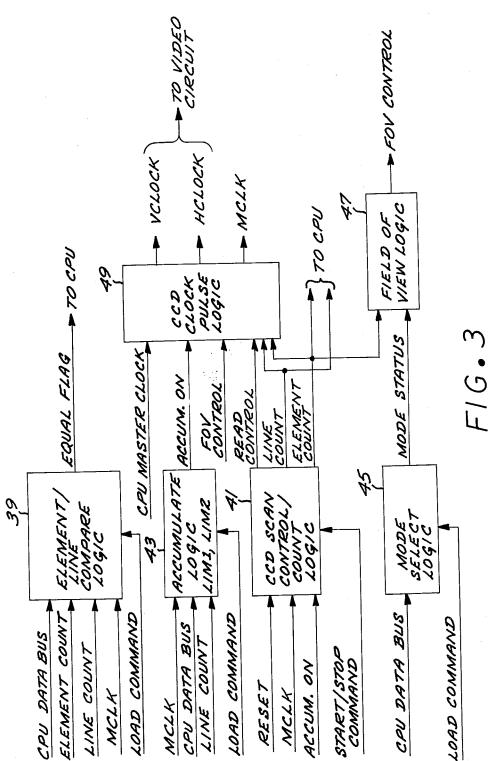
### [57] ABSTRACT

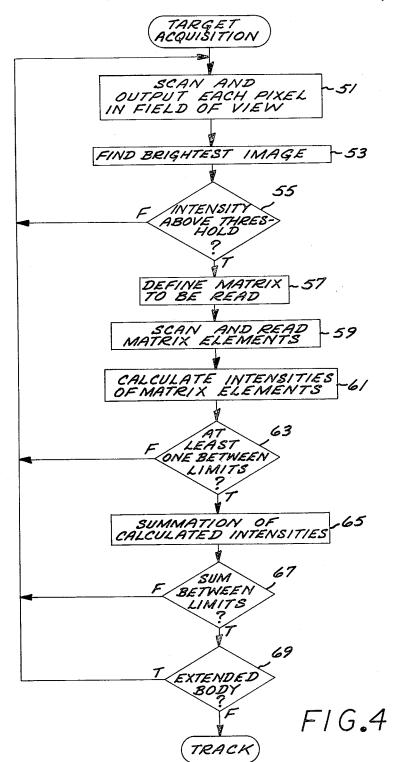
A circuit for scanning and outputting the induced charges in a solid state charge coupled device (CCD) image detector (13) is disclosed in an image detection system (10) for use in a spacecraft attitude control system. The image detection system includes timing control circuitry (25) for selectively controlling the output of the CCD detector (13) so that video outputs are provided only with respect to induced charges corresponding to predetermined sensing element lines of the CCD detector (13). The system also include an analog to digital converter (29) for converting selected video outputs from the CCD detector (13). The timing control circuit (25) and the analog to digital converter (29) are controlled by a programmed microprocessor (15) which defines the video outputs to be converted and further controls the timing control circuit (25) so that no video outputs are provided during the delay associated with analog to digital conversion.

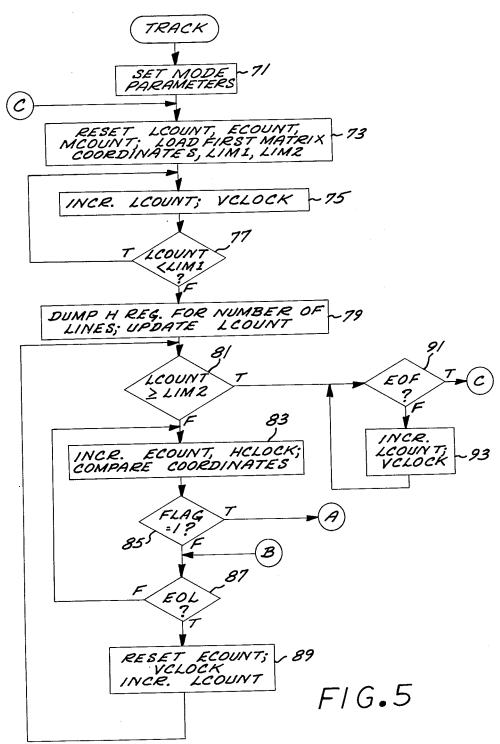
#### 19 Claims, 6 Drawing Figures











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