UNITED STATES PATENT AND TRADEMARK OFFIC	Έ
BEFORE THE PATENT TRIAL AND APPEAL BOARD	)

DECLARATION OF EREZ ZADOK, PH.D. IN SUPPORT OF PETITION FOR INTER PARTES REVIEW OF U.S. PATENT 9,189,437

> Apple 1003 IPR2016-01844



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	My Lee Ba A. B. C. Cl. Gr. 5, A. B. C.	My un My un Level of Backgr A. Dev B. Har C. Ope Claim Ground 5, 6, 9- A. Ove B. Ove C. Ove D. The	<ul> <li>5, 6, 9–12, 14–16, 30, 34, and 43 obvious.</li> <li>A. Overview of Ard.</li> <li>B. Overview of Schmidt.</li> <li>C. Overview of Salomon.</li> <li>D. The combination of Ard, Salomon, and Schmidt renders independent claim 1 obvious.</li> <li>1. The combination of Ard, Salomon, and Schmidt discloses the preamble: "an analog data generating and processing device (ADGPD)."</li> <li>2. The combination of Ard, Salomon, and Schmidt discloses the ADGPD architecture elements.</li> <li>a) The combination of Ard, Salomon, and Schmidt discloses "an input/output (i/o) port."</li> <li>b) The combination of Ard, Salomon, and Schmidt discloses "a program memory."</li> <li>c) The combination of Ard, Salomon, and Schmidt discloses "a destorage memory."</li> <li>d) The combination of Ard, Salomon, and Schmidt discloses "a</li> </ul>



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IX.

X.

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