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Johnson et al.

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[54] SINGLE-CHIP SOFTWARE CONFIGURABLE TRANSCEIVER FOR ASYMMETRIC COMMUNICATION SYSTEM

[75] Inventors: Terence L. Johnson; Peter R. Molnar; Howard E. Levin; Jeffrey P. Gleason; Robin Wiprud; Sujit Sudhaman, all of Austin; Jody Everett, Buda; Michael R. May, Austin; Carlos A. Greaves, Austin; Mathew A. Rybicki, Austin; Mathew A. Pendleton, Cedar Park; John M. Porter, Round Rock, all of Tex.

[73] Assignee: Motorola, Inc., Schaumburg, Ill.

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[51] Int. Cl.⁶ H04B 1/38; H04B 1/56; H04L 5/14; H04L 5/16

[52] U.S. Cl. 375/220; 375/220; 375/219; 375/221; 375/222; 370/276; 370/295; 370/464; 370/480; 455/73

[58] Field of Search 375/220, 219, 375/221, 222, 223; 370/276, 295, 296, 464, 480, 389, 395, 403, 404, 402, 400, 401; 455/73

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Primary Examiner—Stephen Chin

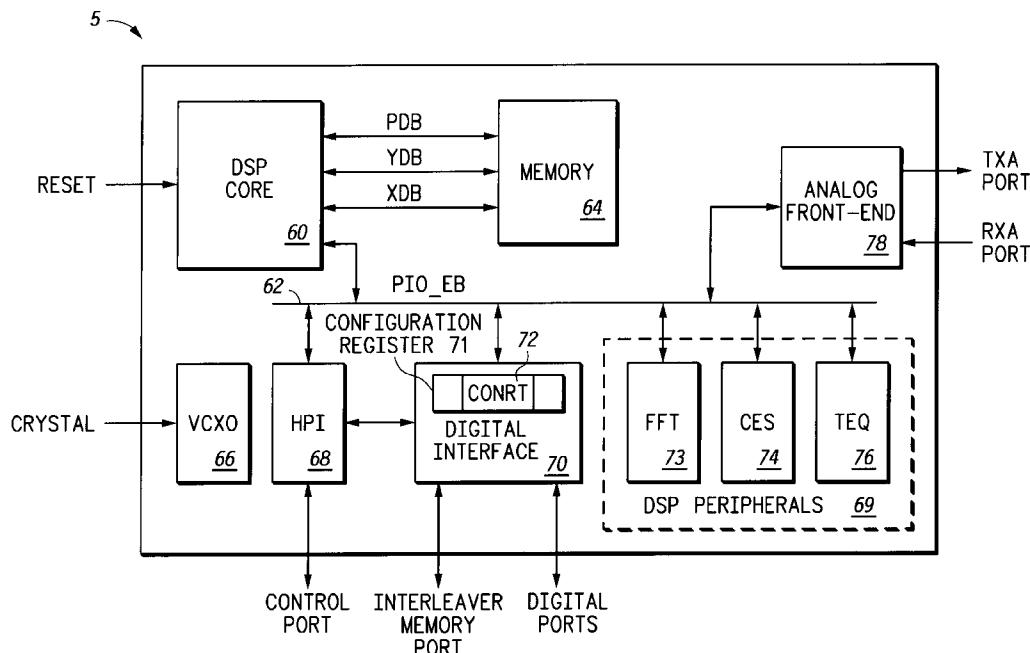
Assistant Examiner—Michael W. Maddox

Attorney, Agent, or Firm—Paul J. Polansky

[57] ABSTRACT

A transceiver (5) for an asymmetric communication system such as asymmetric digital subscriber line (ADSL) includes a configuration register (71) defining operation at either a central office (CO) or a remote terminal (RT). The configuration register (71) includes a control bit (72) for selecting either CO or RT mode. The transceiver (5) includes a signal processing module (70) configured according to the state of the control bit (72). For example, a digital interface (70) converts transmit data into transmit symbols and converts received symbols into receive data. The digital interface (70) uses a large memory (158) as a buffer in the transmit path and a small memory (160) as a buffer in the receive path in CO mode. In RT mode, the digital interface (70) uses the small memory (160) in the transmit path and the large memory (158) in the receive path. The selective configuration allows a single integrated circuit to be used in both CO and RT equipment.

40 Claims, 9 Drawing Sheets



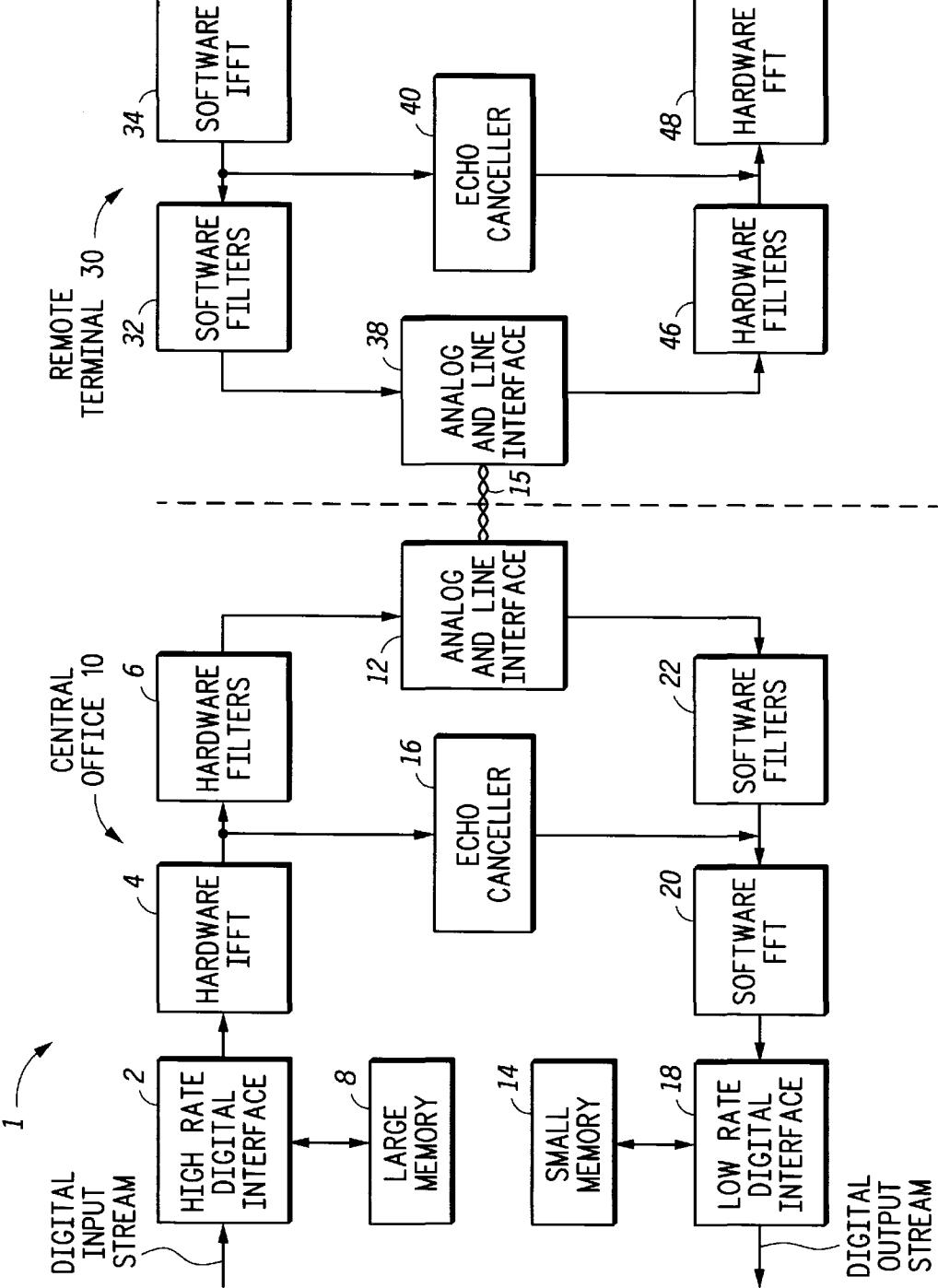


FIG. 1

FIG.2

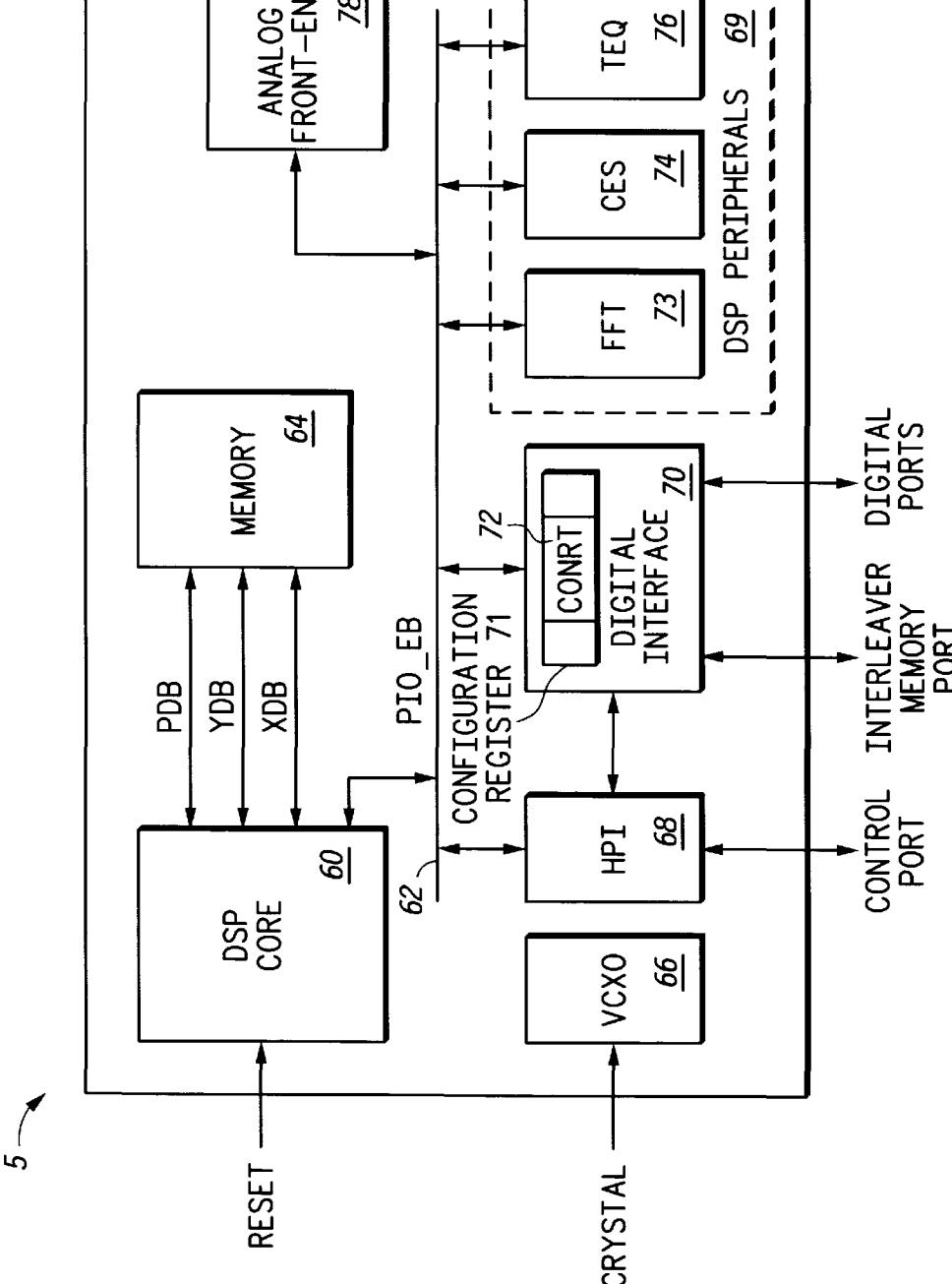


FIG.3

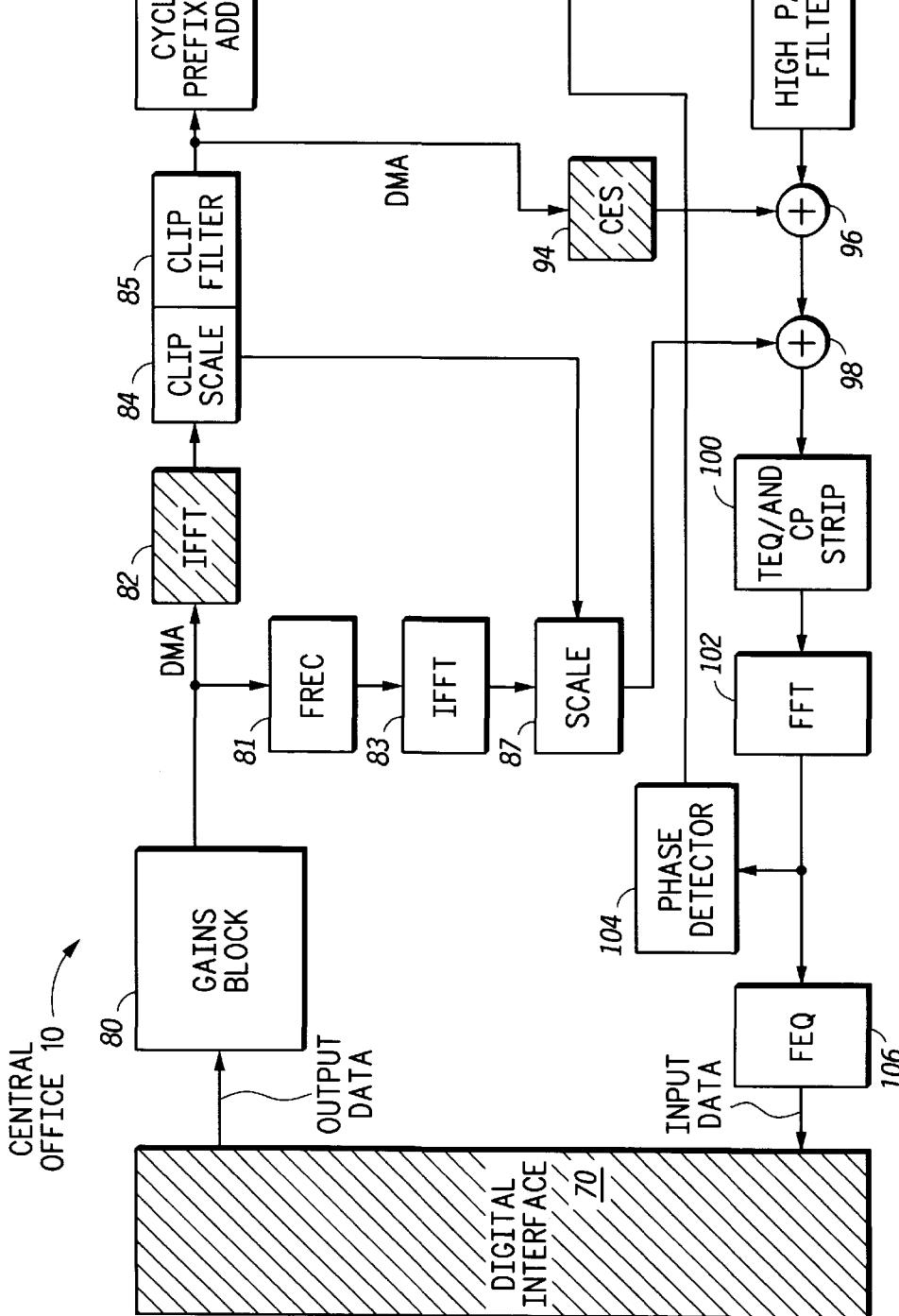
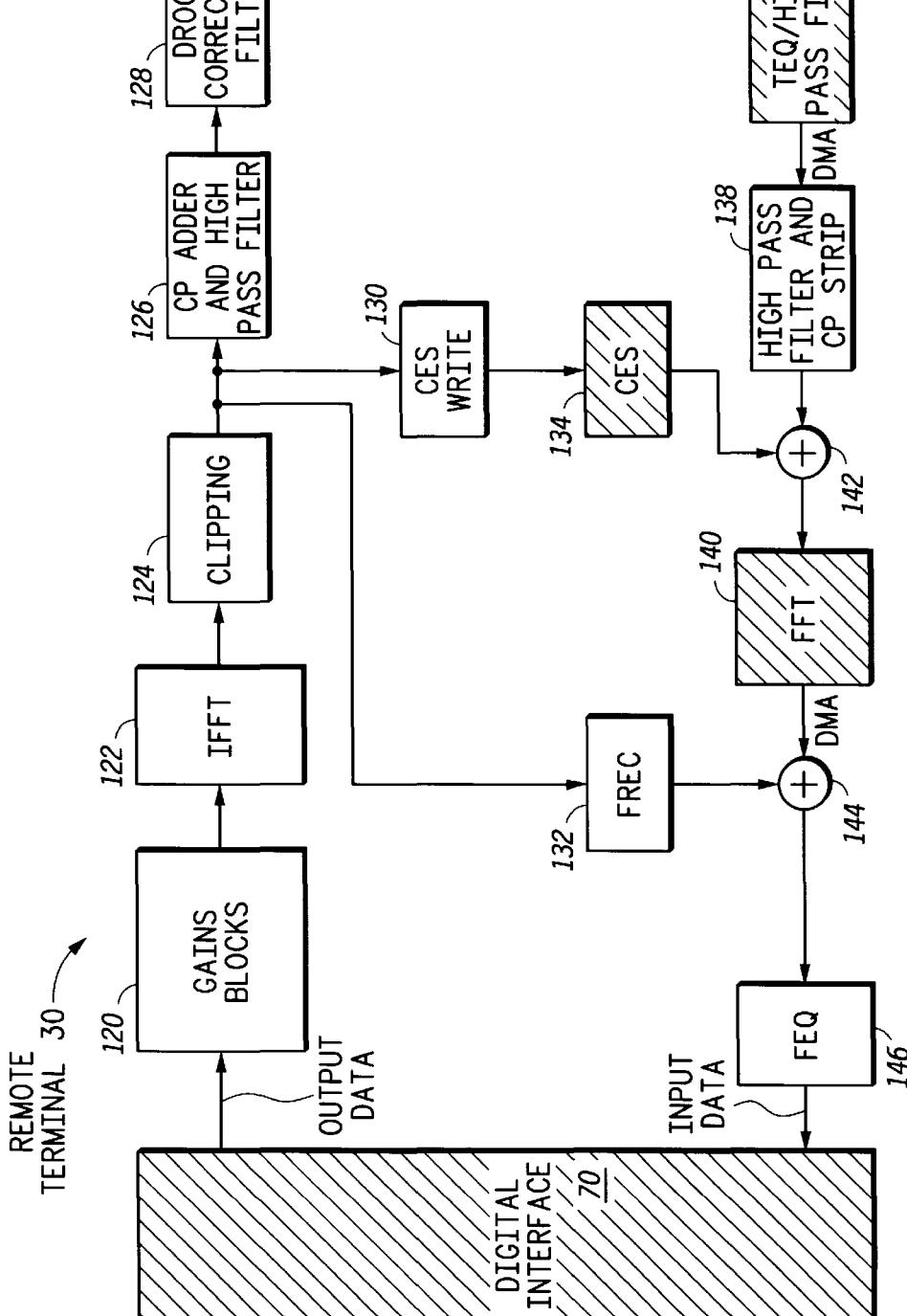


FIG. 4



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