

Exhibit C

**PROPOSED CONSTRUCTIONS AND IDENTIFICATION OF INTRINSIC AND EXTRINSIC EVIDENCE
PATENT NO. 8,880,862**

Term	Realtime’s Proposed Construction	Realtime’s Intrinsic and Extrinsic Evidence	Apple’s Proposed Construction	Apple’s Proposed Construction
“boot data” (cls. 1-6, 8, 9, 11, 13-15, 17, 19, 23, 24, 29, 31-33, 35, 36, 39, 43-45, 47-50, 52, 53, 71, 72, 77, 79-81, 83, 84, 89, 91-93, 97, 98, 107)	“data related to the boot process”	<i>Intrinsic Evidence</i> ’608 patent at Abs., Figs. 7a-8b, 3:41-63, 4:4-23, 9:21-27, 11:25-35, 17:40-42, 21:38-40, 21:53-62, 22:14-19, 22:26-45, 22:51-23:10, 27:43-64, 28:1-25, 28:49-53, 28:57-30:39 ’936 patent at Abs., Figs. 7a-8b, 3:31-60, 4:1-17, 9:6-12, 10:67-11:8, 17:16-18, 21:8-10, 21:23-31, 21:50-55, 21:62-22:13, 22:18-43, 27:6-33, 27:37-44, 27:51-28:46 ’862 patent at Abs., Figs. 7a-8b, 3:35-63, 4:4-20, 9:9-14, 11:7-15, 16:60-62, 20:43-45, 20:58-66, 21:18-23, 21:30-48, 21:53-22:11, 26:38-27:63, 28:1-46, 28:50-54, 28:58-29:19, 29:26-29, 29:33-48, 29:53-64, 30:1-28, 30:32-56, 31:30-41, 31:47-32:11, 32:18-34:26	“data on the storage device identified by the boot data list or otherwise loaded during the boot process”	<i>Intrinsic Evidence</i> ’608: Abstract 27; 21:31-27:61-64 Provisional Patent Application 60/180,111 <i>Extrinsic Evidence</i> ARM Architecture Manual A (2000) IA-32 Intel Architecture Software Developer’s Manual - Volume 3: Programming Model (2000) The IEEE Standard Glossary of Electric and Electronic Terms 103 The IEEE Standard Glossary of Electric and Electronic Terms 113

¹ Where one asserted patent is cited, Apple reserves the right to rely on similar disclosures in the other two asserted patents.

		<p>'608 File History at RTDAPP0000516-517, 622-623</p> <p>'936 File History at RTDAPP0002068-069</p> <p>Incorporated U.S. Pat. Appl. No. 60/180,114 at 8-9, 13, 50, 52, 57-58, 73</p> <p>Incorporated 09/775,897 published as U.S. Pub. No. 2001/ 0047473 at ¶ 13</p> <p><i>Extrinsic Evidence</i></p> <p>Andrew N. Sloss, Dominic Symes, & Chris Wright, <i>ARM System Developer's Guide: Designing and Optimizing System Software</i> 13 (2004)</p>		<p>Tom Shan</p> <p><i>Pentium® System Architecture</i> (December 1999)</p> <p>Apple may testify Wills, who field of co The substa testimony technology including the art. Dr testify reg of ordinary would unc term. Dr provide ex rebut argu Realtime r constructio</p>
<p>"boot data list" / "list of boot data" / "boot list" (cls. 1-6, 8, 9, 11, 13, 14, 19, 97, 98, 107)</p>	<p>No construction necessary beyond the construction for "boot data." To the extent necessary, the term should be construed to have its plain and ordinary meaning in view of the construction of "boot data."</p>	<p><i>Intrinsic Evidence</i></p> <p>'608 patent at Abs., Figs. 7a-8b, 3:41-63, 4:4-23, 9:21-27, 11:25-35, 17:40-42, 21:38-40, 21:53-62, 22:14-19, 22:26-45, 22:51-23:10, 27:43-64, 28:1-25, 28:49-53, 28:57-30:39; claims 1, 4, 7, 8, 22</p> <p>'936 patent at Abs., Figs. 7a-8b, 3:31-60, 4:1-17, 9:6-12,</p>	<p>"the list used by the storage controller to pre-load boot data into its cache memory"</p>	<p><i>Intrinsic E</i></p> <p>'608: Abst 23:10 Prov No. 60/18</p> <p><i>Extrinsic I</i></p> <p><i>ARM Arch Manual A</i> (2000)</p> <p><i>IA-32 Inte Software L</i></p>

		<p>10:67-11:8, 17:16-18, 21:8-10, 21:23-31, 21:50-55, 21:62-22:13, 22:18-43, 27:6-33, 27:37-44, 27:51-28:46; claims 1, 6, 11, 18, 20</p> <p>'862 patent at Abs., Figs. 7a-8b, 3:35-63, 4:4-20, 9:9-14, 11:7-15, 16:60-62, 20:43-45, 20:58-66, 21:18-23, 21:30-48, 21:53-22:11, 26:38-27:63, 28:1-46, 28:50-54, 28:58-29:19, 29:26-29, 29:33-48, 29:53-64, 30:1-28, 30:32-56, 31:30-41, 31:47-32:11, 32:18-34:26; claims 1-9, 11-14, 19, 21, 95, 96-106, 107, 108</p> <p>'608 File History at RTDAPP0000516-517, 622-623</p> <p>'936 File History at RTDAPP0002068-069</p> <p>Incorporated U.S. Pat. Appl. No. 60/180,114 at 8-9, 13, 50, 52, 57-58, 73</p> <p>Incorporated 09/775,897 published as U.S. Pub. No. 2001/ 0047473 at ¶ 13</p> <p><i>Extrinsic Evidence</i></p>		<p>- Volume 3 Programm (2000) <i>The IEEE of Electric Terms 103</i> <i>The IEEE of Electric Terms 113</i> Tom Shan <i>Pentium® System Ar (Decembe</i> Apple may testimony Wills, who field of co The substa testimony technology including the art. Dr. testify reg of ordinary would unc term. Dr. provide ex rebut argu Realtime r constructio</p>
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		Andrew N. Sloss, Dominic Symes, & Chris Wright, <i>Arm System Developer's Guide: Designing and Optimizing System Software</i> 13 (2004)		
“encode” / “encoder” / “encoding” (cls. 10, 32-33, 44-45, 49, 80-81, 92-93)	“hardware or software, or a combination thereof, that converts information from one format to another” / “converting information from one format to another using hardware, software, or a combination thereof”	<p><i>Intrinsic Evidence</i></p> <p>'608 patent at Figs. 1-5, Figs. 9-10, 1:56-58, 3:14-17, 24:52-25:50, 25:53-26:12, 26:19-34, 26:60-27:11, 28:64-29:14, 29:33-30:3, 30:27-39</p> <p>'936 patent at Figs. 1-5, Figs. 9-10, 1:58-60, 3:12-16, 24:15-25:11, 25:14-40, 25:47-62, 26:22-40, 27:39-49, 28:16-28, 28:52-62</p> <p>'862 patent at Figs. 1-5, Figs. 9-10, 1:61-63, 3:16-19, 23:50-24:46, 24:49-25:8, 25:15-30, 25:56-26:7, 27:25-34, 27:64-67, 28:16-19, 29:11-15, 29:27-46, 30:12-17, 30:23-25, 30:46-56, 31:21-29, 31:59-65, 32:29-34, 34:1-3</p> <p>Incorporated U.S. Pat. Appl. No. 60/180,114 at 2, 8-9, 11-12</p>	“hardware or software on the data storage controller that compresses data by converting the contents of a data block into a coded representation of those contents”	<p><i>Intrinsic Evidence</i></p> <p>'608: 25:3</p> <p>'862: 24:1</p> <p>'936: 24:3</p> <p>Provisional Patent Application No. 60/180114</p> <p><i>Extrinsic Evidence</i></p> <p>Previous court orders in <i>Intel v. Actian Corp.</i>, RWS-JDL (2016); <i>Re Oracle Am.</i>, RWS-JDL (2016); <i>Re MetroPCS</i>, No. 6:10-cv-00001 (E.D. Tex. 2010); <i>Re Realtime Data v. Stanley, C</i>, 6696-KBF (2012); <i>Re</i></p>

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