

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

APPLE, INC.,
Petitioner,

v.

REALTIME DATA LLC,
Patent Owner.

Case IPR2016-01737
Patent No. 8,880,862

**PETITIONER'S
MOTION FOR *PRO HAC VICE* ADMISSION
UNDER 37 C.F.R. § 42.10(c)**

EXHIBITS

APPLE-1001	U.S. Patent No. 8,880,862 to Fallon, et al. (“the ’862 Patent”)
APPLE-1002	Excerpts from the Prosecution History of the ’862 Patent (“the Prosecution History”)
APPLE-1003	Declaration of Dr. Charlie Neuhauser (“Dec.”)
APPLE-1004	Curriculum Vitae of Dr. Charlie Neuhauser
APPLE-1005	U.S. Patent No. 5,860,083 (“Sukegawa”)
APPLE-1006	U.S. Patent No. 6,374,353 (“Settsu”)
APPLE-1007	Burrows et al., “On-line Data Compression in a Log-structured File System” (1992) (“Burrows”)
APPLE-1008	U.S. Patent No. 6,145,069 (“Dye”)
APPLE-1009	U.S. Patent No. 7,190,284 (“Dye’284”)
APPLE-1010	U.S. Patent No. 6,317,818 (“Zwiegincew”)
APPLE-1011	Jeff Prosis, <u>DOS 6 – The Ultimate Software Bundle?</u> , PC MAGAZINE, Apr. 13, 1993 (“Prosis”)
APPLE-1012	Excerpts from John L. Hennessey & David A. Patterson, <u>Computer Architecture a Quantitative Approach</u> (1st ed. 1990) (“Hennessey”)
APPLE-1013	(RESERVED)
APPLE-1014	File, <u>Microsoft Press Computer Dictionary</u> (3d ed. 1997)

- APPLE-1015 Excerpts from Tom Shanley & Don Anderson, PCI System Architecture, (4th ed. 1999) (“Shanley”)
- APPLE-1016 Jacob Ziv & Abraham Lempel, A Universal Algorithm for Sequential Data Compression, IT-23 No. 3 IEEE TRANSACTIONS ON INFORMATION THEORY 337 (1977)(“Ziv”)
- APPLE-1017 James A. Storer & Thomas G. Szymanski, Data Compression via Textual Substitution, 19 No. 4 JOURNAL OF THE ASSOCIATION FOR COMPUTING MACHINERY (1982)(“Storer”)
- APPLE-1018 Program File, Microsoft Press Computer Dictionary (3d ed. 1997)
- APPLE-1019 Direct Memory Access, Microsoft Press Computer Dictionary (3d ed. 1997)
- APPLE-1020 RAM and RAM Cache, Microsoft Press Computer Dictionary (3d ed. 1997)
- APPLE-1021 Decoder, Microsoft Press Computer Dictionary (3d ed. 1997)
- APPLE-1022 (RESERVED)
- APPLE-1023 Excerpts from Kyle Loudon, Mastering Algorithms with C (1999) (“Loudon”)
- APPLE-1024 Excerpts from Michael Barr, Programming Embedded Systems in C and C++ (1999)(“Barr”)
- APPLE-1025 Excerpts from Eric Pearce, Windows NT in a Nutshell (1999)(“Pearce”)
- APPLE-1026 Excerpts from Tim O’Reilly, Troy Mott, and Walter Glenn, Windows 98 in a Nutshell (1999)(“O’Reilly”)
- APPLE-1027 Cache, Microsoft Press Computer Dictionary (3d ed. 1997)

APPLE-1028 Declaration of Michael Bittner in support of Petitioner's
Motion for *Pro Hac Vice* Admission

Pursuant to 37 C.F.R. § 42.10(c), Petitioner respectfully requests that the Board recognize Michael Bittner as counsel *pro hac vice* in this proceeding. Petitioner seeks the counsel of Michael Bittner due to his experience in representing Petitioner in other patent-related matters and particularly due to his familiarity with the substantive and technical issues involved in this proceeding. This motion is authorized by the Notice of Filing Date Accorded to Petition and Time for Filing Patent Owner Preliminary Response that was mailed on September 16, 2016 (Paper No. 3).

Statement of Facts

Mr. Bittner is a patent litigation attorney with nine years of experience representing clients in cases involving computer software. Mr. Bittner regularly litigates cases before the Eastern District of Texas and the Northern District of Texas. Through his practice in such cases, Mr. Bittner has gained substantial experience in jury trials, discovery, *Markman* hearings, and appeals. In addition to his legal experience, Mr. Bittner has a Bachelor of Arts in History and Humanities from the University of Texas. Mr. Bittner's biography is attached to the Declaration of Michael Bittner in Support of Petitioner's Motion for *Pro Hac Vice* Admission as Exhibit A.

Mr. Bittner also has particular experience and familiarity with the substantive and technical issues involved in this *Inter Partes* review proceeding.

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