

**PETITIONER'S UPDATED EXHIBIT LIST**

**IPR2016-01622**

<b>Exhibit</b>	<b>Description</b>
1001	U.S. Patent 6,850,414 to Benisek ('414 patent)
1002	UK Patent Application GB 2 289 573 A to Simpson
1003	PC SDRAM Unbuffered DIMM Specification, Version 1.0
1004	U.S. Patent Application Publication No. 2002/0196612 to Gall
1005	PC133 SDRAM Registered DIMM Design Specification
1006	Declaration of Professor Vivek Subramanian ("Subramanian")
1007	'414 Patent File History
1008	File History for U.S. Patent No. 6,332,183
1009	District Court Complaint
1010	Professor Vivek Subramanian's <i>Curriculum Vitae</i>
1011	Intel <i>Small Outline Package Guide</i> (1999) Denver: Intel Corporation.
1012	Micron "64Mb: x32 SDRAM." Micron Technology, Inc., 1999.
1013	U.S. Patent No. 4,954,088 "Socket for Mounting an IC Chip Package on a Printed Circuit Board" Sep. 4, 1990 (Fujizaki et al.)
1014	Deposition Transcript of Professor Joseph B. Bernstein
1015	Paper 2: Petition for <i>INTER PARTES</i> Review

1016	Declaration of Dr. Vivek Subramanian
1017	U.S. Patent Application Pub. No. US 2002/0006032 A1 “Low-Profile Registered Dimm” Jan. 17, 2002 (Karabatsos)
1018	U.S. Patent No. 5,973,951 “Single In-Line Memory Module” Oct. 26, 1999 (Bechtolsheim et al.)
1019	U.S. Patent No. 6,038,132 “Memory Module” Mar. 14, 2000 (Tokunaga et al.)
1020	DE 101 42 361 A1
1021	English Translation of Published Patent Application DE 101 42 361 A1 (Kiehl)
1022	Second Declaration of Dr. Vivek Subramanian (August 17, 2017)
1023	Translation Certification of DE 101 42 361 A1
1024	Patent Owner Response
1025	Petitioner’s Demonstratives

## CERTIFICATE OF SERVICE

Pursuant to 37 CFR § 42.6(e)(4), the undersigned certifies that on November 9, 2017, a complete and entire copy of this Petitioner's Updated Exhibit List and Exhibit 1025 were provided via email to the Petitioner by serving the correspondence email address of record as follows:

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