

UPDATED EXHIBIT LIST

Ex. 2001	Cara Garretson. “More DRAM vendors involved in Justice Department probe.” <i>IDG News Service</i> July 21, 2002. Computer World, Inc. November 21, 2016.
Ex. 2002	“Error Correction Code in SoC FPGA-Based Memory Systems.” <i>Altera Corporation</i> April 2012.
Ex. 2003	“133 MHz PC SDRAM 64-Bit Non-ECC/Parity 144 Pin UNBUFFERED SO-DIMM SPECIFICATION.” <i>Intel</i> , Revision 1.0C. August 2000
Ex. 2004	“PC SDRAM Serial Presence Detect (SPD) Specification.” <i>Intel</i> , Revision 1.2B. November 1999.
Ex. 2005	Declaration of Nathan Nobu Lowenstein in support of Motion for Admission <i>Pro Hac Vice</i>
Ex. 2006	Institution Decision, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 8 (PTAB Apr. 18, 2016)
Ex. 2007	Patent Owner’s Motion To Amend, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 14 (PTAB Jul. 18, 2016)
Ex. 2008	Petitioner’s Opposition To Motion To Amend, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 17 (PTAB Oct. 18, 2016)
Ex. 2009	Patent Owner Reply To Petitioner’s Opposition, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 21 (PTAB Nov. 17, 2016)
Ex. 2010	Petitioner’s Request For Oral Argument, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 27 (PTAB Dec. 9, 2017)
Ex. 2011	Final Written Decision, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 38 (PTAB Mar. 31, 2017)
Ex. 2012	Deposition Transcript Of Vivek Subramanian (Apr. 19, 2017)
Ex. 2013	Exhibit 1 To Deposition Transcript Of Vivek Subramanian (Apr. 19, 2017)
Ex. 2014	Transcript of October 5, 2017 Conference Call

CERTIFICATE OF SERVICE

The undersigned hereby certifies that the following documents were served by electronic service, by agreement between the parties, on the date signed below:

PATENT OWNER POLARIS INNOVATIONS LTD.'S UPDATED EXHIBIT LIST

The names and address of the parties being served are as follows:

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Respectfully submitted,

/ Shawn Chi /

Date: October 7, 2017