UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

KINGSTON TECHNOLOGY COMPANY, INC.,

Petitioner

v.

POLARIS INNOVATIONS LTD.,

Patent Owner

Case IPR2016-01622 Patent 6,850,414 B2

PATENT OWNER'S RESPONSE
TO PETITIONER'S OUT OF TIME (i) REQUEST
FOR REHEARING OF FINAL WRITTEN DECISION
AND (ii) MOTION TO LIMIT THE PETITION



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Exhibit 2002	"Error Correction Code in SoC FPGA-Based Memory Systems." <i>Altera Corporation</i> April 2012.		
Exhibit 2003	"133 MHz PC SDRAM 64-Bit Non-ECC/Parity 144 Pin UNBUFFERED SO-DIMM SPECIFICATION." <i>Intel</i> , Revision 1.0C. August 2000		
Exhibit 2004	"PC SDRAM Serial Presence Detect (SPD) Specification." <i>Intel</i> , Revision 1.2B. November 1999.		
Exhibit 2005	Declaration of Nathan Nobu Lowenstein in support of Motion for Admission <i>Pro Hac Vice</i>		
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Exhibit 2009	Patent Owner Reply To Petitioner's Opposition, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 21 (PTAB Nov. 17, 2016)		
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Exhibit 2011	Final Written Decision, <i>Amerigen Pharms. Ltd. v. Shire LLC</i> , IPR2015-02009, Paper 38 (PTAB Mar. 31, 2017)		
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