# UNITED STATES PATENT AND TRADEMARK OFFICE ————— BEFORE THE PATENT TRIAL AND APPEAL BOARD —————

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC d/b/a ON SEMICONDUCTOR,
Petitioner

v.

POWER INTEGRATIONS, INC., Patent Owner

Case IPR2016-01600 Patent No. 7,834,605

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PATENT OWNER POWER INTEGRATIONS, INC.'S UNOPPOSED MOTION FOR *PRO HAC VICE* ADMISSION OF HOWARD G. POLLACK UNDER 37 C.F.R. § 42.10(c)



## LIST OF EXHIBITS

Exhibit No.	Description
PI 2001	Copy of Agreement and Plan of Merger as between Fairchild and ON Semiconductor filed with the SEC
PI 2002	Press release related to merger between Fairchild and ON Semiconductor
PI 2003	Power Integrations, Inc., v. Fairchild Semiconductor International, Inc. et al., Case No. 08-309-LPS (D. Del.), Dkt. No. 401
PI 2004	RESERVED
PI 2005	Article about completion of merger between ON Semiconductor and Fairchild
PI 2006	Power Integrations, Inc., v. Fairchild Semiconductor International, Inc. et al., Case No. 08-309-LPS (D. Del.), Dkt. No. 731
PI 2007	Confidentiality Agreement, dated September 14, 2015
PI 2008	"XYZs of Oscilloscopes," Tektronix, Inc., © 2000
PI 2009	"Oscilloscope Fundamentals," Tektronix, Inc., © 2009
PI 2010	Declaration of Arthur W. Kelley
PI 2011	U.S. Provisional Patent Application No. 60/325,642
PI 2012	File History for U.S. Patent No. 7,834,605
PI 2013	U.S. Patent No. 5,028,861 ("Pace")
PI 2014	U.S. Patent No. 5,680,034 ("Redl")
PI 2015	Reserved
PI 2016	Resume of Arthur W. Kelley, Ph.D.
PI 2017	Declaration of Howard G. Pollack in Support of Admission Pro Hac Vice



Pursuant to 37 C.F.R. § 42.10(c), the Patent Owner ("Power Integrations") respectfully requests that the Board recognize Howard G. Pollack as counsel *pro hac vice* in this proceeding. Patent Owner seeks the counsel of Howard G. Pollack due to his experience in representing Power Integrations in other patent-related matters and particularly due to his familiarity with the substantive and technical issues involved in this proceeding.

Petitioner does not oppose this motion.

### **Statement of Facts**

Mr. Pollack is a patent litigation attorney with more than 20 years of experience representing clients in cases involving mechanical devices, optical devices, electrical devices, computer hardware, computer software, the Internet, and semiconductors. Mr. Pollack regularly litigates patent cases before the United States Court of Appeals for the Federal Circuit, various federal district courts, and the International Trade Commission. Through his practice in such cases, Mr. Pollack has gained substantial experience in jury trials, bench trials, discovery, Markman hearings, and appeals. He has also been substantively involved in developing technical and legal arguments, and working with technical experts and inventors. Mr. Pollack began his legal career considering patent cases as a clerk for the Honorable Judge H. Robert Mayer on the United States Court of Appeals



for the Federal Circuit from 1992-1994. Power Integrations provides Appendix A to Mr. Pollack's declaration, as evidence of Mr. Pollack's biography.

Mr. Pollack also has particular experience and familiarity with the substantive and technical issues involved in this *inter partes* review proceeding.

Mr. Pollack has particular experience relevant to the patent-at-issue, having been having been co-lead counsel on one case involving this patent, a currently pending suit adverse to Fairchild in the District of Delaware.

Mr. Pollack has also served as counsel for Patent Owner since 1998 in intellectual property matters involving, among others, power supply integrated circuit controller technology. In the litigation matters, Mr. Pollack has overseen all phases of the litigation cases from discovery through claim construction, including expert discovery and trial. Moreover, through his representation in the related matters, Power Integrations has developed a particular relationship with Mr. Pollack such that Power Integrations desires to continue the relationship with Mr. Pollack for the purpose of this proceeding.



## **Declaration of Individual Seeking to Appear**

This Motion for *Pro Hac Vice* Admission is accompanied by a Declaration of Howard G. Pollack.

Accordingly, Power Integrations submits that there is good cause under 37 C.F.R. § 42.10(c) for the Board to recognize Howard G. Pollack as counsel *pro hac vice* during this proceeding.

Respectfully submitted,

Date: August 18, 2017 /Neil A. Warren/

Neil A. Warren, Reg. No. 63,582 Counsel for Patent Owner

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