

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC
d/b/a ON SEMICONDUCTOR,
Petitioner

v.

POWER INTEGRATIONS, INC.,
Patent Owner

Case IPR2016-01600
Patent No. 7,834,605

**DECLARATION OF HOWARD G. POLLACK IN SUPPORT OF PATENT
OWNER'S MOTION FOR *PRO HAC VICE* ADMISSION**

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**DECLARATION OF HOWARD G. POLLACK IN
SUPPORT OF PATENT OWNER'S MOTION FOR
PRO HAC VICE ADMISSION**

I, HOWARD G. POLLACK, hereby declare to the following:

1. I am a member in good standing of the State Bar of California, and am admitted to practice in the United States District Courts for the Northern, Central, and Southern Districts of California, and the United States Court of Appeals for the Federal Circuit. I am co-lead counsel for Patent Owner Power Integrations, Inc. in several pending litigation matters, including in *Power Integrations, Inc. v. Fairchild Semiconductor International, Inc. et al*, Case No. 08-00309 (D. Del.), a co-pending district court case concerning, inter alia, U.S. Patent No. 7,834,605.
2. I have not been suspended or disbarred from practice before any court or administrative body.
3. I have never had an application for admission to practice before any court or administrative body denied on the basis of ethical or moral or ethical character or ethical breach.
4. No sanction or contempt citation has been imposed against me by any court or administrative body.
5. I have read and will comply with the Office Patent Trial Practice Guide and the Board's Rules of Practice for Trials set forth in part 42 of the Code of Federal Regulations.

6. I will be subject to the USPTO Rules of Professional Conduct set forth in 37 C.F.R. §§ 11.101 *et seq.* and disciplinary jurisdiction under 37 C.F.R. § 11.19(a).

7. In the past three years, I have applied for and been granted admission *pro hac vice* in IPR2014-00388 and IPR2016-00809. I am currently applying for *pro hac vice* admission in: IPR2016-01589; IPR2016-01590; IPR2016-01592; IPR2016-01594; IPR2016-01595; and IPR2016-01597.

8. I am an experienced litigation attorney with more than 20 years of experience representing clients in patent cases involving mechanical devices, optical devices, electrical devices, computer hardware, computer software, the Internet, and semiconductors. I regularly litigate patent cases in various forums including the United States Court of Appeals for the Federal Circuit, federal district courts, and the International Trade Commission. Through my experience in patent litigation matters, I have represented clients in many phases of litigation including discovery, Markman hearings, jury trials, bench trials, and appeals. I have also been substantively involved in developing technical and legal arguments, and working with technical experts and inventors.

9. I have particular experience relevant to the patent-at-issue, having been co-lead counsel on one case involving this patent, a currently pending suit adverse to Fairchild in the District of Delaware (identified in paragraph 1 above). I have also served as counsel for the Patent Owner since 1998 in intellectual property matters

involving, among others, power supply integrated circuit controller technology. In the litigation matters, I have overseen all phases of the litigations from discovery through claim construction, including expert discovery and trial.

10. My biography is attached hereto as Appendix A.

11. I began my legal career as a clerk for the Honorable Haldane Robert Mayer on the United States Court of Appeals for the Federal Circuit from 1992 to 1994. Additionally, I served as the Managing Principal of Fish & Richardson's Silicon Valley office from 2007 to 2015.

12. I have read and analyzed the patent-at-issue in this proceeding, the petition, the preliminary response and cited portions of the accompanying exhibits.

13. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Respectfully submitted,

Date: 8-15-17



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