

### US005808948A

### United States Patent [19]

[54] CEMICONDUCTOD MEMODY DEVICE

Kim et al.

5,808,948 [11] **Patent Number:** 

**Date of Patent:** Sep. 15, 1998 [45]

[34]	SEMICONDUCTOR MEMORY DEVICE	
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[21]	Appl. No.:	769,615

[22] Filed: Dec. 18, 1996

[30] Foreign Application Priority Data

Dec. 30, 1995 [KR] Rep. of Korea ...... 95/69724 [51] Int. Cl.<sup>6</sup> ...... G11C 7/00

365/230.03

#### [56] **References Cited**

### U.S. PATENT DOCUMENTS

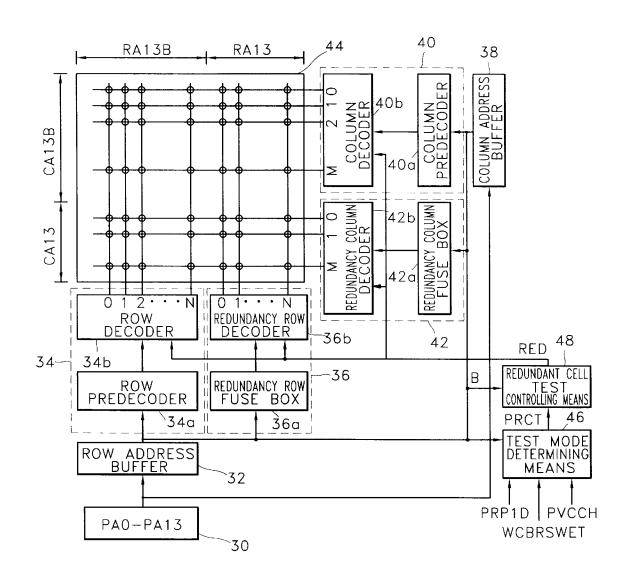
Primary Examiner—A. Zarabian

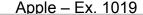
Attorney, Agent, or Firm-Marger, Johnson, McCollom & Stolowitz P.C.

#### **ABSTRACT** [57]

There is provided a semiconductor memory device which does not require an additional input pad to apply a signal for discriminating between a normal cell and a redundant cell. The semicodnuctor memory device has (claim 1). Therefore, the normal cell array or the redundant cell array is sequentially selected and tested by using the same input pad to which the bank select bit is input, without an additional pad.

### 11 Claims, 4 Drawing Sheets







# FIG. 1 (PRIOR ART)

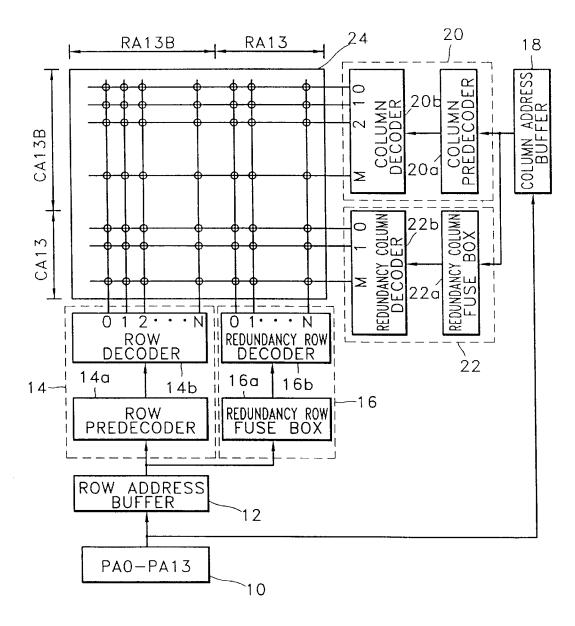




FIG. 2

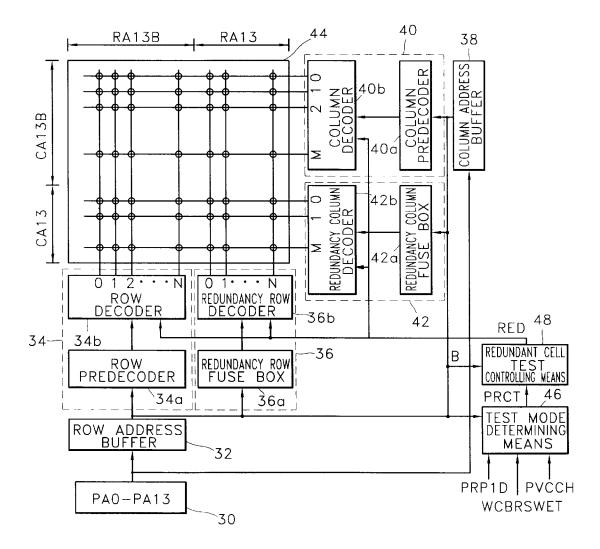


FIG. 3

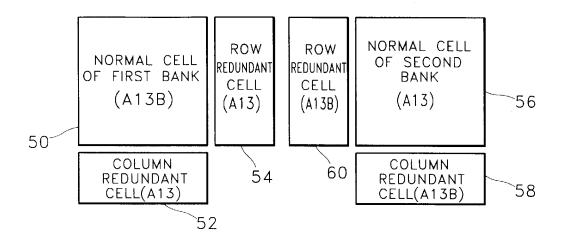
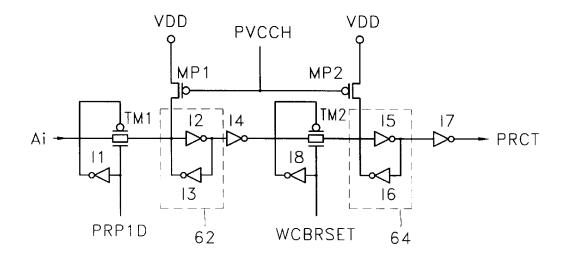
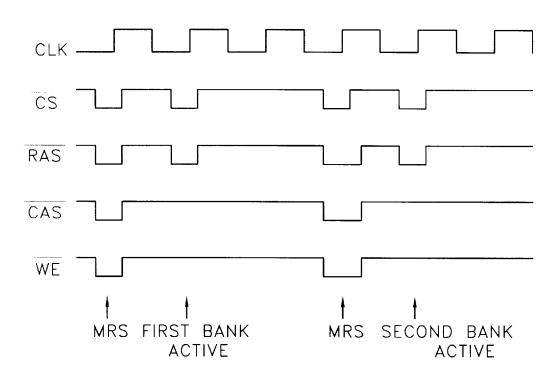


FIG. 4



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FIG. 5



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