

### US006009363A

## United States Patent [19]

### Beckert et al.

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[54]	VEHICLE COMPUTER SYSTEM WITH
	HIGH SPEED DATA BUFFER AND SERIAL
	INTERCONNECT

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[21] Appl. No.: 08/668,781

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### Related U.S. Application Data

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[51]	Int. Cl. <sup>6</sup>	H04B 1/03
[52]	U.S. Cl 701/33; 379	0/522; 340/825.25;
	361/814;	455/345; 307/10.7
[58]	Field of Search	701/29–33, 24,
	701/25, 264/551 01,	240/420 426 601

701/35; 364/551.01; 340/439, 426, 691, 825.5, 310.1, 538, 825.25, 815.69; 361/686, 683, 680, 814, 679; 395/281, 290; 370/522, 476, 537; 455/345, 346, 349, 348; 307/10.7,

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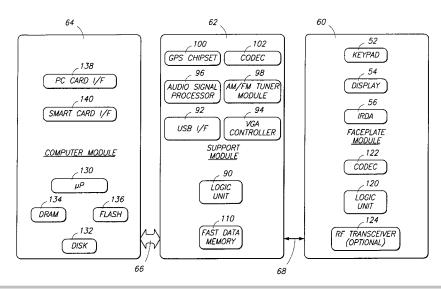
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Primary Examiner—Jacques H. Louis-Jacques Attorney, Agent, or Firm-Lee & Hayes, PLLC

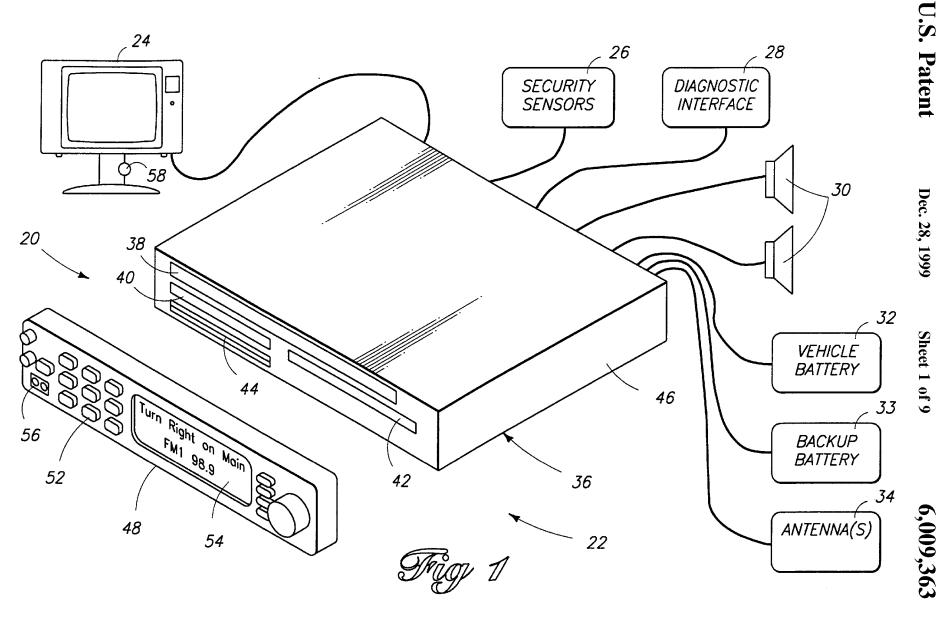
#### **ABSTRACT** [57]

A vehicle computer system has a housing sized to be mounted in a vehicle dashboard or other appropriate location. The housing includes a base unit and a faceplate. A first logic unit is mounted to the base unit to form a support module. The support module has two interfacing slots and can support connections to multiple peripheral devices. The support module facilitates high speed data exchange between the peripheral devices for streaming continuous audio or video data. The support module has a fast data memory to temporarily hold data being communicated between the peripheral devices. The support module also has a memory access circuit associated with each of the peripheral devices which designates at least one storage area within the fast data memory to hold data received from, or to be sent to, the associated peripheral device. The vehicle computer has a computer module which can be connected to or removed from one interfacing slot of the support module. A multi-bit bus (e.g., PCI bus) interfaces the computer module and the support module. The vehicle computer system also has a logic unit mounted to the faceplate to form a faceplate module. This module is detachably connected to the other interfacing slot of the support module. When the faceplate module is attached, a high speed serial interface electronically couples the support module to the faceplate module. The high speed serial interface enables the logic units on the support and faceplate modules to exchange a high speed, synchronized, serial bit stream. This data stream is organized into multiple frames, with each frame having multiple data bits and at least one valid bit to indicate whether the data bits are valid.

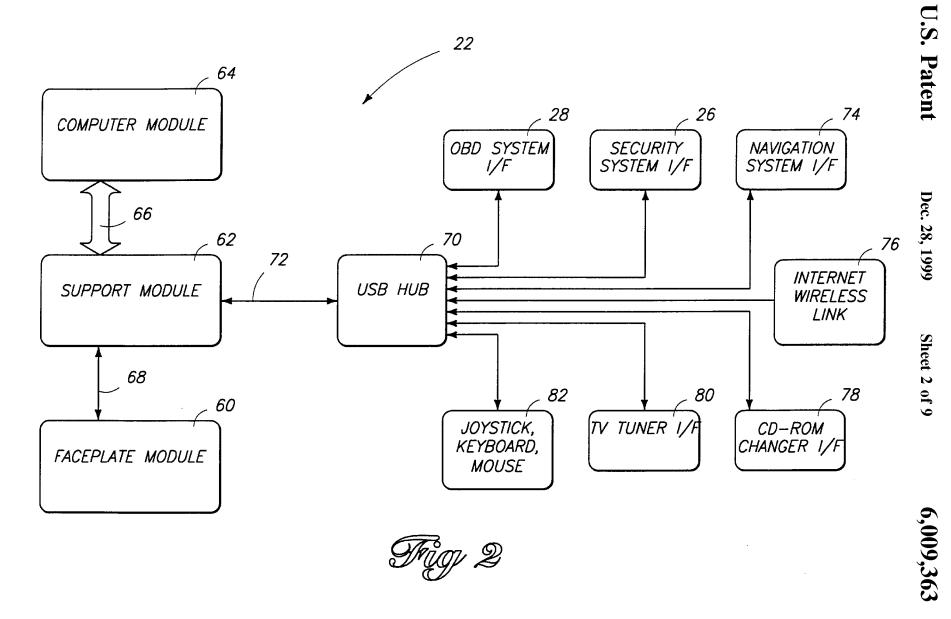
### 31 Claims, 9 Drawing Sheets



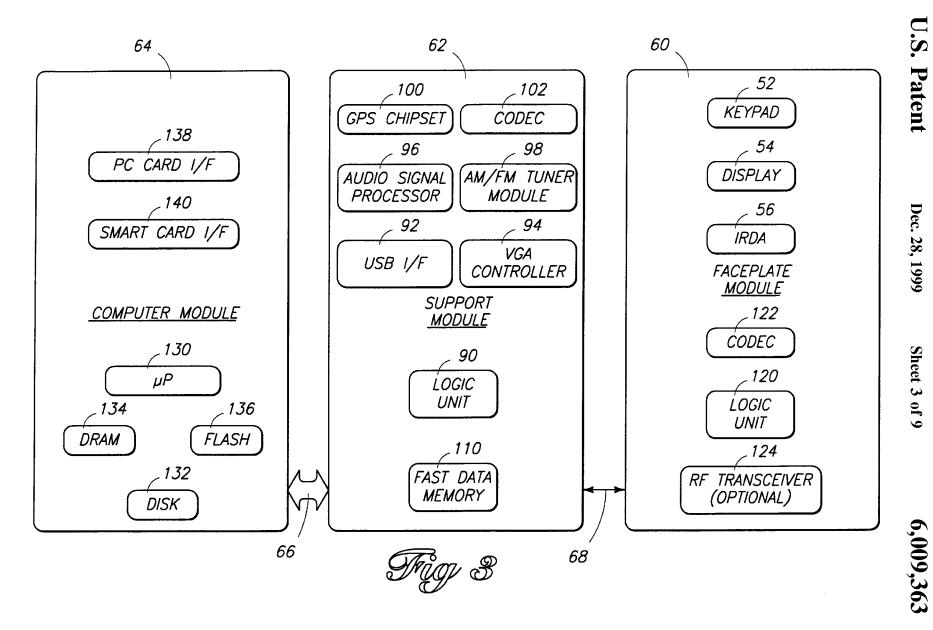




Petitioners Exhibit 1008, Page 2

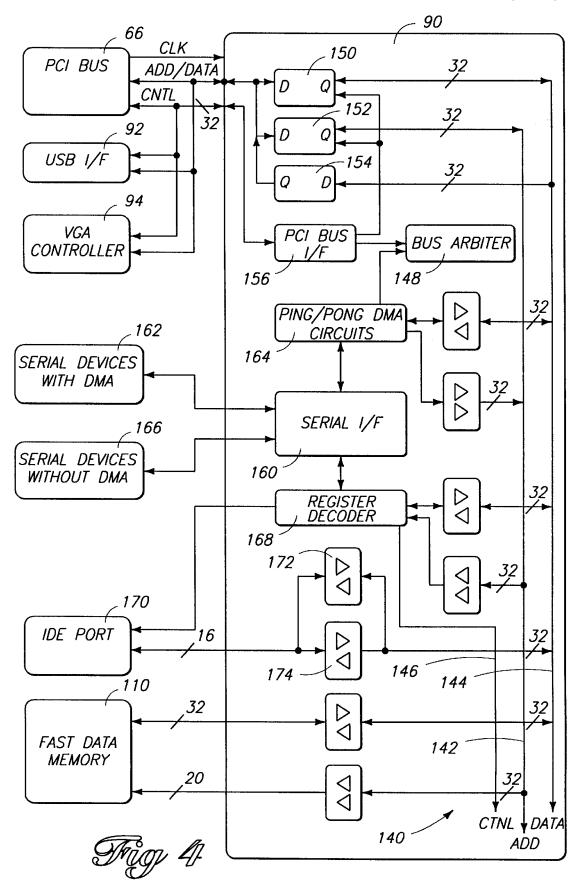


Petitioners Exhibit 1008, Page 3



Petitioners Exhibit 1008, Page 4

Dec. 28, 1999



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