IN THE UNITED STATES PATENT A1ND TRADEMARK OFFICE

In the *Inter Partes* Review of: Trial Number: IPR2016-01425

     U.S. Patent No. 8,155,012

Filed: Sep. 26, 2008

Earliest Related Appln: Apr. 10, 1998

Issued: Apr. 10, 2012

Inventor(s): John F. Austermann, III

Assignee: Chrimar Systems, Inc.

Title: SYSTEM AND METHOD FOR   Panel: To Be Assigned
ADOPTING A PIECE OF TERMINAL
EQUIPMENT

Mail Stop *Inter Partes* Review
Commissions for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DECLARATION OF ANDREW WOLFE, PH.D. IN SUPPORT OF

PETITION FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 8,155,012

1

Table of Contents

I , Andrew Wolfe, Ph.D. hereby declare as follows:

I. INTRODUCTION

1. I have been retained as an expert witness on behalf of D-Link Systems, Inc. ("D-Link") for the above-captioned Petition for *Inter Partes Review* ("IPR") of U.S. Patent No. 8,155,012 ("the '012 patent", Ex. 1001). I am being compensated for my time in connection with this IPR. My compensation is in no way dependent on the outcome of this matter.

2. I have been asked to provide my opinions regarding whether any of claims 31, 34, 35, 36, 40, 43, 44, 52, 56, and 60 of the '012 patent is invalid, as anticipated by the prior art, or would have been obvious to a person having ordinary skill in the art at the time of the alleged invention.

3. The '012 patent issued on April 10, 2012, from U.S. Patent Application No. 12/239,001, filed on September 26, 2008 and has a series of related applications, with the earliest provisional application filing date of April 10, 1998.

4. The face of the '012 patent names John F. Austermann, III and Marshall B. Cummings as the purported inventors.

5. In preparing this Declaration, I have reviewed the '012 patent, the file history of the '012 patent, and numerous prior art references and technical references from the time of the alleged invention.

6. I have been advised and it is my understanding that patent claims in an IPR are given their broadest reasonable interpretation in view of the patent specification, file history, and the understanding of one having ordinary skill in the relevant art at the time of the purported invention.

7. In forming the opinions expressed in this Declaration, I relied upon my education and experience in the relevant field of the art, and have considered the viewpoint of a person having ordinary skill in the relevant art, as of April 1998. My opinions directed to the invalidity of each of claims 31, 34, 35, 36, 40, 43, 44, 52, 56, and 60 (cross 59 / 31, 35, 36, 40, 43, 52) of the '012 patent are based, at least in part, on the following prior art references:

Reference	Date of Public Availability
U.S. Patent No. 5,991,885 to Chang et al. ("Chang")	Chang (Ex. 1006) was filed on June 11, 1997, and issued on November 23, 1999.
U.S. Patent No. 5,121,482 to Patton ("Patton")	Patton (Ex. 1007) was filed on September 11, 1989, and issued on June 9, 1992.
U.S. Patent No. 5,568,525 to De Nijs et al. ("De Nijs")	De Nijs (Ex. 1008) was filed on August 19, 1993 and issued on October 22, 1996.
U.S. Patent No. 5,790,363 to Chaudhry ("Chaudhry")	Chaudhry (Ex. 1009) was filed on December 3, 1997 and issued on August 4, 1998.

U.S. Patent No. 5,406,260 to Cummings et al. ("Cummings")	Cummings (Ex. 1010) was filed on December 18, 1992 and issued on April 11, 1995.
U.S. Patent No. 5,444,184 to Hassel ("Hassel")	Hassel (Ex. 1011) was filed on February 10, 1993 and issued on August 22, 1995.

II.   BACKGROUND AND QUALIFICATIONS

8.   I have more than 30 years of experience as a computer system designer, personal computer graphics designer, educator, and as an executive in the electronics industry.

9.   In 1985, I earned a B.S.E.E. degree in Electrical Engineering and Computer Science from The Johns Hopkins University. In 1987, I received an M.S. degree in Electrical and Computer Engineering from Carnegie Mellon University. In 1992, I received a Ph.D. in Computer Engineering from Carnegie Mellon University. My doctoral dissertation proposed a new approach for the architecture of a computer processor.

10.   In 1983, I began designing touch sensors, microprocessor-based computer systems, and I/O (input/output) cards for personal computers as a senior design engineer for Touch Technology, Inc. During the course of my design projects with Touch Technology, I designed I/O cards for PC-compatible computer systems, including the IBM PC-AT, to interface with interactive

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