

## US005786276A

## **United States Patent** [19]

## Brooks et al.

[45] Date of Patent: Jul. 28, 1998

**Patent Number:** 

[54]	SELECTIVE PLASMA ETCHING OF
	SILICON NITRIDE IN PRESENCE OF
	SILICON OR SILICON OXIDES USING
	MIXTURE OF CH3F OR CH2F2 AND CF4
	AND O2

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[21] Appl. No.: 829,683

[22] Filed: Mar. 31, 1997

[51] Int. Cl.<sup>6</sup> ...... B44C 1/22; H01L 21/302

[52] **U.S. Cl.** ...... **438/724**; 438/719; 438/723; 438/740

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Primary Examiner—R. Bruce Breneman

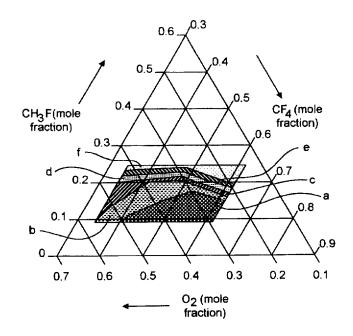
Assistant Examiner—George Goudreau

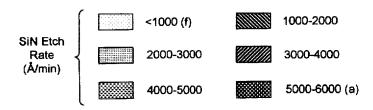
Attorney, Agent, or Firm—Fliesler, Dubb, Meyer & Lovejoy

[57] ABSTRACT

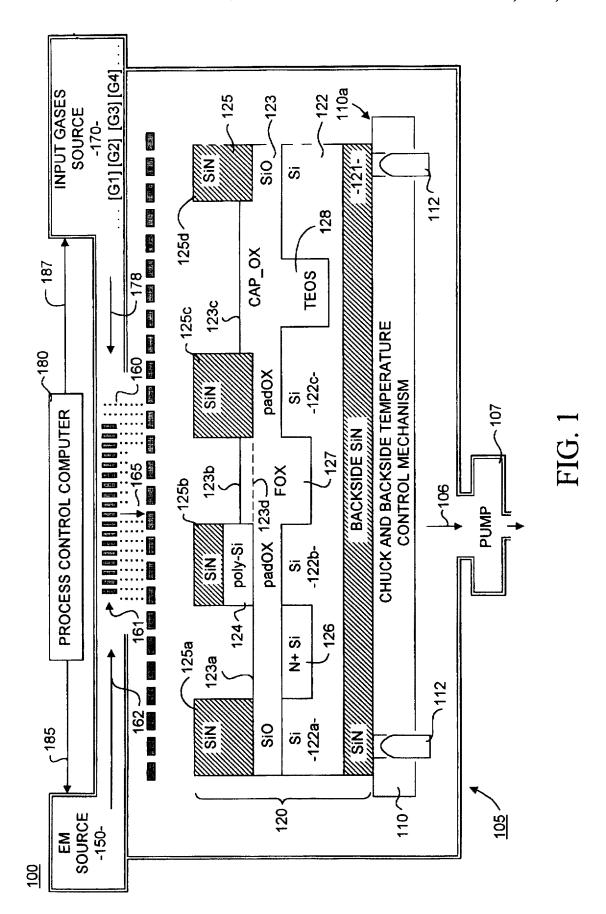
A chemical downstream etching (CDE) that is selective to silicon nitrides (SiN) over silicon oxides (SiO) uses at least one of a CH<sub>3</sub>F/CF<sub>4</sub>/O<sub>2</sub> recipe and a CH<sub>2</sub>F<sub>2</sub>/CF<sub>4</sub>/O<sub>2</sub> recipe. Inflow rates are mapped for the respective components of the input recipe to find settings that provide both high nitride etch rates and high selectivity towards the SiN material. A pins-up scheme is used for simultaneously stripping away backside nitride with topside nitride.

### 18 Claims, 6 Drawing Sheets

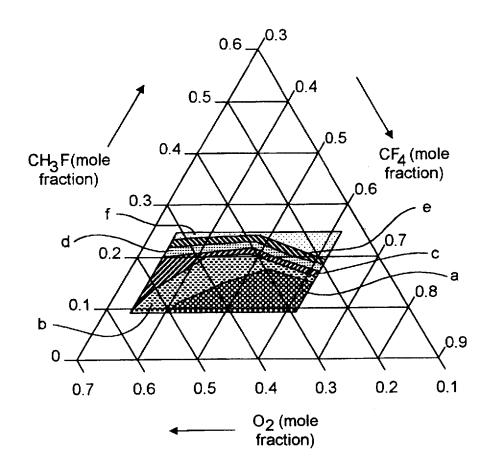












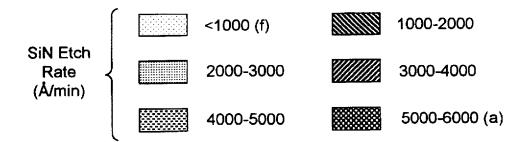
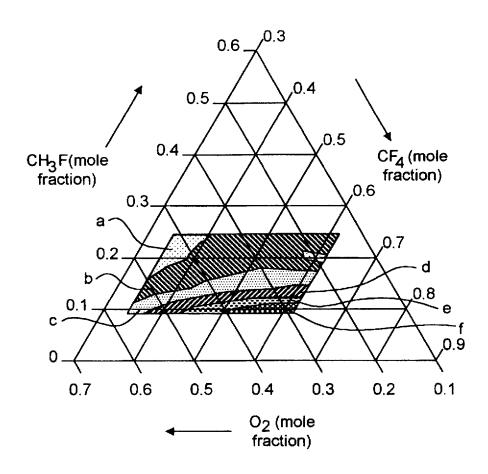


FIG. 2



	<10 (a)	10-20
SiO <sub>2</sub> Etch Rate (Å/min)	20-30	30-40
	40-50	>50 (f)

FIG. 3

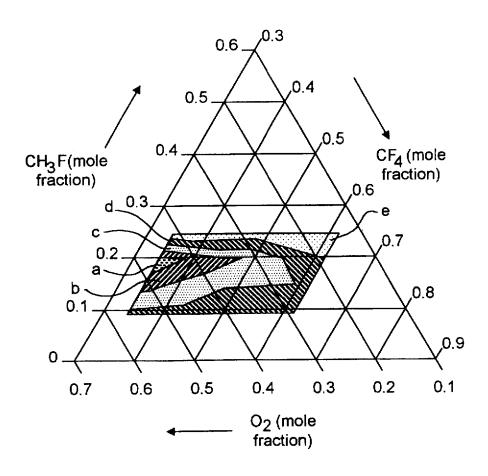


FIG. 4



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