

UNITED STATES PATENT AND TRADEMARK OFFICE

---

BEFORE THE PATENT TRIAL AND APPEAL BOARD

---

TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY, LTD.,  
Petitioner,

v.

GODO KAISHA IP BRIDGE 1,  
Patent Owner.

---

Case IPR2016-01376  
Patent 6,197,696 B1

---

**PETITIONER'S UPDATED EXHIBIT LIST**

In accordance with 37 C.F.R. § 42.63(e), Petitioner hereby submits a current listing of Taiwan Semiconductor Manufacturing Company, Ltd.'s exhibits to counsel for Patent Owner.

**LIST OF EXHIBITS**

<b>Exhibit No.</b>	<b>Description</b>	<b>Previously Filed</b>
Exhibit 1001	U.S. Patent No. 6,197,696 to Aoi et al.	x
Exhibit 1002	Expert Declaration of Dr. Bruce W. Smith, Ph.D.	x
Exhibit 1003	U.S. Patent No. 3,617,824 to Shinoda et al.	x
Exhibit 1004	U.S. Patent No. 3,838,442 to Humphreys.	x
Exhibit 1005	U.S. Patent No. 6,140,226 to Grill et al.	x
Exhibit 1006	U.S. Patent No. 5,635,423 to Huang et al.	x
Exhibit 1007	U.S. Patent No. 5,741,626 to Jain et al.	x
Exhibit 1008	C. Akrouit et al., "A 480-MHz Microprocessor in a 0.12 $\mu$ m $L_{eff}$ CMOS Technology with Copper Interconnects," IEEE J. of Solid-State Circuits, Vol. 33, no. 11 (November 1998).	x
Exhibit 1009	J.N. Burghartz et al., "Monolithic Spiral Inductors Fabricated Using a VLSI Cu-Damascene Interconnect Technology and Low-Loss Substrates," International Electron Devices Meeting (December 1996).	x

<b>Exhibit No.</b>	<b>Description</b>	<b>Previously Filed</b>
Exhibit 1010	U.S. Patent No. 6,100,184 to Zhao et al.	x
Exhibit 1011	U.S. Patent No. 6,103,616 to Yu et al.	x
Exhibit 1012	File History of U.S. Patent No. 6,197,696 to Aoi et al.	x
Exhibit 1013	Japanese Patent Application No. 10-079371 to Aoi.	x
Exhibit 1014	Certified Translation of Japanese Patent Application No. 10-079371 to Aoi.	x
Exhibit 1015	Japanese Patent Application No. 11-075519 to Aoi.	x
Exhibit 1016	Certified Translation of Japanese Patent Application No. 11-075519 to Aoi.	x
Exhibit 1017	U.S. Provisional Patent Application No. 60/071,628.	x
Exhibit 1018	U.S. Patent No. 5,592,024 to Aoyama et al.	x
Exhibit 1019	Transcript of Teleconference with the Board, dated November 16, 2016.	x
Exhibit 1020	<a href="http://ieeexplore.ieee.org/xpl/mostRecentIssue.jsp?punumber=4251">http://ieeexplore.ieee.org/xpl/mostRecentIssue.jsp?punumber=4251</a>	Served only.
Exhibit 1021	“International Electron Devices Meeting. Technical Digest, Technical Digest of the International Electron Devices Meeting from December 8–11, 1996”	Served only.

<b>Exhibit No.</b>	<b>Description</b>	<b>Previously Filed</b>
Exhibit 1022	Burghartz et al., “Monolithic spiral inductors fabricated using a VLSI Cu-damascene interconnect technology and low-loss substrates”	Served only.
Exhibit 1023	<a href="http://ieeexplore.ieee.org/xpl/tocresult.jsp?isnumber=15684">http://ieeexplore.ieee.org/xpl/tocresult.jsp?isnumber=15684</a>	Served only.
Exhibit 1024	G. Gerosa, “Introduction To The Digital Section, Publication Year: 1998,” IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, Nov. 1998	Served only.
Exhibit 1025	J. Dreibelbis, “Introduction to the memory section, Publication Year: 1998,” IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, Nov. 1998	Served only.
Exhibit 1026	L. E. Thon, “Introduction To The Signal Processing Section, Publication Year: 1998,” IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, Nov. 1998	Served only.
Exhibit 1027	<a href="http://ieeexplore.ieee.org/xpl/tocresult.jsp?isnumber=15684&amp;filter%3DAND%28p_IS_Number%3A15684%29&amp;pageNumber=2">http://ieeexplore.ieee.org/xpl/tocresult.jsp?isnumber=15684&amp;filter%3DAND%28p_IS_Number%3A15684%29&amp;pageNumber=2</a>	Served only.
Exhibit 1028	C. Akrouit et al., “A 480-MHz RISC microprocessor in a 0.12- $\mu\text{m}$ $L_{\text{eff}}$ CMOS technology with copper interconnects,” IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, Nov. 1998	Served only.
Exhibit 1029	Declaration of J. Preston Long	Served only.

<b>Exhibit No.</b>	<b>Description</b>	<b>Previously Filed</b>
Exhibit 1030	Excerpts from James D. Plummer et al., “Silicon VLSI Technology: Fundamentals, Practice, and Modeling” (2000).	
Exhibit 1031	Excerpts from C.Y. Chang & S. M. Sze, “ULSI Technology” (1996).	
Exhibit 1032	Excerpts from S. Wolf & R.N. Tauber, “Silicon Processing for the VLSI Era: Volume 1: Process Technology” (1986).	
Exhibit 1033	U.S. Patent No. 5,091,047 to Cleeves et al.	
Exhibit 1034	U.S. Patent No. 6,287,973 to Aoi et al.	
Exhibit 1035	U.S. Patent No. 4,560,436 to Bukhman et al.	
Exhibit 1036	U.S. Patent No. 6,091,081 to Matsubara et al.	
Exhibit 1037	U.S. Patent No. 4,473,437 to Higashikawa et al.	
Exhibit 1038	U.S. Patent No. 5,880,018 to Boeck et al.	
Exhibit 1039	U.S. Patent No. 4,832,789 to Cochran et al.	
Exhibit 1040	U.S. Patent No. 4,855,252 to Peterman et al.	
Exhibit 1041	U.S. Patent No. 5,786,276 to Brooks et al.	
Exhibit 1042	U.S. Patent No. 5,756,216 to Becker et al.	
Exhibit 1043	U.S. Patent No. 5,821,168 to Jain.	
Exhibit 1044	J.M. Moran & D. Maydan, “High Resolution, Steep Profile Resist Patterns,” J. Vac. Sci. & Tech., vol. 16, no. 6 (Nov./Dec. 1979).	

# Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

## Real-Time Litigation Alerts



Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

## Advanced Docket Research



With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

## Analytics At Your Fingertips



Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

## API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

## LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

## FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

## E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.