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(12) United States Patent

Eaton, Jr. et al.

(54) SILICON BASED SUBSTRATE WITH **ENVIRONMENTAL/THERMAL BARRIER** LAYER

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- Subject to any disclaimer, the term of this (*) Notice: patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.
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Related U.S. Application Data

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- (51) Int. Cl.⁷ C23C 4/06; C23C 4/10
- (52) U.S. Cl. 427/452; 427/450; 427/453
- (58) Field of Search 427/450, 452, 427/453

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(10) Patent No.:

(45) Date of Patent:

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* cited by examiner

Primary Examiner-Deborah Jones

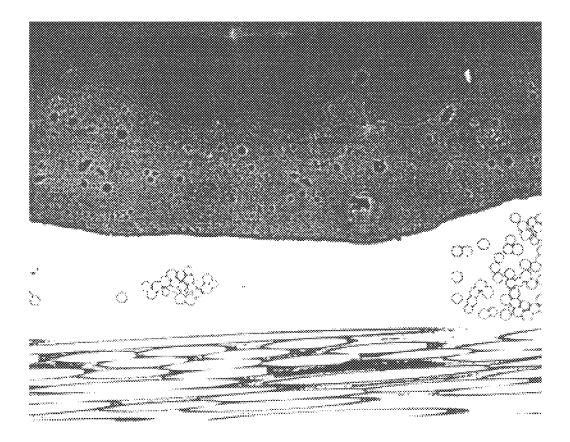
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(57)ABSTRACT

A barrier layer for a silicon containing substrate which inhibits the formation of gaseous species of silicon when exposed to a high temperature aqueous environment comprises a barium-strontium alumino silicate.

28 Claims, 4 Drawing Sheets



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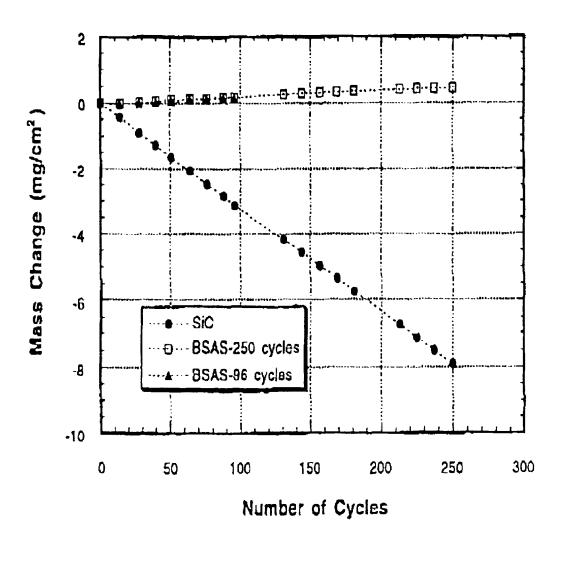
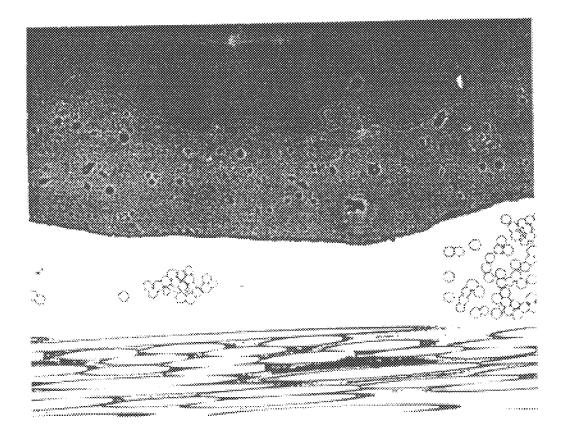
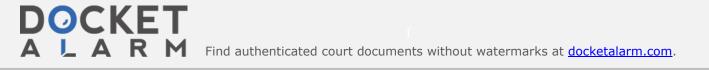


Fig. <u>1</u>

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<u>Fig. 2</u>



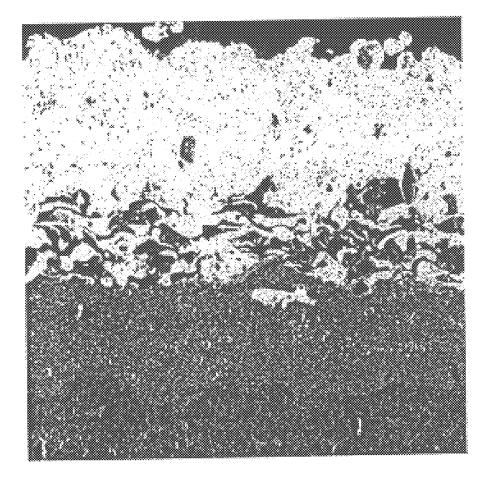


Fig. 3

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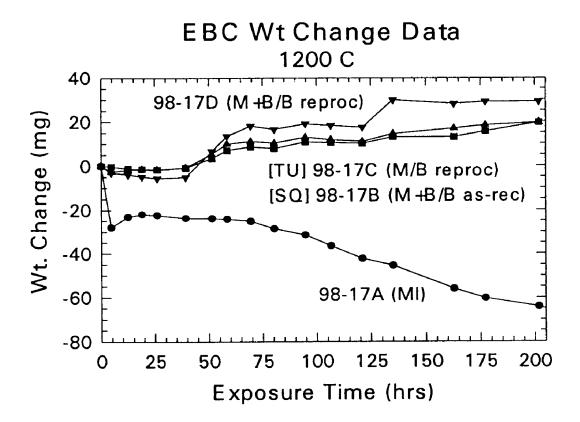


Fig. 4

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