



US008115298B2

(12) **United States Patent**
Shimanuki

(10) **Patent No.:** **US 8,115,298 B2**
(45) **Date of Patent:** **Feb. 14, 2012**

- (54) **SEMICONDUCTOR DEVICE**
- (75) Inventor: **Yoshihiko Shimanuki**, Nanyou (JP)
- (73) Assignees: **Renesas Electronics Corporation**, Kanagawa (JP); **Hitachi Yonezawa Electronics Co., Ltd.**, Yonezawa (JP)
- (*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

- (21) Appl. No.: **12/897,221**
- (22) Filed: **Oct. 4, 2010**
- (65) **Prior Publication Data**
US 2011/0018122 A1 Jan. 27, 2011

Related U.S. Application Data

- (60) Continuation of application No. 12/610,900, filed on Nov. 2, 2009, now Pat. No. 7,821,119, which is a division of application No. 12/222,099, filed on Aug. 1, 2008, now Pat. No. 7,777,312, which is a division of application No. 10/879,010, filed on Jun. 30, 2004, now Pat. No. 7,804,159, which is a continuation of application No. 10/227,817, filed on Aug. 27, 2002, now abandoned, which is a continuation of application No. 09/623,344, filed as application No. PCT/JP00/04340 on Jun. 30, 2000, now abandoned.

Foreign Application Priority Data

- Jun. 30, 1999 (JP) 11-184739
- Apr. 6, 2000 (JP) 2000-105251

- (51) **Int. Cl.**
H01L 23/495 (2006.01)
- (52) **U.S. Cl.** **257/692; 257/666; 257/E23.043; 438/123**

- (58) **Field of Classification Search** 257/692, 257/666, E23.043; 438/123
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

- 5,287,000 A 2/1994 Takahashi et al.
- 5,409,866 A 4/1995 Sato et al.
- 5,521,429 A 5/1996 Aono et al.

(Continued)

FOREIGN PATENT DOCUMENTS

- JP 03-232264 10/1991

(Continued)

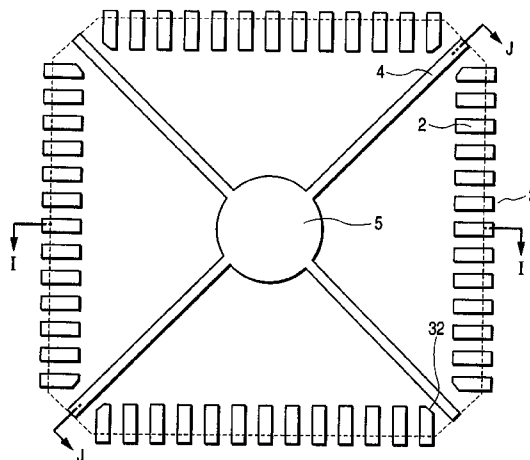
Primary Examiner — Steven J Fulk

(74) *Attorney, Agent, or Firm* — Mattingly & Malur, PC

(57) **ABSTRACT**

A semiconductor device is disclosed which includes a tab (5) for use in supporting a semiconductor chip (8), a seal section (12) as formed by sealing the semiconductor chip (8) with a resin material, more than one tab suspension lead (4) for support of the tab (5), a plurality of electrical leads (2) which have a to-be-connected portion as exposed to outer periphery on the back surface of the seal section (12) and a thickness reduced portion as formed to be thinner than said to-be-connected portion and which are provided with an inner groove (2e) and outer groove (2f) in a wire bonding surface (2d) as disposed within the seal section (12) of said to-be-connected portion, and wires (10) for electrical connection between the leads (2) and pads (7) of the semiconductor chip (8), wherein said thickness reduced portion of the leads (2) is covered by or coated with a sealing resin material while causing the wires (10) to be contacted with said to-be-connected portion at specified part lying midway between the outer groove (2f) and inner groove (2e) to thereby permit said thickness reduced portion of leads (2) and the outer groove (2f) plus the inner groove (2e) to prevent occurrence of any accidental lead drop-down detachment.

14 Claims, 38 Drawing Sheets



US 8,115,298 B2

Page 2

U.S. PATENT DOCUMENTS

5,583,372 A 12/1996 King et al.
5,594,274 A 1/1997 Suetaki
5,614,441 A 3/1997 Hosokawa et al.
5,637,915 A 6/1997 Sato et al.
5,834,831 A 11/1998 Kubota et al.
5,885,852 A 3/1999 Kishikawa et al.
5,888,883 A 3/1999 Sasaki et al.
5,942,794 A * 8/1999 Okumura et al. 257/666
6,025,640 A 2/2000 Yagi et al.
6,081,029 A 6/2000 Yamaguchi
6,111,306 A 8/2000 Kawahara et al.
6,133,637 A * 10/2000 Hikita et al. 257/777
6,201,292 B1 3/2001 Yagi et al.
6,208,020 B1 3/2001 Minamio et al.
6,229,200 B1 5/2001 McCellan et al.
6,281,568 B1 8/2001 Glenn et al.
6,291,273 B1 9/2001 Miyaki et al.
6,352,880 B1 3/2002 Takai et al.
6,355,502 B1 3/2002 Kang et al.

2002/0041010 A1* 4/2002 Shibata 257/666
2003/0127711 A1* 7/2003 Kawai et al. 257/666
2005/0106783 A1* 5/2005 Miyata 438/123
2005/0199987 A1* 9/2005 Danno et al. 257/672

FOREIGN PATENT DOCUMENTS

JP 5-129473 5/1993
JP 07-030036 1/1995
JP 07-211852 8/1995
JP 9-8205 1/1997
JP 10-189830 7/1998
JP 10-335566 12/1998
JP 11-040720 2/1999
JP 11-07440 3/1999
JP 11-074440 3/1999
JP 11-111749 4/1999
JP 2000-12758 1/2000
WO 98-029903 7/1998
WO 01/03186 1/2001

* cited by examiner

FIG. 1

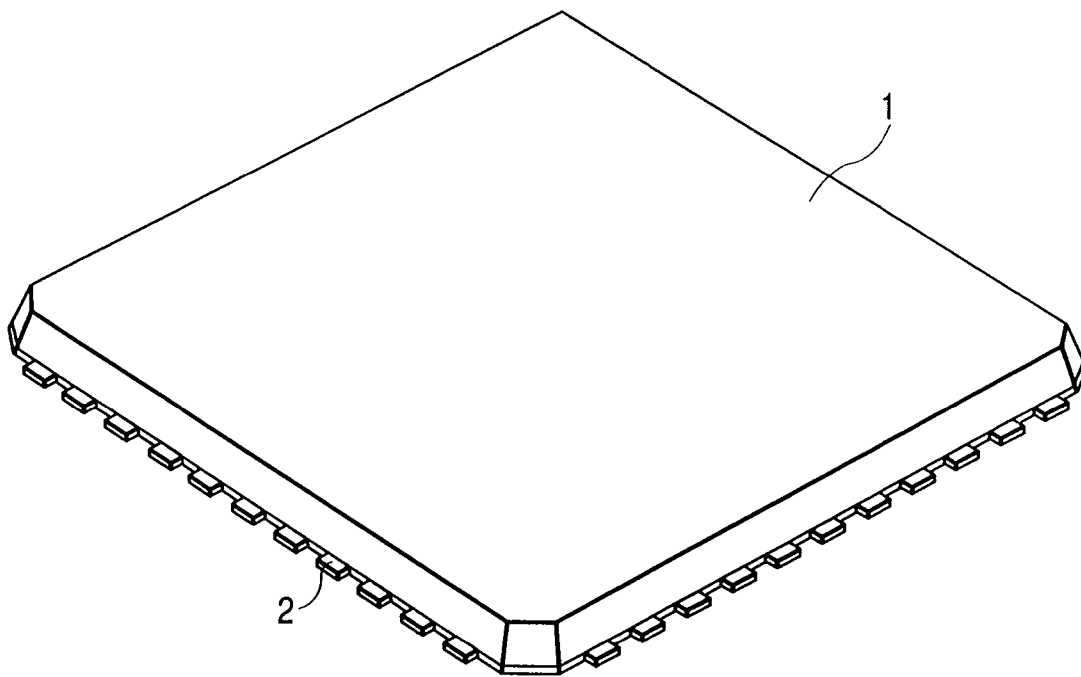


FIG. 2

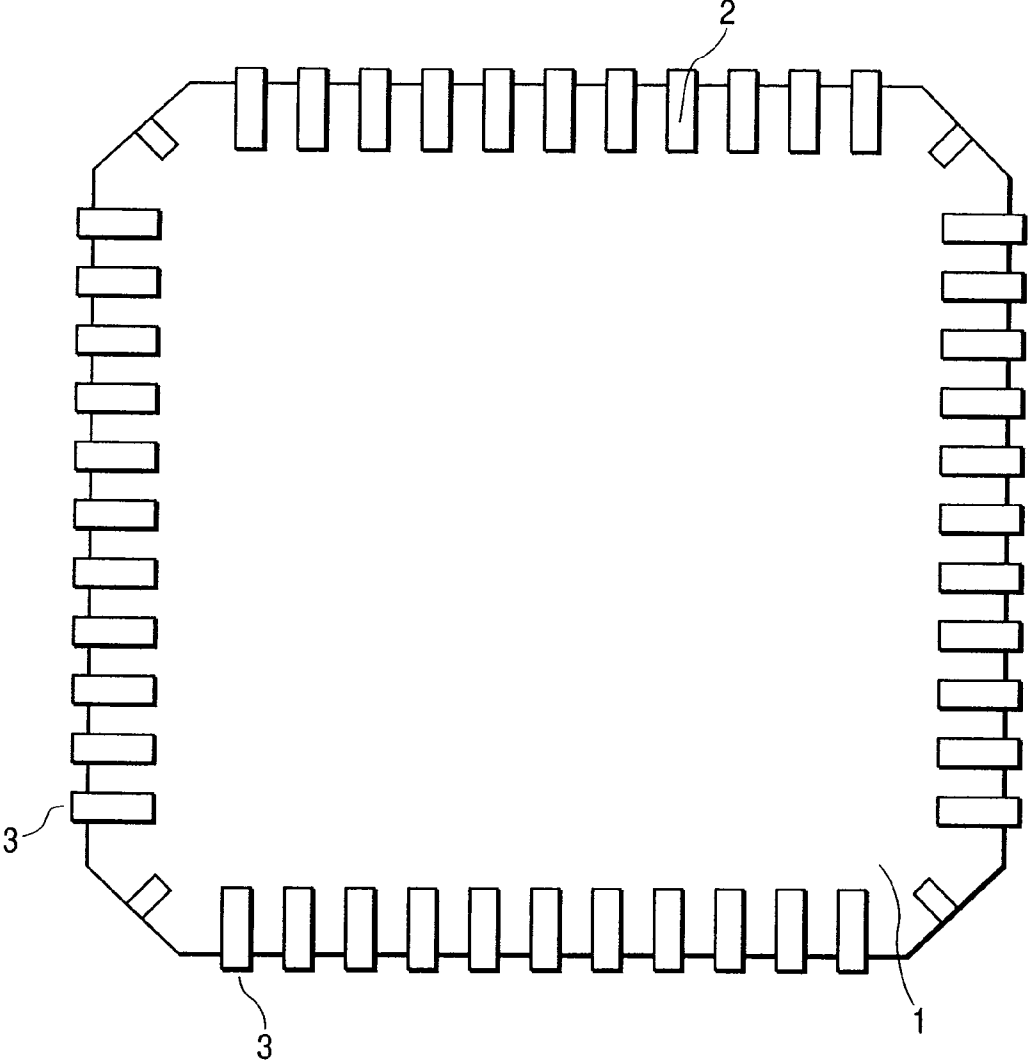


FIG. 3

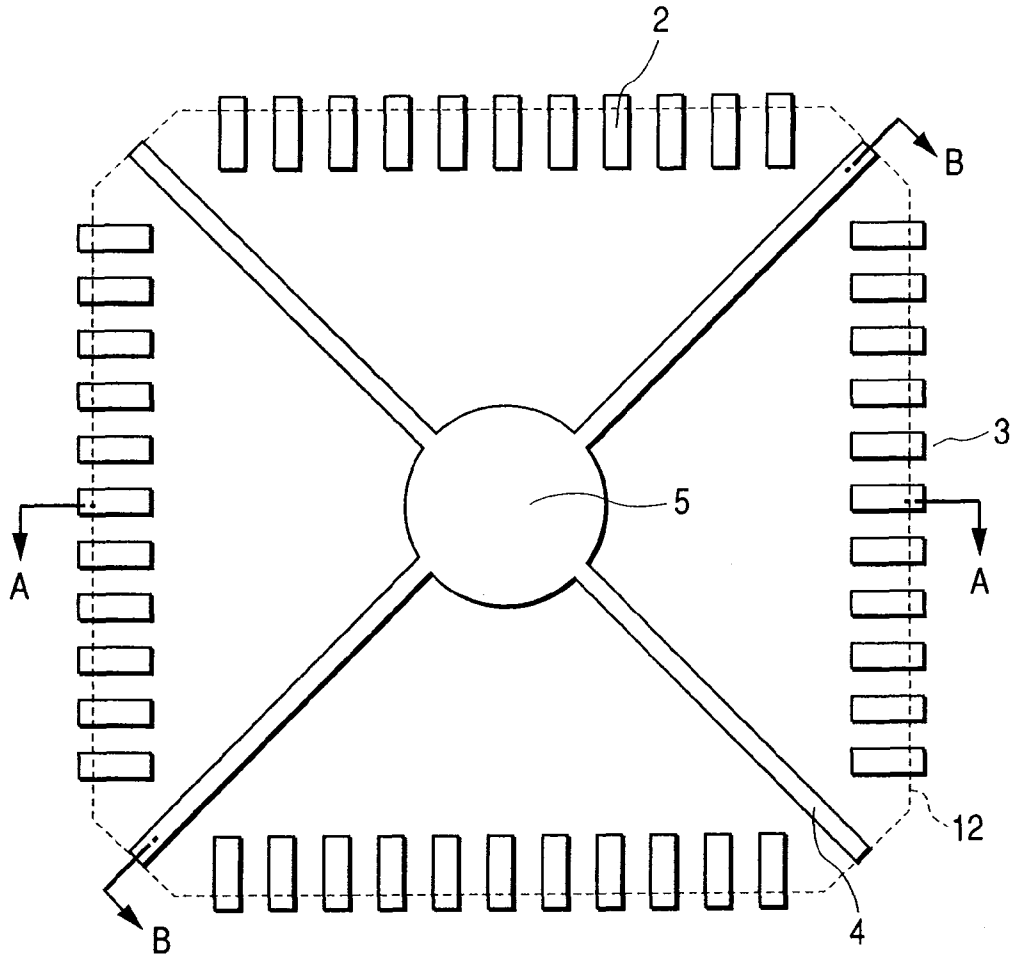


FIG. 4

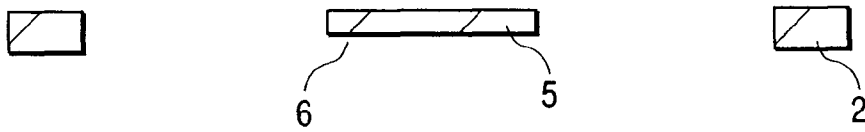
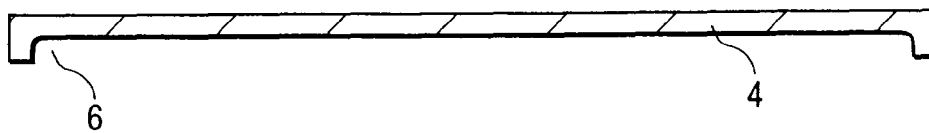


FIG. 5



Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts



Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research



With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips



Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.