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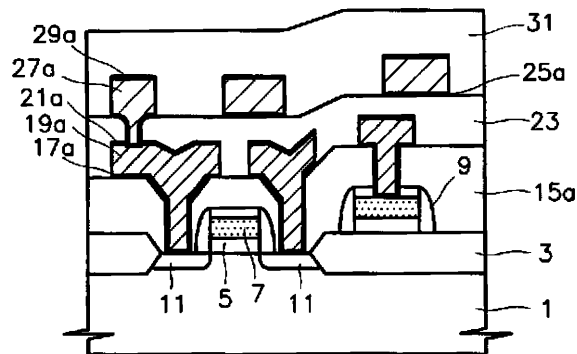
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**(54) [Title of Invention] COPPER METAL WIRE
FORMING METHOD FOR
SEMICONDUCTOR DEVICE**

(57) Abstract

[Purpose] To provide a copper metal wire forming method for a semiconductor device having a noncrystalline diffusion preventing film that can prevent diffusion between the semiconductor substrate and the copper metal film.

[Resolution means] A copper metal wire forming method for a semiconductor device has an interlayer insulating film 15a and a diffusion preventing film formed in order on a semiconductor substrate. The diffusion preventing film is formed by a chemical vapor deposition method using any one of Mo, W, Ti, Ta, WN TiW, TiN, or combinations thereof. Furthermore, a noncrystalline diffusion preventing film 17a is formed by ion injection of impurities into the diffusion preventing film, and then a copper film 19a is formed on the noncrystalline diffusion preventing film. The impurities can be one type selected from boron (B), nitrogen (N), and silicon (Si). Therefore, the diffusion of copper through the noncrystalline diffusion preventing film can be prevented.



Specification

Title of the Invention: COPPER METAL WIRE FORMING METHOD FOR SEMICONDUCTOR DEVICE

[DETAILED DESCRIPTION OF THE INVENTION]

[0001]

[Technical Field of the Invention] The present invention relates to a method of manufacturing the semiconductor device, particularly a method of forming a copper metal wire of a semiconductor device.

[0002]

[Background Technology] Generally, a metal wire forming method of a semiconductor device occupies a very important position in the semiconductor manufacturing process because this is a factor for determining the operation speed, yield, and reliability of the semiconductor device. In a conventional semiconductor device with low integration density, a metal wire layer was formed using pure aluminum, but an aluminum layer experiences contact spiking due to absorption of silicon atoms from the silicon substrate due to the increased temperature during a sintering step, and therefore Al-1% Si where aluminum is saturated with silicon is widely used as a material for the metal wire layer.

[0003]

However, if a wire layer of a semiconductor device is formed by using this Al-1% Si, silicon residue is formed by depositing silicon from the wire layer during heat treatment at a temperature of approximately 450°C or higher, silicon nodules are formed by phase epitaxial growth of silicon atoms at contact holes, and there is increased resistance of the metal wire layer and increased contact resistance. Forming a diffusion preventing film such as a TiN thin film between the metal wire layer and the silicon substrate or the insulating layer is commonly known in order to prevent the formation of aluminum spiking, silicon residue, and silicon nodules caused by the aforementioned reactions between the metal wire layer and the silicon substrate.

[0004]

On the other hand, copper with a lower relative resistance than conventional alumina metal because of a continuous reduction of the area of the semiconductor element is focused on as a next-generation metal wire material.

[0005]

However, the diffusion rate of copper in the diffusion preventing film is much larger than aluminum, and the diffusion preventing ability is lost in existing TiN films at temperatures of 600°C or higher. The cause is that a TiN thin film has a crystalline particle structure with a columnar structure, and therefore copper that primarily diffuses along the grain boundary cannot be effectively blocked. Furthermore, the TiN thin film is formed by a sputtering method, so there are disadvantages that not only is the occurrence of defects in the thin film due to radiation loss higher, but the step coverage is poor.

[0006]

[Problem to Be Resolved by the Invention] A technical problem of the present invention is to provide a copper metal wire forming method for a semiconductor device having a noncrystalline diffusion preventing film with excellent step coverage and that can block diffusion of copper through a grain boundary.

[0007]

[Means for Resolving the Problem] In order to resolve the aforementioned technical problems, the copper metal wire forming method for a semiconductor device according to one aspect of the present invention successively forms an interlayer insulating film (interlevel dielectric layer) and a diffusion preventing film (diffusion barrier film). The diffusion preventing film is formed using any one of Mo, W, Ti, Ta, WN TiW, TiN, or combinations thereof. Furthermore, a noncrystalline diffusion preventing film is formed by ion injection of impurities into the diffusion preventing film, and then a copper film is formed on the noncrystalline diffusion preventing film. The impurities can be one type selected from boron (B), nitrogen (N), and silicon (Si). Therefore, the copper of the copper film will not diffuse into the interlayer insulating film because of the noncrystalline diffusion preventing film.

[0008]

Furthermore, the copper metal wire forming method for a semiconductor device according to another aspect of the present invention forms an interlayer insulating film on the semiconductor substrate, and then forms contact holes that expose the semiconductor substrate by etching. Next, a diffusion preventing film is formed on the interlayer insulating film and the exposed semiconductor substrate. The diffusion preventing film is formed using any one of Mo, W, Ti, Ta, WN TiW, TiN, or combinations thereof. Next, a noncrystalline diffusion preventing film is formed by ion injection of impurities into the diffusion preventing film. The impurities can be one type selected from boron (B), nitrogen (N), and silicon (Si). Next, a copper film is formed on the noncrystalline diffusion preventing film. Therefore, the copper of the copper film will not diffuse into the semiconductor substrate because of the noncrystalline diffusion preventing film.

[0009]

Furthermore, the copper metal wire forming method for a semiconductor device according to another aspect of the present invention has an interlayer insulating film and a first copper film formed in order on a semiconductor substrate. Next, a via hole that exposes the first copper film is formed by etching the interlayer insulating film, and then a diffusion preventing film is formed on the exposed first copper film and the interlayer insulating film. The diffusion preventing film is formed using any one of Mo, W, Ti, Ta, WN TiW, TiN, or combinations thereof. Next, a noncrystalline diffusion preventing film is formed by ion injection of impurities into the diffusion preventing film, and then a second copper film is formed on the noncrystalline diffusion preventing film. The impurities can be one type selected from boron (B), nitrogen (N), and silicon (Si).

[0010]

Furthermore, the copper metal wire forming method for a semiconductor device according to yet another aspect of the present invention forms a first interlayer insulating film on the semiconductor substrate, and then forms contact holes that expose the semiconductor substrate by etching. Next, a first diffusion preventing film is formed on the exposed semiconductor substrate and the first interlayer insulating film, and then the first noncrystalline diffusion preventing film is formed by ion injection of impurities into the first diffusion preventing film.

[0011]

Next, a first copper film and a second diffusion preventing film are formed in order on the first noncrystalline diffusion preventing film. Next, a second noncrystalline

diffusion preventing film is formed by ion injection of impurities into the second diffusion preventing film. Next, a second interlayer insulating film is formed on the entire surface of the second noncrystalline diffusion preventing film, and then a via hole that exposes the first copper film is formed by etching the second interlayer insulating film and the second noncrystalline diffusion preventing film. A third diffusion preventing layer is formed on the entire surface of the substrate in which the via holes are formed, and then a third noncrystalline diffusion preventing film is formed by ion injection of impurities into the third diffusion preventing film. Next, a second copper film is formed such that the via holes can be embedded on the third noncrystalline diffusion preventing film.

[0012]

With the copper metal wire forming method for a semiconductor device of the present invention, a diffusion preventing film is formed and then impurities are ion injected into the diffusion preventing film, so the diffusion of copper through the grain boundary can be prevented by making the crystal structure to be noncrystalline.

[0013]

[Description of the Preferred Embodiments] An embodiment of the present invention is described below in detail by referring to the attached drawings. As illustrated in Fig. 1, a transistor is formed with a gate oxide film 5, a gate electrode 7, a spacer 9, a source/drain region 11, and a capping insulating film 13 on an active region of a semiconductor substrate 1 limited by a field oxide film 3. At this time, the gate electrode 7 and the capping insulating film 13 are also formed on the field insulating film 3. Next, a first interlayer insulating film 15 is formed on the entire surface of the resulting product where the transistor was formed.

[0014]

As illustrated in Fig. 2, a first interlayer insulating film pattern 15a with a contact hole 16 that exposes the surface of the source/drain region 11 of the semiconductor substrate is formed by etching the first interlayer insulating film 15 using a photoetching process. At this time, the capping insulating film 13 formed on the field insulating film 3 is also etched, and a contact hole 16 is formed to expose the gate electrode 7.

[0015]

As illustrated in Fig. 3, the first diffusion preventing film is formed on the entire surface of the semiconductor substrate 1 that is exposed by the contact hole 16, using any one of Mo, W, Ti, Ta, WN TiW, TiN, or combinations thereof, at a thickness of 100 to 1000 Å. The diffusion preventing film is formed by a chemical vapor deposition method with excellent step coverage and minimal occurrence of defects, such as low-pressure chemical vapor deposition (LPCVD) or plasma enhanced chemical vapor deposition (PECVD) at a temperature of 300 to 600°C. Next, the first noncrystalline diffusion preventing film 17 is formed by ion injection of impurities such as boron (B), nitrogen (N) or silicon into the first diffusion preventing film at an acceleration voltage of 20 to 140 KeV and a dose of 10^{16} - 10^{17} ions/cm². This time, the ion injected impurity is positioned at the interface between the semiconductor substrate 1 and the first noncrystalline diffusion preventing film 17. Therefore, the first noncrystalline diffusion preventing film 17 has a crystalline structure that is noncrystalline because of ion injection, and therefore diffusion of copper through the grain boundary and into the source/drain region 11 and the gate electrode 7 is prevented up to 800°C. Furthermore, the first noncrystalline diffusion preventing film 17 prevents reaction between the first interlayer insulating film pattern 15a

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