

08/571,131	<b>SEMICONDUCTOR DEVICE WITH A FIELD-EFFECT TRANSISTOR HAVING A LOWER RESISTANCE IMPURITY DIFFUSION LAYER, AND METHOD OF MANUFACTURING THE SAME</b>	RPXC-29189-PANASONIC CMOS	06-28-2017::16:16:51
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### Patent Assignment Abstract of Title

#### Total Assignments: 3

<b>Application #:</b> 08571131	<b>Filing Dt:</b> 12/12/1995	<b>Patent #:</b> 5733812	<b>Issue Dt:</b> 03/31/1998
<b>PCT #:</b> NONE	<b>Intl Reg #:</b>	<b>Publication #:</b> NONE	<b>Pub Dt:</b>
<b>Inventors:</b> TETSUYA UEDA, TAKASHI UEHARA, KOUSAKU YANO, SATOSHI UEDA			
<b>Title:</b> SEMICONDUCTOR DEVICE WITH A FIELD-EFFECT TRANSISTOR HAVING A LOWER RESISTANCE IMPURITY DIFFUSION LAYER, AND METHOD OF MANUFACTURING THE SAME			

#### Assignment: 1

<b>Reel/Frame:</b> 007810 / 0728	<b>Received:</b> 03/05/1996	<b>Recorded:</b> 12/12/1995	<b>Mailed:</b> 12/02/1996	<b>Pages:</b> 4
<b>Conveyance:</b> ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).				
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#### Assignment: 2

<b>Reel/Frame:</b> 031506 / 0602	<b>Received:</b> 10/28/2013	<b>Recorded:</b> 10/28/2013	<b>Mailed:</b> 10/31/2013	<b>Pages:</b> 5
<b>Conveyance:</b> CHANGE OF NAME (SEE DOCUMENT FOR DETAILS).				
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#### Assignment: 3

<b>Reel/Frame:</b> 031939 / 0247	<b>Received:</b> 01/08/2014	<b>Recorded:</b> 01/08/2014	<b>Mailed:</b> 01/13/2014	<b>Pages:</b> 12
<b>Conveyance:</b> ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).				
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