

08/571,131	SEMICONDUCTOR DEVICE WITH A FIELD-EFFECT TRANSISTOR HAVING A LOWER RESISTANCE IMPURITY DIFFUSION LAYER, AND METHOD OF MANUFACTURING THE SAME	RPXC-29189-PANASONIC CMOS	06-28-2017::16:16:51
------------	--	---------------------------	----------------------

### Patent Assignment Abstract of Title

#### Total Assignments: 3

**Application #:** 08571131      **Filing Dt:** 12/12/1995      **Patent #:** 5733812      **Issue Dt:** 03/31/1998  
**PCT #:** NONE      **Intl Reg #:**      **Publication #:** NONE      **Pub Dt:**  
**Inventors:** TETSUYA UEDA, TAKASHI UEHARA, KOUSAKU YANO, SATOSHI UEDA  
**Title:** SEMICONDUCTOR DEVICE WITH A FIELD-EFFECT TRANSISTOR HAVING A LOWER RESISTANCE IMPURITY DIFFUSION LAYER, AND METHOD OF MANUFACTURING THE SAME

#### Assignment: 1

**Reel/Frame:** 007810 / 0728      **Received:** 03/05/1996      **Recorded:** 12/12/1995      **Mailed:** 12/02/1996      **Pages:** 4  
**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).  
**Assignors:** UEDA, TETSUYA      **Exec Dt:** 12/06/1995  
UEHARA, TAKASHI      **Exec Dt:** 12/06/1995  
YANO, KOUSAKU      **Exec Dt:** 12/06/1995  
UEDA, SATOSHI      **Exec Dt:** 12/06/1995  
**Assignee:** MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.  
1006 OAZA KADOMA, KADOMA-SHI  
OSAKA 571, JAPAN  
**Correspondent:** WILLIAN BRINKS HOFER GILSON & LIONE  
KENNETH L. CAGE, ESQ.  
2000 K STREET, N.W., SUITE 200  
WASHINGTON, D.C. 20006-1809

#### Assignment: 2

**Reel/Frame:** 031506 / 0602      **Received:** 10/28/2013      **Recorded:** 10/28/2013      **Mailed:** 10/31/2013      **Pages:** 5  
**Conveyance:** CHANGE OF NAME (SEE DOCUMENT FOR DETAILS).  
**Assignor:** MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.      **Exec Dt:** 10/01/2008  
**Assignee:** PANASONIC CORPORATION  
1006 OAZA KADOMA, KADOMA-SHI  
OSAKA, JAPAN 571-8501  
**Correspondent:** PANASONIC PATENT CENTER  
1130 CONNECTICUT AVE., N.W., SUITE 1100  
WASHINGTON, DC 20036

#### Assignment: 3

**Reel/Frame:** 031939 / 0247      **Received:** 01/08/2014      **Recorded:** 01/08/2014      **Mailed:** 01/13/2014      **Pages:** 12  
**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).  
**Assignor:** PANASONIC CORPORATION      **Exec Dt:** 10/30/2013  
**Assignee:** RPX CORPORATION  
ONE MARKET PLAZA, STEUART TOWER, SUITE 800  
SAN FRANCISCO, CALIFORNIA 94105  
**Correspondent:** GREGORY M. HOWISON  
P.O. BOX 741715  
DALLAS, TX 75374-1715

Search Results as of: 06/28/2017 16:16:43 PM

#### Disclaimer:

**Assignment information on the assignment database reflects assignment documents that have been actually recorded. If the assignment for a patent was not recorded, the name of the assignee on the patent application publication or patent may be different.**

**If you have any comments or questions concerning the data displayed, contact OPR / Assignments at 571-272-3350**

[Close Window](#)

Exhibit 2068

TSMC - ID: 11