



US008966144B2

(12) **United States Patent**  
**Tasler**

(10) **Patent No.:** **US 8,966,144 B2**  
(45) **Date of Patent:** **\*Feb. 24, 2015**

(54) **ANALOG DATA GENERATING AND PROCESSING DEVICE HAVING A MULTI-USE AUTOMATIC PROCESSOR**

(58) **Field of Classification Search**  
USPC ..... 710/1, 15, 63  
See application file for complete search history.

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(\* ) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 1333 days.

This patent is subject to a terminal disclaimer.

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(21) Appl. No.: **11/467,073**

(22) Filed: **Aug. 24, 2006**

(65) **Prior Publication Data**

US 2007/0005823 A1 Jan. 4, 2007

**Related U.S. Application Data**

(60) Continuation of application No. 11/078,778, filed on Mar. 11, 2005, now abandoned, and a continuation of application No. 10/219,105, filed on Aug. 15, 2002, now Pat. No. 6,895,449, which is a division of application No. 09/331,002, filed on Jun. 14, 1999, now Pat. No. 6,470,399.

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(30) **Foreign Application Priority Data**

Mar. 4, 1997 (DE) ..... 19708755  
Mar. 3, 1998 (WO) ..... PCT/EP98/01187

(57) **ABSTRACT**

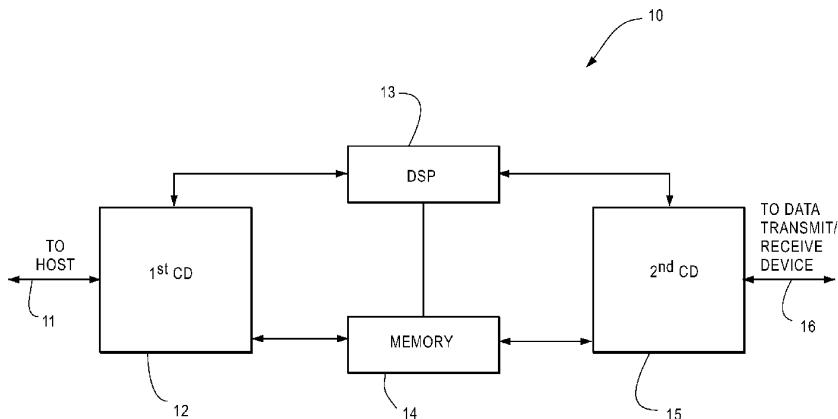
An interface device (10) provides fast data communication between a host device with input/output interfaces and a data transmit/receive device, wherein the interface device (10) comprises a processor means (13), a memory means (14), a first connecting device (12) for interfacing the host device with the interface device, and a second connecting device (15) for interfacing the interface device (10) with the data transmit/receive device. The interface device (10) is configured by the processor means (13) and the memory means (14) in such a way that, when receiving an inquiry from the host device via the first connecting device (12) as to the type of a device attached to the host device, regardless of the type of the data transmit/receive device, the interface device sends a signal to the host device via the first connecting device (12) which signals to the host device that it is communicating with an input/output device.

(51) **Int. Cl.**  
**G06F 13/12** (2006.01)  
**G06F 13/38** (2006.01)  
**G06F 3/06** (2006.01)

(52) **U.S. Cl.**  
CPC ..... **G06F 13/385** (2013.01); **G06F 3/0605** (2013.01); **G06F 3/0607** (2013.01);

(Continued)

**87 Claims, 2 Drawing Sheets**



(52) **U.S. Cl.**  
 CPC ..... *G06F3/0613* (2013.01); *G06F 3/0661*  
 (2013.01); *G06F 3/0664* (2013.01); *G06F*  
*3/0676* (2013.01); *G06F 13/387* (2013.01)  
 USPC ..... **710/63**

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