

# Petitioners' Demonstrative Exhibits

**FUJIFILM Corporation, et al. v. Papst Licensing GmbH & Co, K**  
**IPR2016-01199 & IPR2016-01200**  
**Petitions Based On U.S. Pat. No. 5,758,081 (“Aytac”)**

September 14, 2017

**IPR2016-01199**

**U.S. Pat. No. 8,966,144**

# U.S. Pat. No. 8,966,144 (“the ’144 Patent”)

- Title: “Analog Data Generating and Processing Device having a Multi-Use Automatic Processor”
- Named Inventor: Michael Tasler
- Owner: Papst Licensing GmbH & Co. KG
- Earliest asserted priority date: March 4, 1997

US08966144B2

(12) **United States Patent**  
Tasler

(10) **Patent No.:** US 8,966,144 B2  
(45) **Date of Patent:** Feb. 10, 2013

(54) **ANALOG DATA GENERATING AND PROCESSING DEVICE HAVING A MULTI-USE AUTOMATIC PROCESSOR**

(75) **Inventor:** Michael Tasler, Würzburg (DE)

(73) **Assignee:** Papst Licensing GmbH & Co. KG, St. Georgen (DE)

(\*) **Notice:** Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 1333 days. This patent is subject to a terminal disclaimer.

(21) **Appl. No.:** 11/447,073

(22) **Filed:** Aug. 24, 2006

(65) **Prior Publication Data**  
US 2007/0005823 A1 Jan. 4, 2007

**Related U.S. Application Data**  
(60) Continuation of application No. 11/079,778, filed on Mar. 11, 2005, now abandoned, and a continuation of application No. 10/219,105, filed on Aug. 15, 2002, now Pat. No. 6,805,640, which is a division of application No. 09/331,002, filed on Jun. 14, 1999, now Pat. No. 6,470,399.

**Foreign Application Priority Data**  
(30) Mar. 4, 1997 (DE) 19708755  
Mar. 3, 1998 (WO) PCT/EP98/01187

**Int. Cl.**  
(51) *G06F 15/12* (2006.01)  
*G06F 15/18* (2006.01)  
*G06F 3/06* (2006.01)

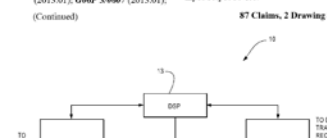
**U.S. Cl.**  
(52) U.S. Cl. *G06F 15/18* (2013.01); *G06F 15/06* (2013.01); *G06F 3/06* (2013.01)

**Field of Classification Search**  
(58) USPC  
See application file for complete search history.

**References Cited**  
(56) U.S. PATENT DOCUMENTS  
3,714,635 A 1/1973 Hancher  
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**ABSTRACT**  
(57) An interface device (10) provides for a first connecting device (12) for interfacing the interface device (10) with a host device (14), wherein the interface device (10) comprises a processor means (13), a memory means (15), and a multi-use automatic processor means (17) that, when receiving an inquiry from the first connecting device (12) in order to interface the interface device (10) with the host device (14), the interface device (10) transmits/receives data via the first connecting device (12) to the host device (14) via the multi-use automatic processor means (17).

87 Claims, 2 Drawing



## IPR2016-01199 Overview

- Challenged Claims: 1-8, 10, 14-20, 22, 28, 29, 38, 52, 56, 57, 59-65, 67, 71-74, 80, 84, 86, and 87
- Claims 1, 84, and 86 are independent
- Ground of Unpatentability: The Challenged Claims are unpatentable under 35 U.S.C. § 103(a) as obvious over U.S. Pat. No. 5,758,081 (“Aytac”), in combination with the SCSI Specification and the Admitted Prior Art.

## IPR2016-01199 Outline

- Undisputed Issues
- Disputed Issues
  - “Regardless of the identity of a manufacturer of the computer”
  - “An automatic file transfer process in which ... at least one file of digitized audio data to be transferred to the computer without requiring any user-loaded file transfer enabling software to be loaded or installed in the computer at any time”
  - Dependent claim 65

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