APPENDIX A

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THOMAS A. GAFFORD Gafford Technology

December 23, 2016 SUMMARY OF QUALIFICATIONS

• Extensive knowledge of analog and digital electronic circuitry, digital computer technology, computer peripherals and computer system design, control systems, operating systems, and transaction processing software.

• Skilled articulation of technical material for both non-technical and technical audiences, with special attention to claim construction issues.

• In-depth analysis of electronic and computer apparatus and functionality.

EDUCATION

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University of Washington

Earned Bachelor of Science in Electrical Engineering, 1972 Key areas of concentration included digital and analog circuit analysis and electromagnetics.

Stanford University

Enrolled in Master of Science in Electrical Engineering, 1972-73. Coursework included logic, circuit and computer design, computer architecture, LISP and ALGOL programming, software algorithm design, and system programming.

PROFESSIONAL EXPERIENCE

1986- Gafford Technology

Now Founder and Owner

Firm undertakes R&D projects, provides computer system-related services, and offers analysis and presentation services that clearly and concisely explain computer and electronic technology to assist clients in litigation efforts.

Specific services include consulting in computer system design, software selection, and network configuration; providing expert factual analysis, claim interpretation assistance, prior art investigation and testimony in patent and hardware / software systems litigation; conducting R&D projects in peripheral switch design and application of hardware design language tools to peripheral interconnection design. Firm has manufactured and sold peripheral switching equipment.

1983- Softix, Incorporated

1986 *Co-Founder and Head of Engineering*

Firm designed and produced reliable and easily maintained systems to control and sell entertainment tickets by ticket agencies and large arena complexes in the United States, Canada,

Seattle, Washington

Palo Alto, California

Las Vegas, Nevada

Campbell, California

Australia, and Hong Kong. The firm was sold in 1987.

Responsibilities included co-managing software development efforts; developing architecture, design, sales, contracting, production, and field support of large-scale software and hardware systems; analyzing, debugging, and writing software application and driver programs for feature enhancements and system integration.

Also responsible for selection, evaluation, integration, installation, customer staff training, and repair support of all hardware components of dual minicomputer systems; for research into graphic printing systems suitable for ticket sales; for development of peripheral switch equipment for evolving system requirements; for the manufacture and sale of peripheral switching equipment.

1976- G Systems

Santa Clara, California

1983 Founder and Owner

Firm managed hardware and software design and development of computer transaction processing systems for a variety of applications and customers, as well as consulting in other hardware design projects.

Projects included design of hardware and software interfaces for disk controller; co-design of mainframe computer and design of mainframe computer elements; design of replacement printer which substituted all traditionally hard-wired functions with software functionality in communication and mechanism controls; design of peripheral switches incorporated into system product; writing of communications software and device drivers. Assisted legal team in patent litigation as technical expert in disk control systems.

1973- Stanford University Artificial Intelligence Laboratory

Palo Alto, California

Seattle, Washington

Tacoma, Washington

1976 Engineer

Position was responsible for computer engineering projects including support of robotics research.

Duties included design, construction, and debugging motor controls and sensor electronics for a robotic arm and its computer interfaces; maintenance and enhancement of large assembly-language software system used for logic design and PC board layout; maintenance of hardware for mainframe timesharing system for lab staff, including ARPANET connection and Xerographic printing equipment.

1971- University of Washington

1972 **Psychology Department** Engineer

Designed and built computer equipment and systems in support of psychological research projects.

1967- United States Air Force

1970 McChord Air Force Base

Sergeant and Instructor

Maintenance technician for air defense computer system; electronics instructor in on-the-job training programs; specialist in analysis and repair of problems in areas beyond standard training and documentation.

PATENTS

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United States Patent #5,621,899August 15, 1997Method for Operating a Repeater for Distributed Arbitration Digital Data Buses

United States Patent #5,684,966 Ne Switch for Distributed Arbitration Digital Data Buses

November 4, 1997

United States Patent #5,758,109 May 26, 1998 Repeater/Switch for Distributed Arbitration Digital Data Buses

United States Patent #6,154,799 November 11, 2000 Repeater/Switch for Distributed Arbitration Digital Data Buses

ADDITIONAL PROFESSIONAL ACTIVITIES

Institute of Electrical and Electronic Engineers Life Member

CASE EXPERIENCE

Cases are grouped into four areas. All cases listed required extensive research and technical analysis. Where expert testimony at trial also occurred, it is noted. The party who is my client is underlined. Cases marked '*trial*' at the right margin included deposition unless specifically noted, cases marked 'arbitration testimony' do not.

CIRCUIT DESIGN

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Experience in this area involves low-level analysis of electronic circuits, usually without regard for any larger system in which the circuits might be used.

Date	Case	
01/94 to 07/94	Wang v. Mitsubishideposition, tridPatent litigation relating to memory modules.deposition, trid	ıl
11/95 to 03/96	Waffer v. Fritz Forwardingarbitration hearingLitigation involving value of computer monitors based on service history and construction quality.arbitration hearing	g
11/96 to 12/97	<u>United Technology Automotive</u> v. National Semiconductor Patent litigation relating to power control integrated circuits.	
05/97 to 09/98	Oneac v. RaychemdepositionPatent litigation relating to circuits for telephone line surge suppression.deposition	n
06/97 to 06/97	Harris Semiconductor v. Hyundai Patent litigation involving static ram IC circuitry.	
12/98 to 03/00	Xeroxv. Hewlett-PackarddepositionPatent litigation relating to electrical circuitry of thermal inkjet printheads.deposition	n
02/01 to 09/02	ICSv. Realtek, ICSv. Cypress, Cypress v. ICSdeposition, tutorial, MarkmaPatent litigation involving clock generator IC circuits.	n
05/01 to 07/01	<u>Altera</u> v. Xilinx, Xilinx v. <u>Altera</u> Patent litigation involving FPGA circuitry.	
05/11 to 04/12	<u>Cascades</u> v. Hynix Patent litigation involving DRAM circuitry.	
01/14 to 04/14	Zenith et al. v. <u>Viewsonic</u> and <u>Craig</u> et al.	3

Patent infringement litigation regarding ATSC compliant TV receivers

03/14 to 07/14 **Cresta Technology Corporation v. <u>Maxlinear Inc.</u>** Patent litigation invoving RF tuner design

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