Robert Louis Stevenson
Curriculum Vitae

Contact   275 Fitzpatrick Hall
Department of Electrical Engineering
University of Notre Dame
Notre Dame, Indiana 46556

Tel.: +1 574-631-8308
Fax: +1 574-631-4393
EMail: rls@nd.edu

WWW: http://engineering.nd.edu/profiles/rstevenson

Professional Experience

10/2013–Present   Associate Chair, Director of Undergraduate Studies
Department of Electrical Engineering
University of Notre Dame, Notre Dame, Indiana

08/2002–Present   Professor
Department of Electrical Engineering
University of Notre Dame, Notre Dame, Indiana

01/2003–Present   Professor
Department of Computer Science and Engineering
University of Notre Dame, Notre Dame, Indiana

08/1996–08/2002   Associate Professor
Department of Electrical Engineering
University of Notre Dame, Notre Dame, Indiana

08/1996–06/1997   Visiting Associate Professor
Department of Electrical and Computer Engineering
University of Delaware, Newark, Delaware

06/1994–08/1994   Research Associate
Intel Corporation, Hillsboro, Oregon

05/1993–08/1993   Research Associate
Air Force Office of Scientific Research
Rome Laboratories, Griffiss AFB, Rome, New York

1

08/1990–08/1996	Assistant Professor Department of Electrical Engineering University of Notre Dame, Notre Dame, Indiana
08/1989–08/1990	Graduate Research Assistant School of Electrical Engineering Purdue University, West Lafayette, Indiana
08/1986–08/1990	Graduate Teaching Assistant School of Electrical Engineering Purdue University, West Lafayette, Indiana
06/1986–08/1986	Intern Engineering Physics Laboratory, E. I. duPont de Nemours & Company, Wilmington, Delaware
06/1985–08/1985	Intern Corporate Technology Center Sperry Corporation, Reston, Virginia

Education

08/1986–08/1990	*Ph.D., Electrical Engineering*, August 1990
	Purdue University, West Lafayette, Indiana
	    Thesis: *Invariant Reconstruction of Curves and Surfaces with Discontinuities with Applications in Computer Vision*
	    Advisor: Professor Edward J. Delp
	    GPA: 6.0/6.0

09/1982–06/1986	*B.S.E.E., Electrical Engineering*, June 1986
	University of Delaware, Newark, Delaware
	    Thesis: *On the Theoretical Properties of Morphological Filters*
	    Advisor: Professor Gonzalo R. Arce
	    GPA: 4.0/4.0

Honors and Awards

1983	University of Delaware, Tau Beta Pi Prize
1984	University of Delaware, Engineering Scholar
1985–1986	University of Delaware, Liston A. Houston Scholarship
1986	IEEE Delaware Bay Section Engineering Award

1986	Valedictorian, University of Delaware
1986	Purdue University Graduate Instructor Fellowship
1986	Phi Kappa Phi Graduate Fellowship
1986–1990	DuPont Graduate Fellowship in Electrical Engineering
1986–1989	National Science Foundation Graduate Fellowhip
1993	Dept. of Electrical Engineering Outstanding Teacher Award
2012	ICIP 2012 Outstanding Reviewer
2013	IS&T 2013 Service Award

Honor Societies

1985–Present	Eta Kappa Nu, Electrical Engineering Honor Society
1985–Present	Tau Beta Pi, Engineering Honor Society
1985–Present	Phi Kappa Phi, Academic Honor Society

Professional Activities

Associate Editor
IEEE Trans. on Image Processing, 1999–2003
IEEE Trans. on Circuits and Systems for Video Tech., 1997–2002
Journal of Electronic Imaging, 1995–1998

Special Issue Editor
Multimedia Systems, *Journal of Electronic Imaging*, 04/1996
Still Image Compression, *Electronic Imaging Newsletter*, 01/1996

Best Paper Award Board Membership
IEEE Trans. on Circuits and Systems for Video Technology, 2002

Symposium Chairman
SPIE/IS&T Symposium on Electronic Imaging, 02/2004

Conference Chairman
SPIE/IS&T Conf. on Visual Information Proc. & Comm. V, 02/2014
SPIE/IS&T Conf. on Visual Information Proc. & Comm. IV, 02/2013
SPIE/IS&T Conf. on Visual Information Proc. & Comm. III, 01/2012
SPIE/IS&T Conf. on Visual Information Proc. & Comm. II, 01/2011
SPIE/IS&T Conf. on Visual Comm. and Image Processing, 01/2009
SPIE/IS&T Conf. on Computational Imaging, 02/2003
SPIE/IS&T Conf. on Image and Video Comm. and Proc., 02/2000

3

SPIE/IS&T Conf. on Visual Comm. and Image Processing, 02/1999
41st Midwest Symposium on Circuits and Systems, 08/1998
SPIE Conf. on Electronic Imaging and Signal Processing, 11/1996
SPIE/IS&T Conf. on Still Image Compression II, 02/1996
SPIE/IS&T Conf. on Image and Video Processing IV, 02/1996
SPIE/IS&T Conf. on Image and Video Processing III, 02/1995
SPIE/IS&T Conf. on Image and Video Processing II, 02/1994

Steering Committee Member
Visual Communications and Image Processing, 1998–2010
Midwest Symposium on Circuits and Systems, 1994–1999
Electronic Imaging, 2003–2005

Organizing Committee Member
Nonlinear Signal and Image Processing Workshop, 2001

Technical Program Committee
International Conference on Image Processing, 1998 – Present
SPIE Computational Imaging, 2003–Present
Visual Communications and Image Processing, 1997–Present
IEEE Symposium on Industrial Electronics & Applications, 2012
International Conference on Multimedia & Expo, 2009–2010
Image and Video Communications and Processing III, 2005
Computational Imaging, 2004
International Conference on Pattern Recognition, 2000
Int. Conf. on Acoustics, Speech, & Signal Proc., 2000, 2006–Present
International Symposium on Circuits and Systems, 1998, 2005
9th IEEE Image and Multi. Signal Processing Workshop, 1996
Midwest Symposium on Circuits and Systems, 1994

Member
Institute of Electrical and Electronics Engineers, IEEE
Society of Photographic Instrumentation Engineers, SPIE
The Society for Imaging Science and Technology, IS&T

Panel Member for the following funding agencies
National Science Foundation

Reviewer for the following funding agencies
National Science Foundation
U.S. Army Research Office
Israel Science Foundation
Hong Kong Research Grants Council
Kentucky EPSCoR Program

4

North Dakota EPSCoR Program
Louisiana Board of Regents

Reviewer for the following archival journals
   IEEE Transactions on Signal Processing
   IEEE Transactions on Image Processing
   IEEE Transactions on Circuits and Systems
   IEEE Transactions on Circuits and Systems for Video Technology
   IEEE Transactions on Instrumentation and Measurement
   IEEE Transactions on Medical Imaging
   IEEE Transactions on Pattern Analysis and Machine Intelligence
   IEEE Transactions on Systems, Man, and Cybernetics
   IEEE Transactions on Neural Networks
   Pattern Recognition Letters
   Signal Processing Letters
   Computer Vision, Graphics, and Image Processing
   Journal of the Optical Society of America
   Journal of Mathematical Imaging and Vision
   Applied Optics
   IET Image Processing

Reviewer for the following textbook companies
   Van Nostrand Reinhold, Electrical Engineering Division
   McGraw-Hill, College/Schaum Division
   MacMillan Publishing Company
   Prentice Hall

Publications

Journals

R. L. Stevenson and G. R. Arce, "Binary Display of Hexagonally Sampled Continuous–Tone Images," *Journal of the Optical Society of America A*, Vol. 2, pp. 1009–1013, July 1985.

G. R. Arce and **R. L. Stevenson**, "On the Synthesis of Median Filter Systems," *IEEE Transactions on Circuits and Systems*, Vol. CAS-34, No. 4, pp. 420–429, April 1987.

R. L. Stevenson and G. R. Arce, "Morphological Filters: Statistics and Further Syntactic Properties," *IEEE Transactions on Circuits and Systems*, Vol. CAS-34, No. 11, pp. 1292–1305, November 1987.

DOCKET ALARM

Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts

Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research

With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips

Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.