UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

Apple, Inc. PETITIONERS

V.

Parthenon Unified Memory Architecture LLC PATENT OWNER

Case IPR No: 2016-01114

Patent No. 7,777,753

TITLE: ELECTRONIC SYSTEM AND METHOD FOR SELECTIVELY ALLOWING ACCESS TO A SHARED MEMORY

PATENT OWNER'S PRELIMINARY RESPONSE UNDER 37 C.F.R. §42.107



TABLE OF CONTENTS

I. INTRODUCTION	.1
II. THE `753 PATENT	.2
III. CLAIM CONSTRUCTION	.3
IV. PETITIONER DOES NOT ESTABLISH A REASONABLE LIKELIHOOI THAT ANY CHALLENGED CLAIM IS INVALID	
1. Ground 1: Bowes, Datasheet, Artieri, and Arimilli (claims 1 and 2)	.6
a. The MPEG Standard Was Considered During the Prosecution of the `753 Patent	
b. Artieri Mirrors the Implementation Identified as Prior Art by the `753 Patent	.7
c. The combination of Bowes, Datasheet, Artieri and Arimilli does not disclose every element of claims 1 and 2 of the `753 Patent	10
i. The proposed combination does not disclose "providing access to the main memory for a request for access to the main memory when the arbiter circuit is in an idle state" [claim 1]	is
ii. The proposed combination does not disclose wherein the video circuit/decoder is further configured to receive data from the [main] memory corresponding to at least one previously decoded [video] image [claim 2]1	15
iii. The proposed combination does not disclose an arbiter that controls access to the main memory [claim 1]	30
d. No motivation to combine Bowes and Artieri	36
2. Ground 2: Bowes, Datasheet, Artieri, Arimilli, and Shanley (claim 4)5	50
3. Ground 3: Bowes, Datasheet, Artieri, and Christiansen (claim 7)	50
4. Ground 4: Bowes, Datasheet, Artieri, Christiansen and Arimilli (claims 8 and 10) 52	ıd
5. Ground 5: Bowes, Datasheet, Artieri, Christiansen, and Shanley (claim 9)5	53



IPR2016-01114 Patent Owner Preliminary Response U.S. Patent No. 7,777,753

6.	Ground 6: Bowes, Datasheet, Artieri, Christiansen, and Gove (claim 1	2)54
V.	CONCLUSION	54



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Cases C.R Bard, Inc. v. M3 Sys., Inc., In re Fine, In re Rambus Inc., 694 F.3d 42 (Fed. Cir. 2012)4 In re Wilson, 424 F.2d 1382, (CCPA 1970)......10 Karlin Tech., Inc. v. Surgical Dynamics, Inc., Kinetic Tech., Inc. v. Skyworks Solutions, Inc., IPR2014-00530, 2014 WL 4925282, (Patent Tr. & App. Bd. Sep. 29, 2014)37 Parthenon Unified Memory Architecture, LLC v. Apple, Inc., Phillips v. AWH Corp., Teleflex, Inc. v. Ficos N. America Corp.,



IPR2016-01114 Patent Owner Preliminary Response U.S. Patent No. 7,777,753

Toyota Motor Corp. v. Cellport Sys., Inc., Case IPR2015-00633, (PTAB Aug. 14, 2015) (Paper 11)	4
Vivid Techs., Inc. v. Am. Sci. & Eng'g, Inc., 200 F.3d 795, 803 (Fed. Cir. 1999)	4
Rules	
35 U.S.C. § 314(a)	1
37 C.F.R. § 42.100(b)	3
37 C.F.R. § 42.5(b)	3



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