

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

HTC CORPORATION, HTC AMERICA, INC., and LG ELECTRONICS, INC.,
Petitioner,

v.

PARTHENON UNIFIED MEMORY ARCHITECTURE LLC,
Patent Owner.

Case No. IPR2016-00847
U.S. Patent No. 5,812,789

PETITIONERS' UPDATED EXHIBIT LIST

Exhibit	Descriptions
1001	U.S. Patent No. 5,812,789 (“the ’789 patent”)
1002	File History for U.S. Patent No. 5,812,789
1003	RESERVED
1004	ISO/IEC 11172-2:1993: Information technology—Coding of moving pictures and associated audio for digital storage media at up to about 1,5 Mbit/s—Part 2: Video,” (1st ed. August 1, 1993) (“ <i>MPEG Standard</i> ”)
1005	S. Rathnam <i>et al.</i> , “An Architectural Overview of the Programmable Multimedia Processor, TM-1,” IEEE Proceedings of COMPCON ’96, pp. 319-326 (1996) (“ <i>Rathnam</i> ”)
1006	R.J. Gove, “The MVP: A Highly-Integrated Video Compression Chip,” Proceedings of the IEEE Data Compression Conference (DCC ’94), pp. 215-224 (March 29-31, 1994)
1007	U.S. Patent No. 5,774,676 (“ <i>Stearns</i> ”)
1008	RESERVED
1009	RESERVED
1010	WorldCat Entry for <i>Rathnam</i>
1011	Patent Owner Claim Construction Brief in Case No. 2:14-cv-690, April 7, 2015
1012	Patent Owner Claim Construction Brief in Case No. 2:14-cv-902, June 18, 2015
1013	RESERVED
1014	Brad Hansen, <i>The Dictionary of Multimedia</i> , 1997
1015	U.S. Patent No. 8,681,164
1016	Excerpt of File History for U.S. Patent No. 8,681,164
1017	RESERVED
1018	RESERVED
1019	Shanley, et al., “PCI System Architecture,” Addison-Wesley Publishing Company, 1995 (3rd ed.) (“ <i>Shanley</i> ”)
1020	Stone, H., “Microcomputer Interfacing,” Addison-Wesley

Exhibit	Descriptions
	Publishing Company, 1982
1021	RESERVED
1022	RESERVED
1023	U.S. Patent No. 5,797,028 (" <i>Gulick 028</i> ")
1024	"Accelerated Graphics Port Interface Specification," Intel Corporation, July 31, 1996 (Revision 1.0) (" <i>AGP</i> ")
1025	VESA Unified Memory Architecture Hardware Specifications Proposal, Version 1.0p (" <i>VUMA</i> ")
1026	RESERVED
1027	RESERVED
1028	RESERVED
1029	Curriculum Vitae of Dr. Harold Stone
1030	Expert Declaration of Dr. Harold Stone (" <i>Stone Decl.</i> ")
1031	RESERVED
1032	U.S. Patent No. 5,682,484 (" <i>Lambrecht</i> ")
1033	RESERVED
1034	Slavenburg, G., "The TriMedia VLIW-Based PCI Multimedia Processor," Microprocessor Forum 1995, Oct. 10-11, 1995 (" <i>Slavenburg</i> ")
1035	G. Moore, "Cramming more components onto integrated circuits," Electronics, Vol. 38, No. 8, Apr. 19, 1965 (" <i>Moore</i> ")
1036	U.S. Patent No. 5,579,052 (" <i>Artieri</i> ")
1037	RESERVED
1038	RESERVED
1039	RESERVED
1040	RESERVED
1041 [NEW]	Conference Call Transcript, dated May 4, 2016

Dated: May 19, 2016

Respectfully Submitted,

/Rajeev Gupta/

Rajeev Gupta

Registration No. 55,873

FINNEGAN, HENDERSON, FARABOW,

GARRETT & DUNNER, L.L.P.

901 New York Avenue, N.W.

Washington, D.C. 20001-4413

Counsel for Petitioner

LG Electronics, Inc.

/Joseph A. Micallef/

Joseph A. Micallef

Registration No. 39,772

SIDLEY AUSTIN LLP

1501 K Street NW

Washington, DC 20005

Counsel for Petitioners HTC

Corporation and HTC America, Inc.

CERTIFICATE OF SERVICE

Pursuant to 37 C.F.R. § 42.6(e), I hereby certify that on this 19th day of May, 2016, I caused to be served a true and correct copy of the foregoing and any accompanying exhibits by e-mail on the following counsel:

Massod Anjom, manjom@azalaw.com
Scott Clark, sclark@azalaw.com
AHMAD, ZAVITSANOS, ANAIPAKOS, ALAVI & MENSING P.C.

Dated: May 19, 2016

Respectfully Submitted,

/Rajeev Gupta/
Rajeev Gupta
Registration No. 55,873
FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.
901 New York Avenue, N.W.
Washington, D.C. 20001-4413
Counsel for Petitioner
LG Electronics, Inc.

/Joseph A. Micallef/
Joseph A. Micallef
Registration No. 39,772
SIDLEY AUSTIN LLP
1501 K Street NW
Washington, DC 20005
Counsel for Petitioners HTC
Corporation and HTC America, Inc.