UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

HTC Corporation,
HTC America, Inc.,
LG Electronics, Inc.,
Samsung Electronics Co., Ltd., and
Samsung Electronics America, Inc.
Petitioners

v.

Parthenon Unified Memory Architecture LLC
Patent Owner

INTER PARTES REVIEW OF U.S. PATENT NO. 7,321,368
Case IPR No.: *To Be Assigned*

DECLARATION OF HAROLD S. STONE, PH.D., REGARDING
U.S. PATENT NO. 7,321,368

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I, Harold S. Stone, Ph.D., declare as follows:

I. INTRODUCTION

A. Engagement

1. I have been retained by counsel for the Petitioners to submit this declaration in connection with Petitioners' Petition for *Inter Partes* Review of claims 1-3, 5, 7, 13-15, 17-21, and 23-25 of U.S. Patent No. 7,321,368 ("'368 Patent") (Ex. 1001).

B. Background and Qualifications

2. I was awarded a Ph.D. and Master's Degree in Electrical Engineering from the University of California-Berkeley in 1963 and 1961, respectively. I received a Bachelor of Science degree in Electrical Engineering from Princeton University in 1960.

3. After my graduation from Berkeley in 1963, I served as a Research Engineer at Boeing and SRI International. I then held faculty positions at Stanford University and at the University of Massachusetts, where I served as a professor of computer science and electrical engineering.

4. In 1984, I started working for IBM as a Manager of Advanced Architecture Studies. In 1990, I became a Research Staff Member at IBM. During my time at IBM, I managed and conducted research in the area of memory systems and optical interconnections. I worked at IBM until 1994, when I became a Fellow at the NEC Research Institute, the highest technical position in the company. At

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