



US008681164B2

(12) **United States Patent**
Owen et al.

(10) **Patent No.:** **US 8,681,164 B2**
(45) **Date of Patent:** ***Mar. 25, 2014**

(54) **ELECTRONIC SYSTEM AND METHOD FOR SELECTIVELY ALLOWING ACCESS TO A SHARED MEMORY**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.
This patent is subject to a terminal disclaimer.

(21) Appl. No.: **13/655,152**

(22) Filed: **Oct. 18, 2012**

(65) **Prior Publication Data**
US 2013/0042072 A1 Feb. 14, 2013

Related U.S. Application Data
(63) Continuation of application No. 13/239,200, filed on Sep. 21, 2011, now Pat. No. 8,314,808, which is a (Continued)

(51) **Int. Cl.**
G06F 13/18 (2006.01)
G06F 15/167 (2006.01)

(52) **U.S. Cl.**
USPC **345/535; 345/541**

(58) **Field of Classification Search**
USPC 345/541, 535, 542, 547, 555
See application file for complete search history.

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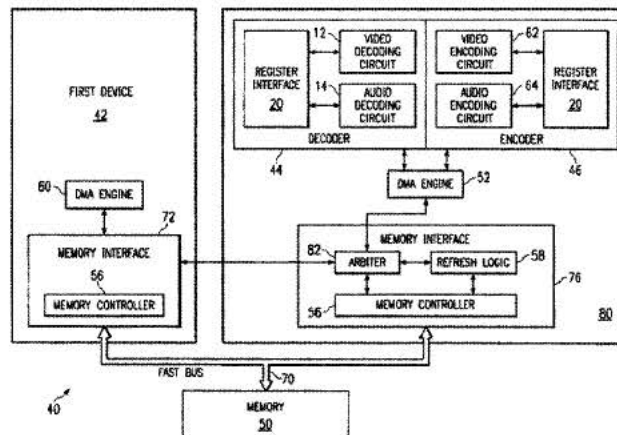
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(57) **ABSTRACT**

An electronic system, an integrated circuit and a method for display are disclosed. The electronic system contains a first device, a memory and a video/audio compression/decompression device such as a decoder/encoder. The electronic system is configured to allow the first device and the video/audio compression/decompression device to share the memory. The electronic system may be included in a computer in which case the memory is a main memory. Memory access is accomplished by one or more memory interfaces, direct coupling of the memory to a bus, or direct coupling of the first device and decoder/encoder to a bus. An arbiter selectively provides access for the first device and/or the decoder/encoder to the memory based on priority. The arbiter may be monolithically integrated into a memory interface. The decoder may be a video decoder configured to comply with the MPEG-2 standard. The memory may store predicted images obtained from a preceding image.

17 Claims, 6 Drawing Sheets



Related U.S. Application Data

- (63) continuation of application No. 13/015,385, filed on Jan. 27, 2011, now Pat. No. 8,054,315, which is a continuation of application No. 12/857,455, filed on Aug. 16, 2010, now Pat. No. 7,898,548, which is a continuation of application No. 12/424,389, filed on Apr. 15, 2009, now Pat. No. 7,777,753, which is a continuation of application No. 11/956,165, filed on Dec. 13, 2007, now Pat. No. 7,542,045, which is a continuation of application No. 10/174,918, filed on Jun. 19, 2002, now Pat. No. 7,321,368, which is a continuation of application No. 09/539,729, filed on Mar. 30, 2000, now Pat. No. 6,427,194, which is a continuation of application No. 08/702,910, filed on Aug. 26, 1996, now Pat. No. 6,058,459.

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