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United States Patent [19]

Ichioka et al.

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[54]	ARRAY TESTER FOR DETERMINING
	CONTACT QUALITY AND LINE INTEGRITY
	IN A TFT/LCD

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[21] Appl. No.: 26,897

[56]

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324/765, 158.1; 250/310; 365/98, 100, 87; 359/55, 57, 59; 371/25.1, 15.1, 22.1; 364/481

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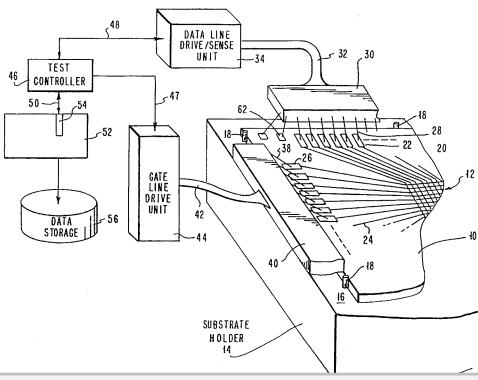
L. C. Jenkins et al., "Functional testing of TFT/LCD arrays" IBM Journal of Research and Development vol. 36, No. 1, Jan. 1992 pp. 59–68.

Primary Examiner—Vinh P. Nguyen Attorney, Agent, or Firm—David Aker

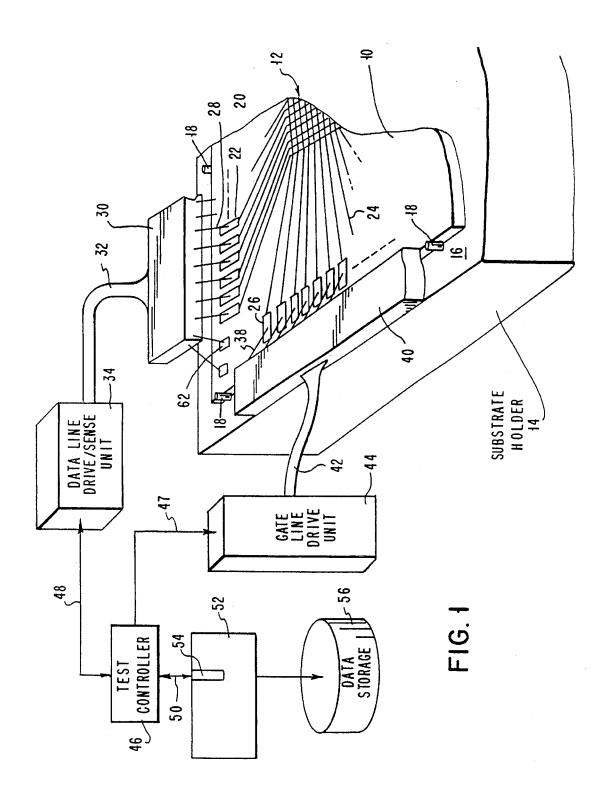
[57] ABSTRACT

An apparatus for testing for and classifying defects in a TFT/LCD array having gate lines and data lines. Devices are provided for activating cells of the array by applying gate pulses to the gate lines and pulses to the data lines. Devices are also provided for acquiring waveforms from data lines of the array. Additional devices sample the waveforms at selected points in time. A computer may be used to classify the waveforms to indicate whether defects are present and if present, the nature of the defects by comparing voltages of the waveform at the selected points in time.

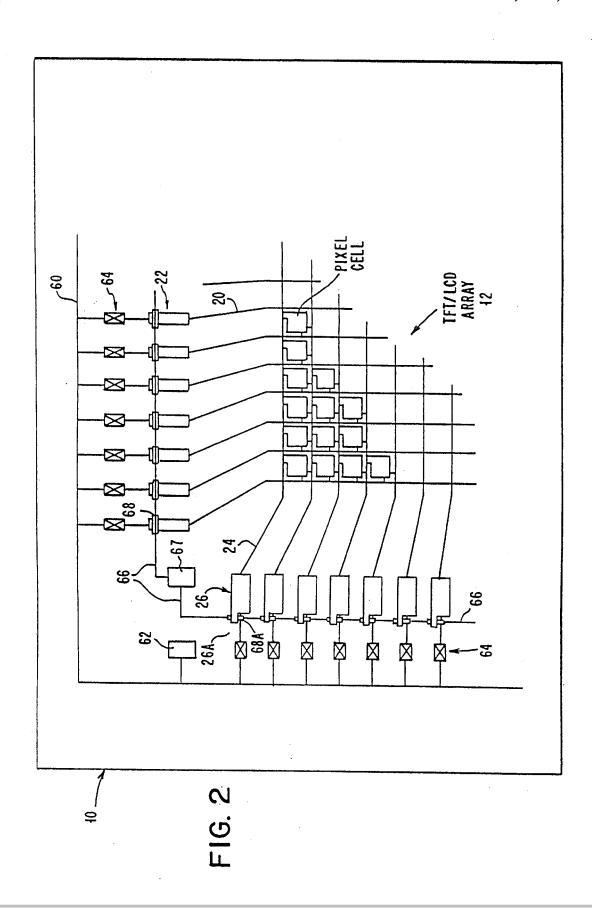
4 Claims, 10 Drawing Sheets













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FIG. 3

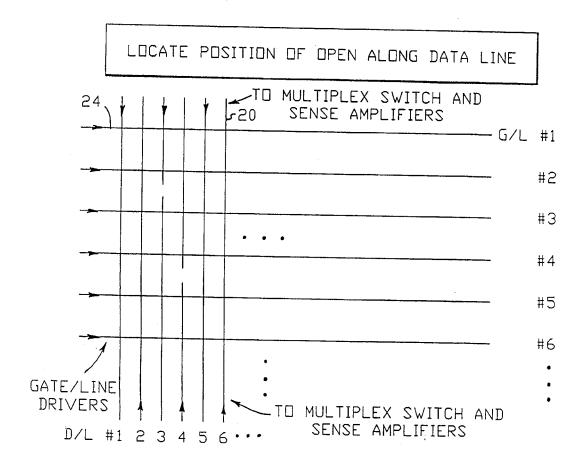
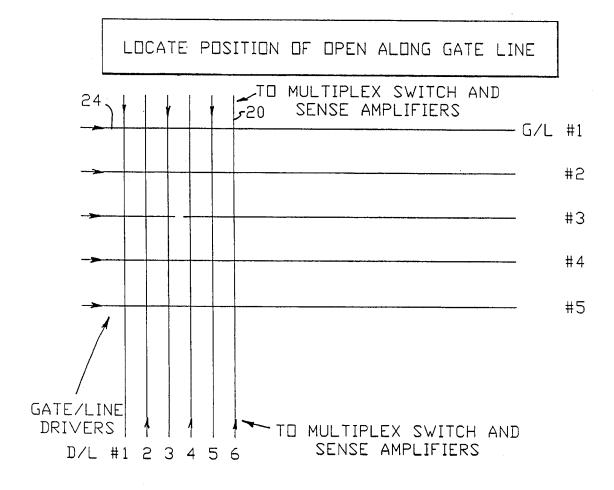


FIG. 4



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